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.REPT 0

IDENTIFICATION

PRODUCT CODE: AC-T367A-MC
PRODUCT NAME: CXUACA0 DEC/X11 DIAG
PRODUCT DATE: JANUARY 1983
MAINTAINER: DISTRIBUTED SYSTEMS DIAGNOSTIC ENGINEERING
AUTHOR: CHERYL CONROY

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1. ABSTRACT

THE DEUNA MODULE IS AN IOMODX THAT EXERCISES UP TO EIGHT DEUNA'S ON THE UNIBUS. THE DEUNA TRANSMITS PACKETS AT A STEADY RATE. RECEIVE PACKETS ARE STORED IN THE DEUNA UNTIL A LARGE (10 PACKETS) TRANSFER CAN BE INITIATED. THIS WILL RESULT IN A BURST MODE OF UNIBUS ACTIVITY FOLLOWED BY A RELATIVELY LOW LEVEL.

2. REQUIREMENTS

HARDWARE:

PDP-11 CPU
28K WORDS OF MEMORY
CONSOLE TERMINAL
DEUNA BOARDSET (M7792, M7793)
PLUS, ONE OF THE FOLLOWING:
LINK BOARD TO BULKHEAD CABLE CONNECTED AND BULKHEAD
TO TRANSCEIVER TAP CABLE CONNECTED (NORMAL ONLINE
CONFIGURATION)
OR
LINK BOARD TO BULKHEAD CABLE CONNECTED AND BULKHEAD
TO FIELD SERVICE EXTERNAL LOOPBACK CONNECTOR INSTALLED
(OFFLINE CONFIGURATION)

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3. PASS IDENTIFICATION

ONE PASS OF THE DEUNA MODULE CONSISTS OF 1000 ITERATIONS
OF:

PASS INITIALIZATION

HALT THE DEUNA
SET THE PORT CONTROL BLOCK
INITIALIZE THE TRANSMIT BUFFERS AND THE EXPECTED CRC'S
INITIALIZE RING BUFFER STRUCTURE
START THE DEUNA

TRANSMISSION

RECEPTION

ANALYSIS

COMPARE THE BYTE COUNT
COMPARE THE EXPECTED CRC
COMPARE THE DATA PACKET ON CRC ERROR

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4. EXECUTION TIME

ONE PASS RUNNING ALONE ON THE PDP-11/24 TAKES AN
AVERAGE OF 1 MINUTE FOR ONE DEUNA.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DEVADR: 170000, VECTOR: 700, BR1: 5, DEVCNT: 1

REQUIRED PARAMETERS:

NONE

6. DEVICE/OPTION SETUP
-----7. MODULE OPERATION

A. PROGRAM INITIALIZATION

HALT THE DEUNA

INITIALIZE THE TRANSMIT BUFFERS AND
EXPECTED CRC'S

START THE DEUNA

D. RECEPTION

COMPARE THE BYTE COUNT

COMPARE THE DATA PACKET ON CRC ERROR

```
SR1 BIT 2 SET(1):
    RUN IN INTERNAL LOOPBACK MODE.
```

SRI BIT 2 CLEAR(0):
RUN IN EXTERNAL LOOPBACK MODE. THIS IS THE DEFAULT MODE.

NON-STANDARD PRINTOUTS

A. ALL PRINTOUTS HAVE THE STANDARD FORMAT DESCRIBED IN THE DEC/X11 DOCUMENT.

B. ERROR MESSAGES DUMP THE CONTENTS OF THE DEUNA DESCRIPTOR RINGS IN THE FOLLOWING ORDER.

```

CSRA    = PCSRO ADDRESS      (1 WORD)
ACSR    = PCSRO CONTENTS    (1 WORD)
ASTAT   = CONTENTS OF PCSRI  (1 WORD)
ERRIYP  = 0      (UNDEFINED) (1 WORD)
TRANSMIT DESCRIPTOR RING     (5 WORDS)
RECEIVE DESCRIPTOR RING     (7 WORDS)

```

PORT CONTROL AND STATUS REGISTER 0 - PCSR0 -

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```

1 1 1 1 1 1 1 0 0 0 0 0 0 0 0 0
5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0
-----
S P R T D R F I I I S
E C X X N C 0 A N N S 0 PORT_COMMAND
I E I I I B T T T E
1 1 1 1 1 1 1 1 R E 1

```

WORD	BITS	FIELD	DESCRIPTION
PCSR0	<15>	SERI	STATUS ERROR INTERRUPT
PCSR0	<14>	PCKE	PORT COMMAND ERROR INTERRUPT
PCSR0	<13>	RXI	RECEIVE RING INTERRUPT
PCSR0	<12>	TXI	TRANSMIT RING INTERRUPT
PCSR0	<11>	DN1	DONE INTERRUPT
PCSR0	<10>	RCBI	RECEIVE BUFFER UNAVAILABLE INTERRUPT
PCSK0	<09>	ZERO	
PCSR0	<08>	FATI	FATAL ERROR INTERRUPT
PCSK0	<07>	INTR	INTERRUPT SUMMARY
PCSR0	<06>	INTE	INTERRUPT ENABLE
PCSR0	<05>	RSET	UNA RESET
PCSR0	<04>	ZERO	
PCSR0	<03:00>	PORT_COMMAND	

PORT CONTROL AND STATUS REGISTER 1 - PCSR1 -

1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0	9	8

X	I	I	SELF_TEST										P	I	R	STATE	I
P	C															C	I
N	A															T	I
R	B															O	I

WORD	BITS	FIELD	DESCRIPTION
----	----	----	----
PCSR1	<15>	XPWR	TRANSCIVER POWER FAILURE
PCSR1	<14>	ICAB	PORT/LINK CABLING FAILURE
PCSR1	<13:08>	SELF_TEST	SELF TEST ERROR CODE
PCSR1	<07>	PCTO	PORT COMMAND TIMEOUT
PCSR1	<06:04>	ZERQES	
PCSR1	<03>	RMTC	REMOTE CONSOLE RESERVED.
PCSR1	<02:00>	STATE	

PORT CONTROL AND STATUS REGISTER 2 - PCSR2 -

```

      1   1   1   1   1   0   0   0   0   0   0   0   0   0
!5! 4! 3! 2! 1! 0! 9! 8! 7! 6! 5! 4! 3! 2! 1! 0!
+-----+-----+-----+-----+-----+-----+-----+
!                                     !
!                                     !
!          PCBB <15:0>              !
!                                     !
!
```


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```

; STATUS DEFINITION BITS - PCSR0
000040 .REST= BIT5 ;RESET DEUNA
000100 .INTE= BIT6 ;INTERRUPT ENABLE
000400 .FATI= BIT8 ;FATAL ERROR INTERRUPT
002000 .RCRI= BIT10 ;RECEIVER BUFFER UNAVAILABLE INTERRUPT
004000 .DNI= BIT11 ;DONE INTERRUPT
010000 .TXI= BIT12 ;TRANSMIT RING INTERRUPT
020000 .RXI= BIT13 ;RECEIVE RING INTERRUPT
040000 .PCEI= BIT14 ;PORT COMMAND ERROR INTERRUPT
100000 .SERI= BIT15 ;STATUS ERROR INTERRUPT
142400 .ERRI= .SERI+.PCEI+.RCRI+.FATI ;ANY ERROR INTERRUPT

```

```

; NI STATUS CODES - FROM PCSR1
000007 .STATE= BIT2+BIT1+BIT0 ;DEUNA STATE MASK
000000 .RESET= 0 ;RESET
000001 .PLOAD= BIT0 ;PRIMARY LOAD
000002 .READY= BIT1 ;READY
000003 .RUN= BIT1+BIT0 ;RUNNING
000005 .UHALT= BIT2+BIT0 ;UNIBUS HALTED
000006 .NHALT= BIT2+BIT1 ;NI HALTED
000007 .BHALT= BIT2+BIT1+BIT0 ;NI & UNIBUS HALTED

```

```

; SOFTWARE SWITCH REGISTER DEFINITION BITS
000001 .DCHECK= BIT0 ;PERFORM BUFFER COMPARISONS
000002 .CHKALL= BIT1 ;FULL BUFFER DATA COMPARISONS
000004 .INTERN= BIT2 ;INTERNAL LOOPBACK

```

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; MODE REGISTER BIT DEFINITIONS
000001 .FDX= BIT0 ;FULL DUPLEX
000004 .LOOP= BIT2 ;INTERNAL LOOPBACK

```

```

; RING DESCRIPTOR BLOCK
000005 .TELEN= 5 ;SIZE TRANSMIT DESCRIPTOR RING
000007 .RELEN= 7 ;SIZE RECEIVE DESCRIPTOR RING
000012 .MXPKT= 10 ;MAXIMUM NUMBER OF PACKETS

000000 .SLEN= 0 ;OFFSET TO SEGMENT LENGTH
000002 .SBUF= 2 ;OFFSET TO SEGMENT BUFFER
000004 .STAT= 4 ;OFFSET TO STATUS WORD
000006 .MLEN= 6 ;OFFSET TO MESSAGE LENGTH WORD
000006 .TDR= 6 ;OFFSET TO TDR
000010 .VA= 10 ;OFFSET TO VIRTUAL ADDRESS
000012 .CRCL= 12 ;OFFSET TO CRC LOW WORD
000014 .CRCH= 14 ;OFFSET TO CRC HIGH WORD

000400 .ENP= BIT8 ;END OF PACKET
001000 .STP= BIT9 ;START OF PACKET
020000 .MATCH= BIT13 ;REC EQUALS XMIT MATCH BIT

004000 .CPC= BIT11 ;CRC FRAME CHECK ERROR
010000 .OFLO= BIT12 ;MESSAGE OVERFLOW ERROR

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020000 .FRAM= BIT13 ;FRAME ERROR
040000 .ERRS= BIT14 ;ERROR SUMMARY
100000 .OWN= BIT15 ;OWNERSHIP BIT, 0=HOST, 1=DEUNA

020000 .NCHN= BIT13 ;NO DATA CHAINING
040000 .URTO= BIT14 ;UNIBUS TIMEOUT
100000 .BUFL= BIT15 ;BUFFER LENGTH FRAME ERROR

```


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```

100000 BIT15=100000
040000 BIT14=400000
020000 BIT13=200000
010000 BIT12=100000
004000 BIT11=40000
002000 BIT10=20000
001000 BIT9=10000
000400 BIT8=4000
000200 BIT7=2000
000100 BIT6=1000
000040 BIT5=400
000020 BIT4=200
000010 BIT3=100
000004 BIT2=40
000002 BIT1=20
000001 BIT0=10
001000 ADDR22=1000
000000 R0=%0
000001 R1=%1
000002 R2=%2
000003 R3=%3
000004 R4=%4
000005 R5=%5
000006 R6=%6
000007 R7=%7
000006 SP=%6
000007 PC=%7
000004 PIRQ6=IUT
000340 PRTY7=340
000300 PRTY6=300
000240 PRTY5=240
000200 PRTY4=200
000140 PRTY3=140
000100 PRTY2=100
000040 PRTY1=40
000000 PRTY0=0
000000 PRTY=0
005746 PUSH=005746
024646 PUSH2=024646
005726 POPSP=005726
022626 POPSP2=022626
000000 TRPDEFD=0
000000 NULL=0

```

;DEFERRED SERVICE:

```

000252' DFSEVNT EXITS
104400 EXITS=TRAP+TRPDEFD
000001 TRPDEFD=TRPDEFD+1
000252' DFSEVNT MSGS
104401 MSGS=TRAP+TRPDEFD
000002 TRPDEFD=TRPDEFD+1
000252' DFSEVNT MSGSS

```

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104402 MSGSS=TRAP+TRPDEFD
000003 TRPDEFD=TRPDEFD+1
000252' DFSEVNT MSGNS
104403 MSGNS=TRAP+TRPDEFD
000004 TRPDEFD=TRPDEFD+1
000252' DFSEVNT DATERS
104404 DATERS=TRAP+TRPDEFD
000005 TRPDEFD=TRPDEFD+1
000252' DFSEVNT HDRERS
104405 HDRERS=TRAP+TRPDEFD
000006 TRPDEFD=TRPDEFD+1
000252' DFSEVNT SUPERS
104406 SUPERS=TRAP+TRPDEFD
000007 TRPDEFD=TRPDEFD+1
000252' DFSEVNT BREAKS
104407 BREAKS=TRAP+TRPDEFD
000010 TRPDEFD=TRPDEFD+1
000252' DFSEVNT ENDS
104410 ENDS=TRAP+TRPDEFD
000011 TRPDEFD=TRPDEFD+1
000252' DFSEVNT DATCKS
104411 DATCKS=TRAP+TRPDEFD
000012 TRPDEFD=TRPDEFD+1
000252' DFSEVNT CDATAS
104412 CDATAS=TRAP+TRPDEFD
000013 TRPDEFD=TRPDEFD+1
000252' DFSEVNT ENDITS
104413 ENDITS=TRAP+TRPDEFD
000014 TRPDEFD=TRPDEFD+1

```

;DIRECT SERVICE:

```

000252' DSEVNT GwBUFs
104414 GwBUFs= TRAP + TRPDEFD
000015 TRPDEFD=TRPDEFD+1
000252' DSEVNT GETPAS
104415 GETPAS= TRAP + TRPDEFD
000016 TRPDEFD=TRPDEFD+1
000252' DSEVNT MAP22s
104416 MAP22s= TRAP + TRPDEFD
000017 TRPDEFD=TRPDEFD+1
000252' DSEVNT RANDs
104417 RANDs= TRAP + TRPDEFD
000020 TRPDEFD=TRPDEFD+1
000252' DSEVNT OT0As
104420 OT0As= TRAP + TRPDEFD
000021 TRPDEFD=TRPDEFD+1
000252' DSEVNT BT0Ds
104421 BT0Ds= TRAP + TRPDEFD
000022 TRPDEFD=TRPDEFD+1

```

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000252' 016701 177532      START: MOV    VECTOR,R1          ;GET INTERRUPT VECTOR ADDRESS
000256' 012721 002456'      MOV    #INSTRV,(R1)+        ;SET THE INTERRUPT VECTOR
000262' 116700 177524      MOVH   R1,R0              ;GET DEVICE PRIORITY
000266' 042700 177400      BIC     #177400,R0          ;CLEAR THE UPPER BYTE
000272' 010011              MOV    R0,(R1)            ;AND SET IN VECTOR MEMORY

000274' 016705 177506      RESTR: MOV    ADDR,R5          ;GET ADDRESS OF DEUNA UNIBUS CSR'S
000300' 012765 000040 000000 1s: MOV    #.REST,.PCSR0(R5)      ;WRITE UPPER BYTE TO CLEAR ALL
000306' 012702 001750      MOV    #1000,R2            ;DEVICE TIMEOUT WAIT COUNT
000312' 032765 000400 000000 2s: BIT    #.FAT1,.PCSR0(R5)      ;TEST FOR FATAL ERROR
000320' 001440              BEQ     3s                ;CONTINUE IF NOT SET
000322'              PRINT    <FATAL ERROR - RESETTING DEUNA>
000322'              MSG     64s                      ;ASCII MESSAGE CALL
000322' 104401 000000' 000332' MSGS,BEGIN,64s          ;ASCII MESSAGE CALL
000330' 000420              BR      65s                ;
000332' 045              .ASCII  'X'
000333' 106 072141 066141 .ASCII  /FATAL ERROR - RESETTING DEUNA/
000340' 042440 071162      64s: .ASCII  /FATAL ERROR - RESETTING DEUNA/
000346' 026440 051040 071545
000354' 072145 064564 063556
000362' 042040 052505 040516
000370' 000              .EVEN
000372' 000372'          65s: CLR     FLAGS          ;CLEAR INTERRUPTS FLAGS WORD ;CC
000376' 056567 000000 005440 BIS     .PCSR0(R5),FLAGS ;GET CONTENTS OF PCSRO ;CC
000404'          HRDR    ERRPRT <FATAL ERROR - RESETTING DEUNA> ;CC
000404' 004767 004430 JSR     PC,ERRMSG      ;SET UP ERROR PRINTOUT
001              .IF B <ERRPRT>
HDRRS,BEGIN,NULL      ;FATAL ERROR - RESETTING DEUNA
.IFF
HDRRS,BEGIN,ERRPRT    ;FATAL ERROR - RESETTING DEUNA
.ENDC
000410' 104405 000000' 005762' JMP     DROP          ;SEEMS TO BE DEAD
000422' 032765 004000 000000 3s: BIT    #.DNI,.PCSR0(R5) ;WAIT FOR DEVICE TO COME READY
000430' 001040      HNE     4s                ;CONTINUE PROCESSING WHEN SET
000432'          BREAK          ;TEMP RETURN TO DECX
000432' 104407 000000'      BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR...
000436' 104407 000000'      BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
000442' 005302      DEC     R2                ;DECREMENT TIMEOUT COUNTER
000444' 001322      BNE     2s                ;AND LOOP TILL EXPIRED
000446'          PRINT    <NO DNI AFTER RESET>
000446'          MSG     66s                      ;ASCII MESSAGE CALL
000446' 104401 000000' 000456' MSGS,BEGIN,66s          ;ASCII MESSAGE CALL
000454' 000412              BR      67s                ;
000456' 045              .ASCII  'X'
000457' 116 020157 047104 .ASCII  /NO DNI AFTER RESET/
000464' 020111 063141 062564
000472' 020162 062562 062563
000500' 000164          .EVEN
000502'          67s: CLR     FLAGS          ;CLEAR INTERRUPTS FLAGS WORD ;CC
000506' 005067 005336      BIS     .PCSR0(R5),FLAGS ;GET CONTENTS OF PCSRO ;CC
000506' 056567 000000 005330 HRDR    ERRPRT <NO DNI AFTER RESET>
000514'          JSR     PC,ERRMSG      ;SET UP ERROR PRINTOUT
000514' 004767 004320

```

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```

001              .IF B <ERRPRT>
HDRRS,BEGIN,NULL      ;NO DNI AFTER RESET
.IFF
HDRRS,BEGIN,ERRPRT    ;NO DNI AFTER RESET
.ENDC
000520' 104405 000000' 005762' JMP     DROP          ;ALL DONE FOLKS
000526' 000167 001272      MOV    #.DNI,.PCSR0(R5) ;CLEAR THE REGISTER
000532' 012765 004000 000000 4s: MOV    #.INTE,.PCSR0(R5) ;ENABLE INTERRUPTS
000540' 012765 000100 000000      CLR     FLAGS          ;CLEAR THE INTERRUPT FLAG WORD
000546' 005067 005272

000552'          DEMAND    .STOP,<UNABLE TO STOP THE DEUNA>
000552' 004767 002000      JSR     PC,SETCMD
000556' 000017          .WORD    .STOP
000560' 103027          BCC     69s
000562'          MSG     68s
000562' 104401 000000' 000606' MSGS,BEGIN,68s          ;ASCII MESSAGE CALL
000570'          HRDR    ERRPRT <> ;CC
000570' 004767 004244      JSR     PC,ERRMSG      ;SET UP ERROR PRINTOUT
001              .IF B <ERRPRT>
HDRRS,BEGIN,NULL      ;
.IFF
HDRRS,BEGIN,ERRPRT    ;
.ENDC
000574' 104405 000000' 005762' JMP     DROP          ;
000602' 000167 001216      .ASCII  'X'
000606' 045              .ASCII  /UNABLE TO STOP THE DEUNA/
000607' 125 060556 066142
000614' 020145 067564 071440
000622' 067564 020160 064164
000630' 020145 042504 047125
000636' 000101          .EVEN
000640'          69s: WAIT     .READY,<UNABLE TO ENTER READY STATE>
000640'          JSR     PC,STATE
000644' 000002          .WORD    .READY
000646' 103031          BCC     71s
000650'          MSG     70s
000650' 104401 000000' 000674' MSGS,BEGIN,70s          ;ASCII MESSAGE CALL
000656'          HRDR    ERRPRT <> ;CC
000656' 004767 004156      JSR     PC,ERRMSG      ;SET UP ERROR PRINTOUT
001              .IF B <ERRPRT>
HDRRS,BEGIN,NULL      ;
.IFF
HDRRS,BEGIN,ERRPRT    ;
.ENDC
000662' 104405 000000' 005762' JMP     DROP          ;
000670' 000167 001130      .ASCII  'X'
000674' 045              .ASCII  /UNABLE TO ENTER READY STATE/
000675' 125 060556 066142
000702' 020145 067564 062440
000710' 072156 071145 071040
000716' 060545 074544 071440
000724' 060564 062564 000
000732' 000732'          .EVEN
71s:

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000736' 004767 004036
000742' 010067 004254
000746' 110167 004252
000752' 112767 000005 004245
000760' 012700 005546'
000764' 004767 004010
000770' 010067 004234
000774' 110167 004232
001000' 112767 000007 004225
001006' 012700 005212'
001012' 004767 003762
001016' 010065 000004
001022' 010165 000006
001026'
001026' 004767 001524
001032' 000001
001034' 103041
001036'
001036' 104401 000000' 001062'
001044'
001044' 004767 003770
001
001050' 104405 000000' 005762'
000
001056' 000167 000742
001062' 045
001063' 125 060556 066142
001070' 020145 067564 071440
001076' 072145 072040 062550
001104' 070040 071157 020164
001112' 067543 072156 067562
001120' 020154 060542 062563
001126' 060440 062144 062562
001134' 071563 000
001140' 001140'
001140'
001140' 012767 000015 004044
001146' 005067 004042
001152' 032767 000004 176636
001160' 001403
001162' 052767 000004 004024
001170'
001170' 004767 001362
001174' 000002
001176' 103034
001200'
001200' 104401 000000' 001224'
001206'
001206' 004767 003626
001
001212' 104405 000000' 005762'

```

```

JSR PC,CVPHY ;CONVERT TO PHYSICAL
MOV R0,RNGBLK ;SET IN RING BLOCK HEADER
MOVH R1,RNGBLK+2 ;AND THE EXTENDED ADDRESS BITS
MOVH #TELEN,RNGBLK+3 ;NUMBER OF WORDS IN EACH ENTRY
MOV #RDRB,R0 ;VIRTUAL ADDRESS OF THE RECEIVE RING
JSR PC,CVPHY ;CONVERT TO PHYSICAL
MOV R0,RNGBLK+6 ;SET IN RING BLOCK HEADER
MOVH R1,RNGBLK+10 ;AND THE EXTENDED ADDRESS BITS
MOVH #TELEN,RNGBLK+11 ;NUMBER OF WORDS IN EACH ENTRY
MOV #PCB,R0 ;GET VIRTUAL PORT CONTROL BASE ADDRESS
JSR PC,CVPHY ;CONVERT TO PHYSICAL
MOV R0,PCSR2(R5) ;SET THE PORT CONTROL BLOCK BASE ADDRESS
MOV R1,PCSR3(R5) ;SET THE EXTENDED ADDRESS BITS
DEMAND .PCRB,<UNABLE TO SET THE PORT CONTROL BASE ADDRESS>
JSR PC,SETCMD
.WORD .PCRB
BCC 73$
MSG 72$
MSG$,BEGIN,72$ ;ASCII MESSAGE CALL
HRDER ERRPRT <> ;CC
JSR PC,ERRMSG ;SET UP ERROR PRINTOUT
.IF B <ERRPRT>
HRDEFS,BEGIN,NULL ;
.IFF
HRDEFS,BEGIN,ERRPRT ;
.ENDC
JMP DROP
.ASCII '%'
.ASCIIZ /UNABLE TO SET THE PORT CONTROL BASE ADDRESS/

72$:
.EVEN
73$:
MOV #15,PCB ;WRITE MODE REGISTER
CLR PCB+2 ;FULL DUPLEX, EXTERNAL LOOPBACK
BIT #.INTERNAL,SF1 ;INTERNAL LOOPBACK
BEG 5$ ;NO - KEEP DEFAULT EXTERNAL
BIS #.LOOP,PCB+2 ;SET THE INTERNAL LOOPBACK BIT
DEMAND .CMD,<UNABLE TO WRITE THE MODE REGISTER>
JSR PC,SETCMD
.WORD .CMD
RCC 75$
MSG 74$
MSG$,BEGIN,74$ ;ASCII MESSAGE CALL
HRDER ERRPRT <> ;CC
JSR PC,ERRMSG ;SET UP ERROR PRINTOUT
.IF B <ERRPRT>
HRDEFS,BEGIN,NULL ;
.IFF
HRDEFS,BEGIN,ERRPRT ;

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000
001220' 000167 000600
001224' 045
001225' 125 060556 066142
001232' 020145 067564 073440
001240' 064562 062564 072040
001246' 062550 066440 062157
001254' 020145 062562 064547
001262' 072163 071145 000
001270' 001270'
001270' 012767 000004 003714
001276'
001276' 004767 001254
001302' 000002
001304' 103037
001306'
001306' 104401 000000' 001332'
001314'
001314' 004767 003520
001
001320' 104405 000000' 005762'
000
001326' 000167 000472
001332' 045
001333' 125 060556 066142
001340' 020145 067564 071040
001346' 060545 020144 064164
001354' 020145 064160 071571
001362' 061551 066141 060440
001370' 062144 062562 071563
001376' 051040 046517 000
001404' 001404'
001404' 012701 006052'
001410' 012702 000002
001414' 012700 005214'
001420' 012021
001422' 012021
001424' 012021
001426' 005302
001430' 001371
001432' 012721 002540
001436' 005021
001440' 012721 003001
001444' 012701 006052'
001450' 012702 000022
001454' 012703 177777
001460' 010304
001462' 004767 003206
001466' 010367 004354
001472' 010467 004352

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.ENDC
JMP DROP
.ASCII '%'
.ASCIIZ /UNABLE TO WRITE THE MODE REGISTER/

74$:
.EVEN
75$:
MOV #4,PCB ;READ PHYSICAL ADDRESS
DEMAND .CMD,<UNABLE TO READ THE PHYSICAL ADDRESS ROM>
JSR PC,SETCMD
.WORD .CMD
RCC 77$
MSG 76$
MSG$,BEGIN,76$ ;ASCII MESSAGE CALL
HRDER ERRPRT <> ;CC
JSR PC,ERRMSG ;SET UP ERROR PRINTOUT
.IF B <ERRPRT>
HRDEFS,BEGIN,NULL ;
.IFF
HRDEFS,BEGIN,ERRPRT ;
.ENDC
JMP DROP
.ASCII '%'
.ASCIIZ /UNABLE TO READ THE PHYSICAL ADDRESS ROM/

76$:
.EVEN
77$:
MOV #HEADER,R1 ;WHERE TO PUT IT
MOV #2,R2 ;PACK IT TWICE
MOV #PCB+2,R0 ;START OF RECEIVED DATA
MOV (R0)+,(R1)+ ;MOVE 6 BYTES
MOV (R0)+,(R1)+ ;BYTES 3 & 4
MOV (R0)+,(R1)+ ;BYTES 5 & 6
DEC R2 ;DECREMENT THE COUNTER
RNE 6$ ;AND LOOP TILL DONE
MOV #2540,(R1)+ ;SET RECORD TYPE FIELD TO DIAG
CLR (R1)+ ;CLEAR THE MSG LENGTH
MOV #3001,(R1)+ ;SET SUBTYPE FIELD TO DEC/X
MOV #HEADER,R1 ;START OF HEADER
MOV #18,,R2 ;SIZE OF HEADER
MOV #-1,R3 ;INITIAL CRC
MOV R3,R4 ;SAME IN R4
JSR PC,BLKCRRC ;DO THE CALCULATIONS
MOV R3,HDRCRC ;SAVE THE CRC - HIGH WORD
MOV R4,HDRCRC+2 ;SAVE THE CRC - LOW WORD

```

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```

001476' 004767 002552      JSR    PC,TSETUP      ;SETUP THE TRANSMIT RING STRUCTURE
001502' 004767 003006      JSR    PC,RSETUP      ;SETUP THE RECEIVER RING STRUCTURE

001506' 012700 005222'      MOV    #RNGBLK,H0      ;GET THE VIRTUAL ADDRESS
001512' 004767 003262      JSR    PC,CVTPHY      ;CONVERT TO PHYSICAL
001516' 010067 003472      MOV    R0,PCB+2      ;SET THE RING BUFFER STRUCTURE
001522' 010167 003470      MOV    R1,PCB+4      ; AND THE EXTENDED ADDRESS BITS
001526' 012767 000011 003456  MOV    #11,PCB      ;WRITE RING FUNCTION CODE
001534'                                DEMAND .CMD,<UNABLE TO SET THE RING BUFFER STRUCTURE>
001534' 004767 001016      JSR    PC,SETCMD
001540' 000002      .WORD    .CMD
001542' 103037      BCC     79$
001544'      MSG     78$
001544' 104401 000000' 001570' MSG$,BEGIN,78$      ;ASCII MESSAGE CALL
001552'                                HRDR    ERRPRT <>      ;CC
001552' 004767 003262      JSR    PC,ERRMSG      ;SET UP ERROR PRINTOUT
                                .IF B <ERRPRT>
                                HRDR$,BEGIN,NULL
                                .IFF
                                HRDR$,BEGIN,ERRPRT
                                .ENDC
001556' 104405 000000' 005762' JMP     DROP
                                .ASCII '% '
                                .ASCIIZ /UNABLE TO SET THE RING BUFFER STRUCTURE/

001564' 000167 000234      79$:
001570' 045
001571' 125 060556 066142
001576' 020145 067564 071440
001604' 072145 072040 062550
001612' 071040 067151 020147
001620' 072542 063146 071145
001626' 071440 071164 061565
001634' 072564 062562 000
                                .EVEN
001642'      79$:
001642'                                DEMAND .START,<UNABLE TO START THE DEUNA>
001642' 004767 000710      JSR    PC,SETCMD
001646' 000004      .WORD    .START
001650' 103030      BCC     81$
001652'      MSG     80$
001652' 104401 000000' 001676' MSG$,BEGIN,80$      ;ASCII MESSAGE CALL
001660'                                HRDR    ERRPRT <>      ;CC
001660' 004767 003154      JSR    PC,ERRMSG      ;SET UP ERROR PRINTOUT
                                .IF B <ERRPRT>
                                HRDR$,BEGIN,NULL
                                .IFF
                                HRDR$,BEGIN,ERRPRT
                                .ENDC
001664' 104405 000000' 005762' JMP     DROP
                                .ASCII '% '
                                .ASCIIZ /UNABLE TO START THE DEUNA/

001672' 000167 000126      80$:
001676' 045
001677' 125 060556 066142
001704' 020145 067564 071440
001712' 060564 072162 072040
001720' 062550 042040 052505
001726' 040516 000
                                .EVEN
001732'      81$:
001732'                                WAIT    .RUN,<UNABLE TO ENTER RUN STATE>

```

SEQ 19

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```

001732' 004767 000720      JSR    PC,%STATE
001736' 000003      .WORD    .RUN
001740' 103030      BCC     83$
001742'      MSG     82$
001742' 104401 000000' 001766' MSG$,BEGIN,82$      ;ASCII MESSAGE CALL
001750'                                HRDR    ERRPRT <>      ;CC
001750' 004767 003064      JSR    PC,ERRMSG      ;SET UP ERROR PRINTOUT
                                .IF B <ERRPRT>
                                HRDR$,BEGIN,NULL
                                .IFF
                                HRDR$,BEGIN,ERRPRT
                                .ENDC
001754' 104405 000000' 005762' JMP     DROP
                                .ASCII '% '
                                .ASCIIZ /UNABLE TO ENTER RUN STATE/

001762' 000167 000036      82$:
001766' 045
001767' 125 060556 066142
001774' 020145 067564 062440
002002' 072156 071145 071040
002010' 067165 071440 060564
002016' 062564 000
                                .EVEN
002022'      83$:
002022' 000404      BR     LOOP      ;DO SOME TRANSMIT/RECEPTIONS

002024' 005065 000000      DROP: CLR    .PCSR0(R5)      ;DISABLE INTERRUPTS
002030'                                END      ;THATS ALL FOLKS
002030' 104410 000000'      ENDS,BEGIN

```

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```

;--
;
; EXECUTE A SERIES OF TRANSMITS AND RECEPTIONS
;
;--
LOOP: MOV     RPCNT,R2          ;GET NUMBER OF RECEIVER PACKETS
      MOV     TDRB,R3        ;ADDRESS OF TRANSMITTER DESC BLOCK
      MOV     RDRB,R4        ;GET ADDRESS OF RECEIVER DESC BLOCK
      BIC     #C1003,,STAT(R3) ;CLEAR ALL BUT THE EXTENDED ADDRESS BITS
      BIS     #OWN,,STAT(R3)  ;SET OWNERSHIP TO DEUNA
      ADD     #TELEN*2,R3     ;ADVANCE TO NEXT ENTRY
      BIC     #C403,,STAT(R3) ;CLEAR ALL BUT THE EXTENDED ADDRESS BITS
      BIS     #OWN,,STAT(R3)  ;SET OWNERSHIP TO DEUNA
      ADD     #TELEN*2,R3     ;ADVANCE TO NEXT ENTRY
      BIC     #C3,,STAT(R4)   ;CLEAR ALL BUT THE EXTENDED ADDRESS BITS
      BIS     #OWN,,STAT(R4)   ;ASSIGN OWNERSHIP TO THE DEUNA
      CLR     #LEN(R4)        ;CLEAR THE RETURNED BYTE COUNT
      ADD     #RELEN*2,R4     ;ADVANCE TO NEXT ENTRY
      DEC     R2              ;DECREMENT PACKET COUNTER
      BNE     1$             ;DO ENTIRE LIST
      CLR     FLAGS          ;CLEAR INTERRUPT (CSR) FLAG WORD
      DEMAND  .PDMO,<UNABLE TO ISSUE A POLL DEMAND>
      JSR     PC,SETCMD
      .WORD   .PDMO
      RCC     65$
      MSG     64$
      MSGS,BEGIN,64$          ;ASCII MESSAGE CALL
      HRDR    ERRPRT <>      ;CC
      JSR     PC,ERRMSG       ;SET UP ERROR PRINTOUT
      .IF B <ERRPRT>
      HRDERS,BEGIN,NULL      ;
      .IFF
      HRDERS,BEGIN,ERRPRT    ;
      .ENDC
      JMP     DRUP
      .ASCII  '%'
      .ASCIIZ /UNABLE TO ISSUE A POLL DEMAND/

      .EVEN
65$:
002240' 002240' 65$:
002240' 012702 007640 MOV     #4000,,R2          ;TIMEOUT LOOP COUNT
002244' 016700 003574 MOV     FLAGS,R0          ;GET CURRENT FLAGS WORD
002250' 032700 142400 BIT     #ERR1,R0          ;LOOK FOR ERROR CONDITION
002254' 001040 BNE     3$
002256' 042700 147777 HIC     #C<.TX1+.RX1>,,R0 ;KEEP ONLY COMPLETION BITS
002262' 022700 030000 CMP     #.TX1+.RX1,R0    ;LOOK FOR BOTH COMPLETION
002266' 001433 BEQ     3$          ;FINISHED WHEN BOTH SET
002270' BREAK
002270' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
002274' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.

```

SEQ 21

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```

      DEC     R2              ;DECREMENT THE LOOP COUNT
      BNE     2$             ;LOOP WHILE NOT ZERO
      PRINT   <RECEIVER TIMEOUT>
      MSG     66$
      MSGS,BEGIN,66$          ;ASCII MESSAGE CALL
      BR      67$
      .ASCII  '%'
      .ASCIIZ /RECEIVER TIMEOUT/

      .EVEN
67$:
002336' 002336' 67$:
002336' 004767 002476 HRDR    ERRPRT <RECEIVER TIMEOUT> ;CC
002336' 004767 002476 JSR     PC,ERRMSG       ;SET UP ERROR PRINTOUT
001      .IF B <ERRPRT>
      HRDERS,BEGIN,NULL      ;RECEIVER TIMEOUT
      .IFF
      HRDERS,BEGIN,ERRPRT    ;RECEIVER TIMEOUT
      .ENDC
      CLR     .PCSR0(R5)      ;DISABLE INTERRUPTS ;CC
      HR      10$
      CLR     .PCSR0(R5)      ;DISABLE INTERRUPTS ;CC
      JSR     PC,CSRCHK       ;CHECK THE CSR (FLAG) FOR ERRORS
      BCS     11$            ;DROP ON REAL TIGHT ONE
      JSR     PC,CHKSTA       ;CHECK TRANSMIT/RECEIVE STATUS
      BCS     11$            ;AND EXIT ON ERROR
      JSR     PC,CHKHCT       ;CHECK THE BYTE COUNT
      BCS     11$
      JSR     PC,CHKCRC       ;CHECK THE RETURNED CRC'S
      BCS     9$
      BIT     #DCHECK,SR1     ;PERFORM DATA COMPARISONS
      REQ     10$
      JSR     PC,CHKBUF       ;CHECK THE DATA BUFFERS
      BCS     11$
      ENDTIT
      ENDTIS,BEGIN          ;SIGNAL END OF ITERATION.
      MOV     #.INTE,,PCSR0(R5) ;MONITOR SHALL TEST END OF PASS
      JMP     LOOP          ;ENABLE INTERRUPTS
      ENDTIT
      ENDTIS,BEGIN          ;SIGNAL END OF ITERATION. ;CC
      JMP     RSTRT
      ENDTIS,BEGIN          ;MONITOR SHALL TEST END OF PASS

```


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```

;--
;
; SERVICE THE DEUNA INTERRUPTS
;
; ON RETURN
;   FLAGS = COPY OF PCSR0
;
; NOTE: THE RECEIVE FLAG IS SET ONLY WHEN "ALL" PACKETS
;       HAVE BEEN RECEIVED.
;--
;--
002456' 010046      INTSRV: MOV    R0,-(SP)          ;SAVE R0
002460' 010146      MOV    R1,-(SP)          ;SAVE R1
002462' 016701      MOV    ADDR,R1          ;GET ADDRESS OF DEUNA
002466' 056167      BIS     000000 003350    .PCSR0(R1),FLAGS ;GET CONTENTS OF PCSR0
002474' 116761      MOVH    003345 000001    FLAGS+1,.PCSR0+1(R1) ;AND WRITE UPPER BITS TO CLEAR
002502' 032767      BIT     020000 003334    #.RXI,FLAGS      ;TEST FOR RECEIVER INTERRUPT
002510' 001417      BEQ     3S              ;NOT THIS ONE
002512' 012701      MOV     005546' 002512    #RDRH,R1        ;RECEIVER BUFFER RING
002516' 016700      MOV     002512    RPCNT,R0          ;NUMBER OF PACKETS
002522' 005761      1S:     TST     000004    .STAT(R1)      ;TEST THE OWNERSHIP BIT
002526' 100405      BHI     2S              ;AND EXIT IF NEGATIVE
002530' 062701      ADD     000016    #.RELEN*2,R1          ;ADVANCE TO NEXT ENTRY
002534' 005300      DEC     R0              ;DECREMENT PACKET COUNTER
002536' 001371      BNE     1S              ;AND LOOP TILL ALL TESTED
002540' 000403      BR     3S
002542' 042767      2S:     BIC     020000 003274 2S:     #.RXI,FLAGS      ;CLEAR THE FLAG BIT
002550' 012601      3S:     MOV     (SP)+,R1          ;RESTORE R1
002552' 012600      MOV     (SP)+,R0          ;RESTORE R0
002554' 000002      RTI                    ;FOR JUST RETURN

```

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```

;--
;
; SET THE COMMAND AND WAIT FOR COMPLETION
;
; ON RETURN
;   C = 0 - SUCCESS
;   C = 1 - FAILURE - COMMAND TIMEOUT
;--
;--
002556' 004767      SETCMD: JSR     PC,SAVREG          ;SAVE ALL REGISTERS
002562' 017600      MOV     000016    #16(SP),R0        ;GET THE COMMAND WORD
002566' 062766      ADD     000002 000016    #2,16(SP)    ;ADJUST THE RETURN ADDRESS
002574' 052700      BIS     000100    #.INTE,R0          ;FORCE THE INTERRUPT ENABLE
002600' 010065      MOV     000000    R0,.PCSR0(R5)      ;SET THE COMMAND FUNCTION CODE
002604' 012700      MOV     000144    #100,,R0          ;OUTER TIMEOUT LOOP COUNT
002610' 012701      1S:     MOV     000062    #50,,R1      ;INNER LOOP COUNT
002614' 032767      2S:     BIT     004000 003222 2S:     #.DNI,FLAGS      ;TEST FOR COMMAND COMPLETION
002622' 001011      BNE     3S              ;EXIT WHEN SET
002624' 005301      DEC     R1              ;TEST A FEW TIMES BEFORE BREAKING
002626' 001372      BNE     2S              ;QUICK LOOP ENTRY
002630'             BREAK                  ;TEMP EXIT TO MONITOR
002630' 104407      BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR....
002634' 104407      BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
002640' 005300      DEC     R0              ;DECREMENT TIMEOUT LOOP COUNT
002642' 001362      BNE     1S              ;LONG LOOP ENTRY
002644' 000261      SEC                    ;SET THE ERROR INDICATOR
002646' 042767      3S:     BIC     004000 003170 3S:     #.DNI,FLAGS      ;CLEAR THE DONE BIT
002654' 000207      RTS     PC              ;AND RETURN
;--
;
; WAIT FOR DEUNA TO ENTER SPECIFIED STATE
;
; ON RETURN
;   C = 0 - SUCCESS
;   C = 1 - FAILURE - COMMAND TIMEOUT
;--
;--
002656' 004767      WSTATE: JSR     PC,SAVREG          ;SAVE ALL REGISTERS
002662' 017602      MOV     000016    #16(SP),R2        ;GET EXPECTED STATE
002666' 062766      ADD     000002 000016    #2,16(SP)    ;ADJUST THE RETURN ADDRESS
002674' 012701      MOV     000100    #100,R1           ;TIMEOUT VALUE
002700' 016500      1S:     MOV     000002    .PCSR1(R5),R0 ;GET STATUS WORD
002704' 042700      BIC     177770    #C,STATE,R0       ;KEEP ONLY THE STATE FIELD
002710' 020002      CMP     R0,R2           ;COMPARE WITH EXPECTED
002712' 001407      BEQ     2S              ;EXIT IF THE SAME
002714'             BREAK                  ;TEMP RETURN TO DECK
002714' 104407      BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR....
002720' 104407      BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
002724' 005301      DEC     R1              ;DECREMENT TIMEOUT COUNTER
002726' 001364      BNE     1S              ;AND LOOP BACK IF NOT ZERO
002730' 000261      SEC                    ;SET THE ERROR FLAG
002732' 000207      2S:     RTS     PC              ;AND RETURN

```

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```

;--
;
; CHECK THE CSR (FLAGS) FOR ERRORS AND
; NORMAL COMPLETION CODES
;--

002734' 004767 002204 CSRCHK: JSR PC,SAVREG ;SAVE ALL REGISTERS
002740' 016700 003100 MOV FLAGS,R0 ;GET INTERRUPT SUMMARY
002744' 032700 000400 1S: BIT #.FAT1,R0 ;TEST FOR FATAL ERROR
002750' 001425 BEQ ZS
002752' PRINT <FATAL ERROR INTERRUPT>
002752' MSG 64S
002752' 104401 000000' 002762' MSGS,BEGIN,64S ;ASCII MESSAGE CALL
002760' 000414 BR 65S
002762' 045 .ASCII '%'
002763' 106 072141 066141 .ASCIZ /FATAL ERROR INTERRUPT/
002770' 042440 071162 071157
002776' 044440 072156 071145
003004' 072562 072160 000
003012' 003012' .EVEN
003012' 65S: HDRER ERRPRT <FATAL ERROR INTERRUPT>
003012' 004767 002022 JSR PC,ERRMSG ;SET UP ERROR PRINTOUT
003012' 001 .IF B <ERRPRT>
003016' 104405 000000' 005762' HDRERS,BEGIN,NULL ;FATAL ERROR INTERRUPT
003016' 000 .IFF
003024' 032700 002000 2S: HDRERS,BEGIN,ERRPRT ;FATAL ERROR INTERRUPT
003030' 001435 .ENDC
003032' BIT #.RCH1,R0 ;RECEIVE BUFFER UNAVAILABLE
003032' BEQ ZS
003032' PRINT <RECEIVE BUFFER NOT AVAILABLE INTERRUPT>
003032' MSG 66S
003032' 104401 000000' 003042' MSGS,BEGIN,66S ;ASCII MESSAGE CALL
003040' 000424 BR 66S
003042' 045 .ASCII '%'
003043' 122 061545 064545 .ASCIZ /RECEIVE BUFFER NOT AVAILABLE INTERRUPT/
003050' 062566 041040 063165
003050' 062566 020162 067516
003064' 020164 073101 064541
003072' 060554 066142 020145
003100' 067111 062564 071162
003106' 070165 000164 .EVEN
003112' 67S: HDRER ERRPRT <RECEIVE BUFFER NOT AVAILABLE INTERRUPT>
003112' JSR PC,ERRMSG ;SET UP ERROR PRINTOUT
003112' 004767 001722 .IF B <ERRPRT>
003112' 001 HDRERS,BEGIN,NULL ;RECEIVE BUFFER NOT AVAILABLE INTERRUPT
003116' 104405 000000' 005762' .IFF
003116' 000 HDRERS,BEGIN,ERRPRT ;RECEIVE BUFFER NOT AVAILABLE INTERRUPT
003124' 032700 040000 3S: .ENDC
003130' 001430 BIT #.PCE1,R0 ;PORT COMMAND ERROR
003132' BEQ ZS
003132' PRINT <PORT COMMAND ERROR INTERRUPT>
003132' MSG 68S

```

SEG 25

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```

003132' 104401 000000' 003142' MSGS,BEGIN,68S ;ASCII MESSAGE CALL
003140' 000417 BR 69S
003142' 045 .ASCII '%'
003143' 120 071157 020164 .ASCIZ /PORT COMMAND ERROR INTERRUPT/
003150' 067503 066555 067141
003156' 020144 071105 067562
003164' 020162 067111 062564
003172' 071162 070165 000164 .EVEN
003200' 69S: HDRER ERRPRT <PORT COMMAND ERROR INTERRUPT>
003200' JSR PC,ERRMSG ;SET UP ERROR PRINTOUT
003200' 004767 001634 .IF B <ERRPRT>
003200' 001 HDRERS,BEGIN,NULL ;PORT COMMAND ERROR INTERRUPT
003204' 104405 000000' 005762' .IFF
003204' 000 HDRERS,BEGIN,ERRPRT ;PORT COMMAND ERROR INTERRUPT
003212' 032700 100000 4S: .ENDC
003216' 001425 BIT #.SER1,R0 ;STATUS ERROR INTERRUPT
003220' BEQ ZS
003220' PRINT <STATUS ERROR INTERRUPT>
003220' MSG 70S
003220' 104401 000000' 003230' MSGS,BEGIN,70S ;ASCII MESSAGE CALL
003226' 000414 BR 71S
003230' 045 .ASCII '%'
003231' 123 060564 072564 .ASCIZ /STATUS ERROR INTERRUPT/
003236' 020163 071105 067562
003244' 020162 067111 062564
003252' 071162 070165 000164 .EVEN
003260' 71S: HDRER ERRPRT <STATUS ERROR INTERRUPT>
003260' JSR PC,ERRMSG ;SET UP ERROR PRINTOUT
003260' 004767 001554 .IF B <ERRPRT>
003260' 001 HDRERS,BEGIN,NULL ;STATUS ERROR INTERRUPT
003264' 104405 000000' 005762' .IFF
003264' 000 HDRERS,BEGIN,ERRPRT ;STATUS ERROR INTERRUPT
003272' 032767 010000 002544 5S: .ENDC
003300' 001026 BIT #.TX1,FLAGS ;TEST THAT THE TRANSMITTER FINISHED
003302' BNE ZS
003302' PRINT <NO TRANSMIT INTERRUPT>
003302' MSG 72S
003302' 104401 000000' 003312' MSGS,BEGIN,72S ;ASCII MESSAGE CALL
003310' 000414 BR 73S
003312' 045 .ASCII '%'
003313' 116 020157 071124 .ASCIZ /NO TRANSMIT INTERRUPT/
003320' 067141 066563 072151
003326' 044440 072156 071145
003334' 072562 072160 000
003342' 003342' .EVEN
003342' 73S: HDRER ERRPRT <NO TRANSMIT INTERRUPT>
003342' JSR PC,ERRMSG ;SET UP ERROR PRINTOUT
003342' 004767 001472 .IF B <ERRPRT>
003342' 001 HDRERS,BEGIN,NULL ;NO TRANSMIT INTERRUPT
003342' .IFF
003346' 104405 000000' 005762' HDRERS,BEGIN,ERRPRT ;NO TRANSMIT INTERRUPT

```

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000
003354' 000402      .ENDC
003356' 000241      BR      7S
003360' 000401      6S:   CLC      ;CLEAR THE ERROR INDICATOR
003362' 000261      BR      8S      ;AND EXIT
003364' 000207      7S:   SEC      ;SET THE ERROR INDICATOR
                        8S:   RTS     PC      ;AND RETURN

```

SEQ 27

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```

;--
;
; CHECK THE RECEIVED PACKETS DATA
;
; ON RETURN
;   C = 0 - SUCCESS
;   C = 1 - FAILURE - COMMAND TIMEOUT
;--
CHKBUF: JSR     PC,SAVREG      ;SAVE ALL REGISTERS
        MOV     #RDRH,R3      ;START OF RECEIVER RING DECS.
        MOV     #TDRB+<LEN*2>,R4 ;START OF TRANSMITTER RING DESC.
        MOV     R3,R5          ;RECEIVE PACKET COUNT
1S:     MOV     .VA(R4),R0      ;TRANSMIT BUFFER
        MOV     .VA(R3),R1      ;RECEIVE BUFFER
        ADD     #18,,R1        ;BYPASS THE HEADER
        MOV     .LEN(R3),R2     ;EXPECTED BYTE COUNT
        SUB     #22,,R2        ;BACK OFF FOR CMC AND HEADER
        ASH     R2             ;CONVERT BYTE COUNT TO WORD COUNT
        CLR     -(SP)          ;CLEAR THE STATUS WORD
        CLR     A#AS          ;CLEAR THE ACTUAL WORD
        CLR     ASB           ;CLEAR THE SHOULD BE WORD
2S:     CMP     (R0),(R1)       ;COMPARE THE BYTES
        BEQ     3S            ;ADVANCE TO NEXT IF SAME
        MOV     R0,SHADR       ;SET ADDRESS OF SHOULD BE
        MOV     (R0),ASB       ;SET DATA OF SHOULD BE
        MOV     R1,#ASADK      ;SET ADDRESS OF ACTUAL
        MOV     (R1),A#AS      ;SET DATA OF ACTUAL
        DATERR                ;REPORT THE ERROR
        DATERR,BEGIN          ;DATA ERROR!!!
        INC     (SP)          ;SET THE ERROR FLAG
        BIT     #CMALL,SR1     ;TEST ENTIRE BUFFER
        BEQ     4S            ;EXIT IF CLEAR
        ADD     #2,R0          ;ADVANCE THE SHOULD BE POINTER
        ADD     #2,R1          ;ADVANCE THE ACTUAL POINTER
        DEC     R2             ;DECREMENT THE BYTE COUNT
        BNE     2S            ;AND LOOP TILL DONE
4S:     TST     (SP)+          ;TEST THE ERROR FLAG
        BEQ     5S            ;CONTINUE PROCESSING IF ZERO
        SEC     5S            ;SET THE ERROR FLAG
        BR      6S            ;AND EXIT
5S:     ADD     #LEN*4,R4      ;ADVANCE TRANSMIT POINTER
        ADD     #LEN*2,R3      ;ADVANCE RECEIVER POINTER
        DEC     R5             ;DECREMENT THE PACKET COUNTER
        BNE     1S            ;AND LOOP TILL DONE
6S:     RTS     PC            ;RETURN

```

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```

;--
;
; CHECK TRANSMIT AND RECEIVE STATUS REGISTERS
;
; ON RETURN
;   C = 0 - SUCCESS
;   C = 1 - FAILURE - COMMAND TIMEOUT
;--

003552' 004767 001366      CHKSTA: JSR    PC,SAVREG          ;SAVE ALL REGISTERS
003556' 012701 005250'      MOV    #TDH*2,R1          ;FIRST TRANSMIT POINTER
003562' 012702 005546'      MOV    #RDRB,R2          ;FIRST RECEIVE POINTER
003566' 016703 001442      MOV    #PCNT,R3          ;RECEIVE PACKET COUNT
003572' 016100 000004      1S:   MOV    .STAT(R1),R0      ;GET TRANSMIT STATUS
003576' 042700 016377      BIC    #16377,R0          ;CLEAR THE LOWER BYTE
003602' 020027 020400      CMP    R0,#.ENP+.MATCH      ;SHOULD BE JUST END OF PACKET
003606' 001432      BEQ    ZS          ;CONTINUE TESTING IF EXACT
003610'      PRINT    <TRANSMIT PACKET STATUS ERROR>
003610'      MSG    64S
003610' 104401 000000' 003620'  MSGS,BEGIN,64S      ;ASCII MESSAGE CALL
003616' 000417      BR    65S
003620' 045      .ASCII  '%'
003621' 124 060562 071556      .ASCIIZ  /TRANSMIT PACKET STATUS ERROR/
003626' 064555 020164 060520
003634' 065543 072145 051440
003642' 060564 072564 020163
003650' 071105 067562 000162

; EVEN
003656'      65S:   SOPER    ERRPRT <TRANSMIT PACKET STATUS ERROR>
003656'      JSR    PC,ERRMSG      ;SET UP ERROR PRINTOUT
003656' 004767 001156      .IF B <ERRPRT>
001      SOPERs,BEGIN,NULL      ;TRANSMIT PACKET STATUS ERROR
003662' 104406 000000' 005762' .IFF
000      SOPERs,BEGIN,ERRPRT      ;TRANSMIT PACKET STATUS ERROR
000      .ENDC
003670' 000261      SEC          ;SET THE ERROR FLAG
003672' 000447      BR    4S          ;AND EXIT ROUTINE
003674' 016200 000004      2S:   MOV    .STAT(R2),R0      ;GET RECEIVE STATUS
003700' 042700 000377      BIC    #377,R0          ;CLEAR LOWER BYTE
003704' 020027 045400      CMP    R0,#.ERRS+.CMC+.STP+.ENP;EXPECT A CMC ERROR
003710' 001432      BEQ    JS          ;CONTINUE PROCESSING IF EXACT MATCH
003712'      PRINT    <RECEIVE PACKET STATUS ERROR>
003712'      MSG    66S
003712' 104401 000000' 003722'  MSGS,BEGIN,66S      ;ASCII MESSAGE CALL
003720' 000417      BR    67S
003722' 045      .ASCII  '%'
003723' 122 061545 064545      .ASCIIZ  /RECEIVE PACKET STATUS ERROR/
003730' 062566 050040 061541
003736' 062553 020164 072123
003744' 072141 071565 042440
003752' 071162 071157 000

; EVEN
003760'      67S:   SOPER    ERRPRT <RECEIVER PACKET STATUS ERROR>
003760'      JSR    PC,ERRMSG      ;SET UP ERROR PRINTOUT
003760' 004767 001054

```

SEQ 29

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```

001      .IF B <ERRPRT>
003764' 104406 000000' 005762' SOPERs,BEGIN,NULL      ;RECEIVER PACKET STATUS ERROR
000      .IFF
003772' 000261      SOPERs,BEGIN,ERRPRT      ;RECEIVER PACKET STATUS ERROR
003774' 000406      .ENDC
003776' 062701 000024      SEC          ;SET THE ERROR FLAG
004002' 062702 000016      3S:   BR    4S          ;AND EXIT ROUTINE
004006' 005303      ADD    #.TELEN*4,R1      ;ADVANCE THE TRANSMIT POINTER
004010' 001270      ADD    #.RELEN*2,R2      ;ADVANCE THE RECEIVE POINTER
004012' 000207      DEC    R3          ;DECREMENT THE PACKET COUNT
000207      RNE    1S          ;LOOP TILL ALL DONE
000207      RTS    PC          ;AND RETURN

```

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```

;--
;
; CHECK THE BYTE COUNTS
;
; ON RETURN
;   C = 0 - SUCCESS
;   C = 1 - FAILURE - COMMAND TIMEOUT
;--
004014' 004767 001124      CHKBCI: JSR   PC,SAVREG      ;SAVE ALL REGISTERS
004020' 012701 005546'      MOV   #RDB,R1      ;START OF RECEIVER RING DECS.
004024' 016700 001204      MOV   RPCNT,R0      ;PACKET COUNTER
004030' 026161 000000 000006 1S:  CMP   .LEN(R1),.LEN(R1)    ;COMPARE EXPECTED BYTE COUNT WITH RECEIVED
004036' 001424      BEQ   2S      ;OK IF THE SAME
004040'      PRINT  <BYTE COUNT ERROR>      ;DISPLAY THE MESSAGE
004040'      MSG   64S
004040' 104401 000000' 004050'      MSG$,BEGIN,64S      ;ASCII MESSAGE CALL
004046' 000411      RR   65S
004050' 045      .ASCII  '%'
004051' 102 072171 020145      .ASCIIZ /BYTE COUNT ERROR/
004056' 067543 067165 020164
004064' 071145 067562 000162      .EVEN
004072'      65S:
004072'      SOFER  ERRPRT <BYTE COUNT ERROR>
004072' 004767 000742      JSR   PC,ERRMSG      ;SET UP ERROR PRINTOUT
004072' 001      .IF B <ERRPRT>
004076' 104406 000000' 005762'      SOFERS,BEGIN,NULL      ;BYTE COUNT ERROR
004076' 000      .IFF
004104' 000261      SOFERS,BEGIN,ERRPRT      ;BYTE COUNT ERROR
004106' 000404      .ENDC
004110' 062701 000016      2S:  SEC
004114' 005300      BR   3S      ;SET THE ERROR FLAG
004116' 001344      ADD   #.LEN*2,R1      ;AND TAKE THE ERROR EXIT
004120' 000207      DEC   R0      ;ADVANCE RECEIVER POINTER
004120'      BNE  1S      ;DECREMENT THE PACKET COUNTER
004120'      RTS   PC      ;LOOP TILL ALL TESTED
004120'      ;RETURN

```

SEQ 31

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```

;--
;
; CHECK THE RECEIVED PACKET CRC PATTERNS
;
; ON RETURN
;   C = 0 - SUCCESS
;   C = 1 - FAILURE - COMMAND TIMEOUT
;--
004122' 004767 001016      CHKCRC: JSR   PC,SAVREG      ;SAVE ALL REGISTERS
004126' 012702 005546'      MOV   #RDB,R2      ;START OF RECEIVER RING DECS.
004132' 016701 001076      MOV   RPCNT,R1      ;NUMBER OF RECEIVE PACKETS
004136' 016200 000010      1S:  MOV   .VA(R2),R0      ;GET STARTING ADDRESS OF PACKET
004142' 066200 000006      ADD   .LEN(R2),R0      ;ADD RECEIVED PACKET LENGTH
004146' 162700 000004      SUB   #4,R0      ;BACK OFF FOR CRC
004152' 026220 000012      CMP   .CRCL(R2),(R0)+      ;COMPARE THE ACTUAL WITH EXPECTED
004156' 001003      HNE  2S      ;AND EXIT IF BAD
004160' 026220 000014      CMP   .CRCH(R2),(R0)+      ;COMPARE THE ACTUAL WITH EXPECTED
004164' 001426      BEQ   3S      ;CONTINUE PROCESSING IF OK
004166'      2S:  PRINT  <CRC COMPARISON ERROR>
004166'      MSG   64S
004166' 104401 000000' 004176'      MSG$,BEGIN,64S      ;ASCII MESSAGE CALL
004174' 000413      RR   65S
004176' 045      .ASCII  '%'
004177' 103 041522 061440      .ASCIIZ /CRC COMPARISON ERROR/
004204' 066557 060560 071562
004212' 067551 020156 071145
004220' 067562 000162      .EVEN
004224'      65S:
004224'      SOFER  ERRPRT <CRC COMPARISON ERROR>
004224' 004767 000610      JSR   PC,ERRMSG      ;SET UP ERROR PRINTOUT
004224' 001      .IF B <ERRPRT>
004230' 104406 000000' 005762'      SOFERS,BEGIN,NULL      ;CRC COMPARISON ERROR
004230' 000      .IFF
004236' 000261      SOFERS,BEGIN,ERRPRT      ;CRC COMPARISON ERROR
004240' 000404      .ENDC
004242' 062702 000016      3S:  SEC
004246' 005301      BR   4S      ;SET THE ERROR FLAG
004250' 001332      ADD   #.LEN*2,R2      ;AND EXIT ROUTINE
004252' 000207      DEC   R1      ;ADVANCE RECEIVER POINTER
004252'      BNE  1S      ;DECREMENT PACKET COUNT
004252'      RTS   PC      ;LOOP TILL ALL DONE
004252'      ;RETURN

```

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```

;--
;
; TRANSMIT RING SET UP ROUTINE
;
;--
004254' 004767 000664      TSETUP: JSR    PC,SAVEREG      ;SAVE ALL REGISTERS
004260' 005067 000742      CLR     IPCNT          ;NUMBER OF PACKETS GENERATED
004264' 012704 002000      MOV     #BDSIZ,R4      ;MAX NUMBER OF RECEIVER BYTES
004270' 012703 005236'      MOV     #IDR8,R3      ;TRANSMIT DESCRIPTOR RING ENTRY
004274' 012763 000022      000000 1S:  MOV     #18,,SLEN(R3)    ;SET THE SIZE OF THE HEADER
004302' 012700 006052'      MOV     #HEADER,R0      ;GET VIRTUAL ADDRESS
004306' 010063 000010      MOV     R0,,VA(R3)      ;SET THE VIRTUAL ADDRESS
004312' 004767 000462      JSR     PC,CVTPHY      ;CONVERT TO PHYSICAL
004316' 010063 000007      MOV     R0,,SHUF(R3)    ;AND THE STARTING ADDRESS
004322' 052701 001000      BIS     #.STP,R1      ;SET THE START OF PACKET BIT
004326' 010163 000004      MOV     R1,,STAT(R3)    ;SET THE EXTENDED ADDRESS BITS <16-17>
004332' 005063 000006      CLR     #IDR8,R3      ;AND CLEAR THE TDR ENTRY
004336' 062703 000012      ADD     #.TELEN*2,R3      ;POINT TO NEXT PACKET
004342'                                RAND          ;CALCULATE A PACKET SIZE
004342' 104417 000000'      RANDB,BEGIN
004346' 016702 173502      MOV     RANDB,R2      ;GET THE RAW NUMBER
004352' 042702 177001      BIC     #C776,R2      ;KEEP LESS THAN 512. AND EVEN
004356' 062702 000144      ADD     #100,,R2      ;FORCE A LOWER LIMIT
004362' 162704 000026      SUB     #22,,R4      ;SUBTRACT THE HEADER & CRC
004366' 020204      CMP     R2,R4      ;COMPARE WITH WHATS STILL AVAILABLE
004370' 002401      BLT     ZS      ;AND CONTINUE IF LESS THAN
004372' 010402      MOV     R4,R2      ;FORCE TO WHATS AVAILABLE
004374' 010263 000000      2S:  MOV     R2,,SLEN(R3)    ;AND SET IN BYTE COUNT LOCATION
004400'                                RAND          ;GET A RANDOM NUMBER
004400' 104417 000000'      RANDB,BEGIN
004404' 016700 173444      MOV     RANDB,R0      ;TO FORM THE STARTING ADDRESS
004410' 042700 177401      BIC     #C376,R0      ;KEEP THE NUMBER SMALL AND EVEN
004414' 062700 000252'      ADD     #START,R0      ;FORM THE STARTING ADDRESS
004420' 010063 000010      MOV     R0,,VA(R3)      ;SAVE THE VIRTUAL ADDRESS
004424' 004767 000350      JSR     PC,CVTPHY      ;CONVERT TO A PHYSICAL ADDRESS
004430' 010063 000002      MOV     R0,,SHUF(R3)    ;AND PACK IN MEMORY
004434' 052701 000400      BIS     #.ENP,R1      ;SET THE END OF PACKET BIT
004440' 010163 000004      MOV     R1,,STAT(R3)    ;AND PACK IN RING STRUCTURE
004444' 005063 000006      CLR     #IDR8,R3      ;CLEAR THE TRANSMIT TDR
004450' 062703 000012      ADD     #.TELEN*2,R3      ;POINT TO NEXT PACKET
004454' 005267 000546      INC     IPCNT          ;COUNT THE PACKETS
004460' 026727 000542      000012      CMP     IPCNT,#MAXPRI    ;COMPARE WITH MAX NUMBER OF ENTRIES
004466' 001404      BEQ     JS      ;AND EXIT IF EQUAL
004470' 160204      SUB     R2,R4      ;SUBTRACT LENGTH OF THIS PACKET
004472' 020427 000310      CMP     R4,#200      ;ENOUGH TO DO ANOTHER
004476' 003276      BGT     JS      ;IFUP - KEEP GOING
004500' 016767 000522      000526 3S:  MOV     IPCNT,IPCNT      ;COPY INTO RECEIVER PACKET COUNTER
004506' 006367 000514      ASL     IPCNT          ;MULTIPLY BY TWO
004512' 000207      RTS     PC      ;RETURN

```

SEQ 33

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```

;--
;
; RECEIVE RING SET UP ROUTINE
;
;--
004514' 004767 000424      RSETUP: JSR     PC,SAVEREG      ;SAVE ALL REGISTERS
004520' 016703 000510      MOV     RPCNT,R3      ;SET THE PACKET COUNT
004524' 012704 005250'      MOV     #IDR8+<.TELEN*2>,R4    ;TRANSMIT DESCRIPTOR RING ENTRY
004530' 012705 005546'      MOV     #IDR8,R5      ;RECEIVE DESCRIPTOR RING ENTRY
004534' 012700 006116'      MOV     #RECBUF,R0      ;RECEIVE BUFFER
004540' 010065 000010      1S:  MOV     R0,,VA(R5)      ;SAVE VIRTUAL ADDRESS
004544' 016401 000010      MOV     #VA(R4),R1      ;GET START OF TRANSMIT DATA BUFFER
004550' 016402 000000      MOV     #SLEN(R4),R2      ;GET THE TRANSMIT BUFFER SIZE
004554' 010346      MOV     R3,--(SP)      ;SAVE R3
004556' 010446      MOV     R4,--(SP)      ;SAVE R4
004560' 016703 001262      MOV     HUNCRC,R3      ;GET HEADER CRC VALUE - HIGH WORD
004564' 016704 001260      MOV     HUNCRC+2,R4      ;GET HEADER CRC VALUE - LOW WORD
004570' 004767 000100      JSR     PC,BLACRC      ;PERFORM THE CALCULATION
004574' 005103      CMP     R3      ;INVERT THE DATA PATTERN - HIGH WORD
004576' 005104      CMA     R4      ;INVERT THE DATA PATTERN - LOW WORD
004600' 010465 000012      MOV     R4,,CHCRC(R5)    ;SAVE EXPECTED CRC - LOW WORD
004604' 010365 000014      MOV     R3,,CHCRC(R5)    ;SAVE EXPECTED CRC - HIGH WORD
004610' 012604      MOV     (SP)+,R4      ;RESTORE R4
004612' 012603      MOV     (SP)+,R3      ;RESTORE R3
004614' 062702 000026      ADD     #22,,R2      ;ACCOUNT FOR THE EXPECTED CRC & HEADER
004620' 010265 000000      MOV     R2,,SLEN(R5)    ;AND SET IN RING BUFFER
004624' 016500 000010      MOV     #VA(R5),R0      ;GET VIRTUAL ADDRESS
004630' 004767 000144      JSR     PC,CVTPHY      ;CONVERT TO PHYSICAL ADDRESS
004634' 010065 000002      MOV     R0,,SHUF(R5)    ;SET THE RECEIVE BUFFER ADDRESS
004640' 010165 000004      MOV     R1,,STAT(R5)    ;SET THE EXTENDED ADDRESS BITS <16-17>
004644' 005065 000006      CLR     #SLEN(R5)      ;CLEAR THE RECEIVE MESSAGE LENGTH
004650' 016500 000010      MOV     #VA(R5),R0      ;GET STARTING VIRTUAL ADDRESS
004654' 060200      ADD     R2,R0      ;ADD SIZE OF BUFFER
004656' 062704 000024      ADD     #.TELEN*4,R4      ;POINT TO NEXT TRANSMIT RING
004662' 062705 000016      ADD     #.RELEN*2,R5      ;POINT TO NEXT RECEIVER RING
004666' 005303      DEC     R3      ;COUNT THE NUMBER TO DO
004670' 001323      BNE     JS      ;AND LOOP TILL DONE
004672' 000207      RTS     PC      ;RETURN

```

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```

;--
;
; CRC CALCULATION ON A BLOCK OF DATA
;
; ON CALL
;   R1 = ADDRESS OF DATA BLOCK
;   R2 = BYTE COUNT
;   R3,R4 = INITIAL CRC
;
; ON RETURN
;   R3,R4 = CRC CODE
;--
;
004674' 010046 BLKCR: MOV R0,-(SP) ;SAVE R0
004676' 010146 MOV R1,-(SP) ;SAVE R1
004700' 010246 MOV R2,-(SP) ;SAVE R2
004702' 112100 1S: MOVR (R1)+,R0 ;GET NEXT BYTE
004704' 004767 000012 JSR PC,GETCRC ;CALCULATE THE CRC
004710' 077204 2S: SOB R2,1S ;LOOP TILL DONE
004712' 012602 MOV (SP)+,R2 ;RESTORE R2
004714' 012601 MOV (SP)+,R1 ;RESTORE R1
004716' 012600 MOV (SP)+,R0 ;RESTORE R0
004720' 000207 RTS PC ;AND RETURN
;
;
; BYTE WISE 32-BIT CRC CALCULATOR
;
; ON CALL
;   R0 = NEW BYTE TO ADD TO CRC
;   R3,R4 = CURRENT PARTIAL CRC CODE
;
; ON RETURN
;   R3,R4 = UPDATED CRC
;--
;
004722' 010046 GETCRC: MOV R0,-(SP) ;SAVE R0
004724' 010146 MOV R1,-(SP) ;SAVE R1
004726' 010246 MOV R2,-(SP) ;SAVE R2
004730' 042700 177400 BIC #*C377,R0 ;CLEAR HIGH BYTE
004734' 074004 XOR R0,R4 ;MERGE NEW BYTE WITH OLD CRC
004736' 012701 166670 MOV #POLYH,R1 ;GET CRC POLYNOMIAL HIGH WORD
004742' 012702 101440 MOV #POLYLO,R2 ;GET CRC POLYNOMIAL LOW WORD
004746' 012700 000010 MOV #8,,R0 ;LOOP COUNT
004752' 000241 1S: CLC ;CLEAR THE CARRY
004754' 006003 ROR R3 ;SHIFT RIGHT THE CRC
004756' 006004 ROR R4 ;32 BITS WORTH
004760' 103002 BCC 2S ;SKIP IF BIT 0 NOT SET
004762' 074103 XOR R1,R3 ;EXCLUSIVE OR IN THE POLY
004764' 074204 XOR R2,R4 ;BOTH HIGH AND LOW WORDS
004766' 077007 2S: SOB R0,1S ;AND LOOP ON ALL 8 BITS
004770' 012602 MOV (SP)+,R2 ;RESTORE R2
004772' 012601 MOV (SP)+,R1 ;RESTORE R1
004774' 012600 MOV (SP)+,R0 ;RESTORE R0
004776' 000207 RTS PC ;AND RETURN

```

SEQ 35

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```

;--
;
; CONVERT 16 BIT VIRTUAL ADDRESS TO 18 BIT PHYSICAL ADDRESS
;
; ON CALL
;   R0 = 16 BIT VIRTUAL ADDRESS
;
; ON RETURN
;   R0 = 16 LOWER BITS OF PHYSICAL ADDRESS
;   R1 = 2 UPPER BITS OF PHYSICAL ADDRESS
;--
;
005000' 010067 173120 CVTPHY: MOV R0,RBUFVA ;SAVE IN VIRTUAL ADDRESS LOCATION
005004' 005004 GETPA RBUFVA ;CONVERT TO PHYSICAL
005004' 104415 000000' 000124' GETPAS,BEGIN, RBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
005012' 016700 173110 MOV RBUFPA,R0 ;GET PHYSICAL ADDRESS
005016' 016701 173106 MOV RBUFPA,R1 ;GET EXTENDED ADDRESS
005022' 006201 ASR R1 ;SHIFT INTO LOWER TWO BITS
005024' 006201 ASR R1 ; BIT 2
005026' 006201 ASR R1 ; BIT 1
005030' 006201 ASR R1 ; BIT 0
005032' 042701 177774 BIC #*C3,R1 ;KEEP ONLY TWO BITS
005036' 000207 RTS PC ;AND RETURN
;
;
; SET UP EXTENDED ERROR PRINTOUT
;
;--
;
005040' 004767 000100 ERRMSG: JSR PC,SAVREG ;SAVE ALL OF THE REGISTERS ;CC
005044' 016704 172736 MOV ADDR,R4 ;GET ADDR OF PCSR0 ;CC
005050' 010467 173024 MOV R4,CSRA ;LOAD ADDRESS OF PCSR0 ;CC
005054' 016767 000764 173020 MOV FLAGS,ACSR ;LOAD CONTENTS OF PCSR0 ;CC
005062' 016467 000002 173014 MOV .PCSR1(R4),ASTAT ;LOAD CONTENTS OF PCSR1 ;CC
005070' 012767 000000 173010 MOV #0,,ERRTYP ;SET ERROR TYPE UNDEFINED ;CC
;
;
005076' 012702 005236' MOV #IDRB,R2 ;GET ADDRESS OF XMIT DESC ;CC
005102' 012703 005762' MOV #ERRRPT,R3 ;GET ADDRESS OF ERROR PRINTOUT ;CC
005106' 012705 000005 MOV #TELEN,R5 ;GET SIZE OF XMIT DESC RING ;CC
005112' 012223 1S: MOV (R2)+,(R3)+ ;CREATE TABLE ;CC
005114' 005305 DEC R5 ;DECREMENT THE COUNTER ;CC
005116' 001375 BNE 1S ;LOOP UNTIL END OF XMIT DESC RING ;CC
005120' 012705 000007 MOV #RELEN,R5 ;GET SIZE OF REC. DESC RING ;CC
005124' 012702 005546' MOV #IDRB,R2 ;GET ADDRESS OF RECEIVE DESC RING ;CC
005130' 012223 2S: MOV (R2)+,(R3)+ ;ADD RECEIVE RING TO TABLE ;CC
005132' 005305 DEC R5 ;DECREMENT TABLE ENTRY COUNTER ;CC
005134' 001375 BNE 2S ;LOOP UNTIL END OF RECEIVE RING ;CC
005136' 012723 177777 MOV #177777,(R3)+ ;TABLE TERMINATOR ;CC
005142' 000207 RTS PC ;AND RETURN ;CC
;
;
; SAVE REGISTERS R0 - R5
;
;--
;
005144' 010146 SAVREG: MOV R1,-(SP) ;SAVE R1
005146' 010246 MOV R2,-(SP) ;SAVE R2
005150' 010346 MOV R3,-(SP) ;SAVE R3
005152' 010446 MOV R4,-(SP) ;SAVE R4

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005154' 010546      MOV    R5, -(SP)      ;SAVE R5
005156' 012746 005174' MOV    *RSTREG, -(SP)    ;FIX A RESTORE REGISTERS ADDRESS
005162' 016646 000014' MOV    14(SP), -(SP)    ;GET THE RETURN ADDRESS
005166' 010066 177762' MOV    R0, -16(SP)    ;SAVE R0
005172' 000207      RIS    PC              ;AND RETURN

;--
;
;   RESTORE REGISTERS R0 - R5
;
;--
005174' 012605  RSTREG: MOV    (SP)+, R5      ;RESTORE R5
005176' 012604      MOV    (SP)+, R4      ;RESTORE R4
005200' 012603      MOV    (SP)+, R3      ;RESTORE R3
005202' 012602      MOV    (SP)+, R2      ;RESTORE R2
005204' 012601      MOV    (SP)+, R1      ;RESTORE R1
005206' 012600      MOV    (SP)+, R0      ;RESTORE R0
005210' 000207      RTS    PC              ;AND RETURN

```

SEQ 37

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```

005212' 000000 000000 000000 PCH:  .WORD  0,0,0,0      ;PORT CONTROL BLOCK
005220' 000000

005222' 000000      RNGTHLK: .WORD  0      ;ADDRESS OF TRANSMIT DESCRIPTOR RING BASE
005224' 000      .BYTE  0      ;EXTENDED ADDRESS BITS <16-17>
005225' 000      .BYTE  0      ;NUMBER OF WORDS IN TDRB
005226' 000000      TPCNT:  .WORD  0      ;NUMBER OF ENTRIES IN RING

005230' 000000      .WORD  0      ;ADDRESS OF RECEIVE DESCRIPTOR RING BASE
005232' 000      .BYTE  0      ;EXTENDED ADDRESS BITS <16-17>
005233' 000      .BYTE  0      ;NUMBER OF WORDS IN EACH ENTRY
005234' 000000      RPCNT:  .WORD  0      ;NUMBER OF ENTRIES IN RECEIVE RING

005236' 000144      TDRB:  .BLKW  .TELEN*, MAXPKT*2    ;TRANSMIT DECEIPTION RING ENTRIES
005246' 000106      RDRB:  .BLKW  .RELEN*, MAXPKT    ;RECEIVE DESCRIPTOR RING ENTRIES

005762' 000031      ERRPRI: .BLKW  25.      ;ERROR PRINTOUT ENTRIES      ;CC

006044' 000000      FLAGS:  .WORD  0      ;INTERRUPT FLAG WORD
006046' 000000 000000      HDPCRC: .WORD  0,0
006052' 000022      HEADER: .BLKW  14.
006116' 002000      RECBUF: .BLKB  BUFSIZ
      .END

```


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CROSS REFERENCE TABLE -- MACRO NAMES

HKMOD	1#																	
AREAK	1#	632	955	1051	1077													
HTOD	1#																	
CKUATA	1#																	
DATAACK	1#																	
DATEHP	1#	1230																
DEMAND	269#	662	721	752	777	828	853	924										
DEFVMT	1#	537	540	543	546	549	552	555	558	561	564	567	570					
DEFVMT	1#	578	581	584	587	590	593											
END	1#	900																
ENDIF	1#	992	997															
ENDMOD	1#																	
EQUATS	1#	482																
EXIT	1#																	
GETPA	1#	1550																
GRUFF	1#																	
HRDER	1#	622	650	668	690	727	758	783	834	859	881	930	970	1106	1129			
	1150	1170	1190															
IOMOD	1#																	
IOMODP	1#																	
IOMODR	1#																	
IOMODX	1#	377																
MAP22	1#																	
MODULE	1#	378																
MSG	1#	606	638	666	688	725	750	781	832	857	879	928	961	1096	1116			
	1139	1160	1180	1266	1291	1331	1373											
MSGN	1#																	
MSGS	1#																	
HBKMOD	1#																	
OTUA	1#																	
PIRQ	1#																	
PRINT	261#	607	637	960	1095	1115	1138	1159	1179	1265	1290	1330	1372					
RAND	1#	1414	1424															
SHKMOD	1#																	
SUPER	1#	1277	1302	1340	1383													
WAIT	281#	684	875															

.ARS. 000000 000
010116 001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

CXUACA.UHJ,CXUACA.LST/CRF/SOL/HL:TOC=DDXCOM,P11,CXUACA.MAC
RUN-TIME: 2 3 .5 SECONDS
RUN-TIME RATIO: 88/7=12.6

DVID1: 1 BIT SET FOR EACH DH11 LINE. ITS POSITION SHOULD CORRESPOND WITH THE LINE #. E.G. IF DH11 LINE 6 IS USED "DVID1" BIT 6 SHOULD BE SET.

6. DEVICE SETUP

A. THE USER MUST LOAD AND START TEST 21 OF MAINDEC-11-DZVTA IN THE VT20 PDP11/05 IN ORDER FOR THIS MODULE TO EXERCISE. CONSULT THE ABOVE DOCUMENT AND COMPLY WITH THE OPERATING INSTRUCTIONS FOR TEST 21 (SECTION 26). THIS DEC/X11 MODULE EXPECTS THE USER TO ENTER DATA ON EACH SELECTED TUBE AND SET EACH TUBE IN THE CONTINUOUS TRANSMIT MODE. THIS STEP IS TAKEN AFTER THE DEC/X11 EXERCISER HAS BEEN STARTED BY THE "RUN" COMMAND. TYPICAL USER ACTION ON EACH SELECTED TUBE WILL BE AS FOLLOWS:

KEY	FUNCTION
CTRL E	CLEAR SCREEN
CTRL W	GENERATE WORST CASE CHARACTER PATTERN ON TOP OF SCREEN
CTRL T	CONTINUOUS TRANSMIT TO DEC/X11 MODULE (DEC/X11 MODULE WILL RECEIVE DATA AND TRANSMIT IT BACK TO BOTTOM OF SCREEN)

NOTE: IF THE CHARACTER PATTERN FAILS TO RETURN ON THE BOTTOM OF THE SCREEN AFTER THE "CTRL T", THEN REPLY AFTER "END PASS" IS REPORTED FOR THIS DEC/X11 MODULE (DH11 RECEIVERS ARE TURNED OFF SECONDS BEFORE "END PASS" MSG). IF DATA IS STILL NOT RETURNED FROM HOST COMPUTER (DEC/X11 SYSTEM) THEN VERIFY THE VT20 HOST COMPUTER BY RUNNING MAINDEC-11-DZVTE.

B. IF LINES WITH BAUD RATES OTHER THAN 9600 ARE TO BE USED, THEN THE VALUE OF THE CORRESPONDING WORD IN THE BAUD RATE TABLE (16 WORDS STARTING AT LOC 008) MUST BE MODIFIED REFER TO THE PDP-11 PERIPHERALS AND INTERFACING HANDBOOK FOR THE EXACT VALUES NEEDED

8. OPERATOR OPTIONS

A. THE USER CAN MODIFY (VTA 14) "DVID1" TO SELECT OR DESELECT INDIVIDUAL VT20'S. THIS MODULE IS QUITE ABLE TO HANDLE VT20'S THAT DO NOT HAPPEN TO HAVE ADJACENT DH11 LINES.

B. THE USER CAN USE THE "MOD" COMMAND TO DUMP THE TABLES OR BUFFERS DESCRIBED IN 7.2 TO OBTAIN MORE DETAILED ERROR INFORMATION.

9. ERROR PRINTOUTS

9.1 ERROR FORMAT - RECEIVE

CSRA = CSR ADDRESS
 CSRC = NRC WORD AS FOLLOWS:

THE # PRINTED OUT LABELED AS "STATC" IS THE NEXT RECEIVED CHARACTER
 BIT 15 = DATA PRESENT
 BIT 14 = OVERRUN
 BIT 13 = FRAMING
 BIT 12 = PARITY
 BIT 11-8 = LINE #
 BIT 7-0 = DATA RECEIVED

WITH SOME ERRORS SUCH AS "NO DH11 LINES REMAIN SELECTED" THE CONTENTS OF THE DH11 REGISTERS ARE IRRELEVANT. IN SUCH CASES THEY ARE PRINTED ANYWAYS.

9.2 ERROR FORMAT - TRANSMIT

CSRA = CSR ADDRESS
 CSRC = CSR CONTENTS AS FOLLOWS:

BIT 7 = XMITR READY
 BIT 6 = XMITR INTERRUPT ENABLED

IDENTIFICATION

PRODUCT CODE: AC-E956B-MC
PRODUCT NAME: CXVTBB0 DH11/VT20 MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

VTB IS AN IOMODX THAT EXERCISES UP TO FOUR VT20'S (DH11 LINES). IT IS INTENDED TO BE A DATA HANDLING ROUTINE USED IN CONJUNCTION WITH TEST 21 OF MAINDEC-11-DBVTA (PREVIOUSLY LCADEC AND RUNNING IN THE VT20'S PDP11-05). DATA IS ENTERED AT EACH SELECTED TUBE AND SET INTO THE CONTINUOUS TRANSMIT MODE. THIS DATA IS THEN RECEIVED AND RETRANSMITTED BY THE VT20 HOST COMPUTER (THIS DEC/X11 MODULE). ALL LINES SELECTED FOR TEST CAN BE ACTIVATED AND RUN CONCURRENTLY. ALL TRANSMIT AND RECEIVE ERRORS ARE REPORTED ON THE CONSOLE TTY. NO DATA ERRORS ARE REPORTED BY THIS MODULE.

2. REQUIREMENTS

HARDWARE: AT LEAST ONE VT20 CONNECTED VIA A DH11
STORAGE: VTB REQUIRES:
1. DECIMAL WORDS: 4002
2. OCTAL WORDS: 07642
3. OCTAL BYTES: 17504

3. PASS DEFINITION

ONE PASS OF THE VTB MODULE CONSISTS OF CONTINUOUSLY RECEIVING AND TRANSMITTING THE DATA ENTERED ON ALL SELECTED LINES FOR THE PERIOD DEFINED BELOW.

4. EXECUTION TIME

EXECUTION TIME VARIES WITH THE NUMBER OF JOBS (MODULES) ACTIVE, THE BAUD RATE AND THE NUMBER OF TUBES BEING EXERCISED. HOWEVER, THIS MODULE RUNNING ALONE WILL TAKE NO MORE THAN 3 MINUTES WITH 16 TUBES AT 110 BAUD

5. CONFIGURATION PARAMETERS

DEFAULT PARAMETERS:

DVA:160020, VCT:350, BR1:5, BR2:0, DVC:1

LOBR-L17BR: IF ANY OF THE DH11 LINES IS NOT A 9600 BAUD LINE THE WORD ASSOCIATED WITH THAT LINE MUST BE MODIFIED BEFORE RUNNING

REQUIRED PARAMETERS:

DVC: NO OF VT20'S IF GREATER THAN 1

DVID1: 1 BIT SET FOR EACH DH11 LINE. ITS POSITION SHOULD
 CORRESPOND WITH THE LINE # E.G. IF DH11 LINE 6
 IS USED "DVID1" BIT 6 SHOULD BE SET.

6. DEVICE SETUP

A. THE USER MUST LOAD AND START TEST 21 OF MAINDEC-11-D2VTA IN THE VT20
 PDP11/05 IN ORDER FOR THIS MODULE TO EXERCISE. CONSULT THE ABOVE
 DOCUMENT AND COMPLY WITH THE OPERATING INSTRUCTIONS FOR TEST 21
 (SECTION 26). THIS DEC/X11 MODULE EXPECTS THE USER TO ENTER DATA
 ON EACH SELECTED TUBE AND SET EACH TUBE IN THE CONTINUOUS
 TRANSMIT MODE. THIS STEP IS TAKEN AFTER THE DEC/X11 EXERCISER
 HAS BEEN STARTED BY THE "RUN" COMMAND. TYPICAL USER ACTION
 ON EACH SELECTED TUBE WILL BE AS FOLLOWS:

KEY	FUNCTION
CTRL E	CLEAR SCREEN
CTRL H	GENERATE HOST CASE CHARACTER PATTERN ON TOP OF SCREEN
CTRL T	CONTINUOUS TRANSMIT TO DEC/X11 MODULE (DEC/X11 MODULE WILL RECEIVE DATA AND TRANSMIT IT BACK TO BOTTOM OF SCREEN)

NOTE: IF THE CHARACTER PATTERN FAILS TO RETURN ON THE
 BOTTOM OF THE SCREEN AFTER ONE "CTRL T", THEN RETRY AFTER
 "END PASS" IS REPORTED FOR THIS DEC/X11 MODULE (DH11 RECEIVERS
 ARE TURNED OFF SECONDS BEFORE "END PASS" MSG). IF DATA IS STILL
 NOT RETURNED FROM HOST COMPUTER (DEC/X11 SYSTEM) THEN VERIFY
 THE VT20 HOST COMPUTER BY RUNNING MAINDEC-11-D2VTE.

B. IF LINES WITH BAUD RATES OTHER THAN 9600 ARE TO BE USED, THEN THE
 VALUE OF THE CORRESPONDING WORD IN THE BAUD RATE TABLE (16
 WORDS STARTING AT LOC "LOBR") MUST BE MODIFIED REFER TO THE
 PDP-11 PERIPHERALS AND INTERFACING HANDBOOK FOR THE EXACT
 VALUES NEEDED

8. OPERATOR OPTIONS

A. THE USER CAN MODIFY (VTA 14) "DVID1" TO SELECT OR
 DESELECT INDIVIDUAL VT20'S. THIS MODULE IS QUITE
 ABLE TO HANDLE VT20'S THAT DO NOT HAPPEN TO HAVE ADJACENT
 DH11 LINES.

B. THE USER CAN USE THE "MOD" COMMAND TO DUMP THE TABLES
 OR BUFFERS DESCRIBED IN 7.2 TO OBTAIN MORE DETAILED
 ERROR INFORMATION.

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9.1 ERROR FORMAT - RECEIVE

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 CSRC = NRC WORD AS FOLLOWS:

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BIT 15	= DATA PRESENT
BIT 14	= OVERRUN
BIT 13	= FRAMING
BIT 12	= PARITY
BIT 11-8	= LINE #
BIT 7-0	= DATA RECEIVED

WITH SOME ERRORS SUCH AS "NO DH11 LINES REMAIN SELECTED"
 THE CONTENTS OF THE DH11 REGISTERS ARE IRRELEVANT.
 IN SUCH CASES THEY ARE PRINTED ANYWAYS.

9.2 ERROR FORMAT - TRANSMIT

CSRA = CSR ADDRESS
 CSRC = CSR CONTENTS AS FOLLOWS:

BIT 7	= XMITR READY
BIT 6	= XMITR INTERRUPT ENABLED

SEQ 0006

[illegible]

[illegible][illegible][illegible][illegible]

TABLE OF BAUD RATES FOR DH11 LINES										
L0BR:	0335500	1	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 0
L1BR:	0335500	2	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 1
L2BR:	0335500	3	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 2
L3BR:	0335500	4	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 3
L4BR:	0335500	5	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 4
L5BR:	0335500	6	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 5
L6BR:	0335500	7	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 6
L7BR:	0335500	8	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 7
L8BR:	0335500	9	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 8
L9BR:	0335500	10	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 9
L10BR:	0335500	11	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 10
L11BR:	0335500	12	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 11
L12BR:	0335500	13	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 12
L13BR:	0335500	14	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 13
L14BR:	0335500	15	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 14
L15BR:	0335500	16	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 15
L16BR:	0335500	17	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 16
L17BR:	0335500	18	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 17
L18BR:	0335500	19	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 18
L19BR:	0335500	20	DEF	FAULT	BAUD	RATE	(9600)	FOR	DH11	LINE 19

[illegible]

57	00000000	00000000
58	00000000	00000000
59	00000000	00000000
60	00000000	00000000
61	00000000	00000000
62	00000000	00000000
63	00000000	00000000
64	00000000	00000000
65	00000000	00000000
66	00000000	00000000
67	00000000	00000000
68	00000000	00000000
69	00000000	00000000
70	00000000	00000000
71	00000000	00000000
72	00000000	00000000
73	00000000	00000000
74	00000000	00000000
75	00000000	00000000
76	00000000	00000000
77	00000000	00000000
78	00000000	00000000
79	00000000	00000000
80	00000000	00000000

[illegible][illegible]

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;DTERMINE IF ANY DH-'S ARE SELECTED. IF SO SETUP DEVICE REGISTER
;ADDRESSES, INTERRUPT VECTORS, AND START THE
;MODULE PROCESSING... IF NOT, DROP THE MODULE FROM THE RUN
START:  MOV     $4,INTPR      ;4 INTERRUPTS / ITERATION
        MOV     $2,INTPR    ;20 WORDS TO NEW ITERATION
        MOV     $2,INTPR    ;20 WORDS TO NEW ITERATION
        MOV     $004400, $ADDF ;INIT THE DR11
        MOV     $4, WAITPR   ;MAKE A LONG 1ST PASS TO ALLOW FOR STARTING VT20S
        MOV     $0,INT,SP    ;CHECK THE MODULE STACK
        MOV     $0,INT,SP    ;CHECK FOR SELECTED VT20-'S
        BNE     $0,INT,SP    ;ANY SELECTED?
;WE END UP AT "DROP" IF NO VT20
;IF ALL "NO" DROP THIS MODULE
DROP:   MOV     $0,INT,SP     ;SETUP ADDRESS OF THE DR11 CTRL/STATUS REG
        MOV     $0000, $0000 ;RC: PREVENT DR11 INTERRUPTS
        MSHCS, BEGIN, ERRMS  ;ASCII MESSAGE CALL WITH COMMON HEADER

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396					
397	000664	016767	177156	177502	
398	000664	014777	004000	017474	
399	000664	104400	000000	017474	
400	000664	104410	000000		
401					
402					
403					
404	000652	115767	177537	177360	
405	000660	005267	177344	177352	
406	000666	005267	177346		
407	000672	006167	177342		
408	000676	006167	177336		
409	000702	006167	177328		
410	000706	006167	177326		
411	000712	005467	177322		
412	000716	005000			

[illegible]


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413 000720 012767 000003 177324 MOV #3, WAIT1M ;REGULAR PASS TIMEOUT
414 000720 012767 000003 177324 CLR #0 ;INIT WORD WE ARE GOING TO SENT TO X12
415 000720 012767 000003 177324 DUMCNT ;CLEAR OUT COUNT FOR # OF LINES
416 000720 012767 000003 177324 CMPEB ;LINE READY TO PRINT?
417 000720 012767 000003 177324 LOEC(0), #4 ;IF NOT GO SEE ABOUT NEXT LINE
418 000720 012767 000003 177324 BNE #3, LOEC(0) ;IF IT IS READY SET IT TO ACTIVE MODE
419 000720 012767 000003 177324 SEC ;CREATE IT BIT AND
420 000720 012767 000003 177324 ROR ;CREATE IT BIT NEW "BAR" REGISTER
421 000720 012767 000003 177324 DEC DUMCNT ;ADD 1 TO LINE COUNT
422 000720 012767 000003 177324 ROR ;CLEAR THE LINE #
423 000720 012767 000003 177324 MOV #17760, TEMP ;INDICE IT TO MAKE IT REAL
424 000720 012767 000003 177324 BVC #17760, TEMP ;LINE SURE ONLY VIRTAC IS SET
425 000720 012767 000003 177324 MOV #0, TEMP ;PUT IT INTO THE STATUS REGISTER
426 000720 012767 000003 177324 LOEC(0), TEMP ;NOW GET THE CHARACTER COUNT
427 000720 012767 000003 177324 NEG ;USE IT TO GET COMPLEMENT
428 000720 012767 000003 177324 MOV #0, TEMP ;AND TEST THE BYTE COUNT REGISTER WITH IT
429 000720 012767 000003 177324 LOS(0), VIRTAC ;GET VIRTUAL ADDRESS OF THE BUFFER
430 000720 012767 000003 177324 GETPAS, BEGIN, VIRTAC ;GET PHYSICAL ADDRESS FROM 16-BIT VIRTAC
431 000720 012767 000003 177324 BVC #17777, EXTAD ;CLEAR ALL BUT THE EXTEND BITS
432 000720 012767 000003 177324 BVS #17777, EXTAD ;SET ADDRESS EXTEND BITS IN STATUS REG
433 000720 012767 000003 177324 MOV #0, TEMP ;SETUP CURRENT ADDRESS FOR THIS LINE
434 000720 012767 000003 177324 BVS #17777, EXTAD ;AND REINIT ITS CHARACTER COUNT
435 000720 012767 000003 177324 CLR #0, TEMP ;CLEAR C BIT TO
436 000720 012767 000003 177324 ROR ;MAKE SURE THAT THIS LINES "BAR" REGISTER BIT IS CLEAR
437 000720 012767 000003 177324 ADD #2, RO ;GO ON TO THE NEXT LINE
438 000720 012767 000003 177324 CMPEB ;HAVE WE DONE ALL LINES YET?
439 000720 012767 000003 177324 BNE #40 ;NO GO TO ALL LINES YET?
440 000720 012767 000003 177324 TST TEMP ;ALL DONE, ARE ANY LINES STILL ACTIVE?
441 000720 012767 000003 177324 BNE #0, TEMP ;IF SO, GO START UP UP XMITTING
442 000720 012767 000003 177324 MSGNS, BEGIN, ERRRE ;LINES REPAIR, STOP THE DB11
443 000720 012767 000003 177324 ENDS, BEGIN ;ASCII MESSAGE CALL WITH COMMON HEADER
444 000720 012767 000003 177324 MOV #0, TEMP ;AND DROP THIS MODULE
445 000720 012767 000003 177324 JMP #0, TEMP ;SET THE BAR REGISTER
446 000720 012767 000003 177324 WODROP: JSR MAIN ;GO WAIT FOR SOMETHING TO HAPPEN
447 000720 012767 000003 177324 PC, SETUP ;DO A BUNCH OF INITIALIZATION
448 000720 012767 000003 177324 ;THIS IS THE MAIN PROGRAM LOOP
449 000720 012767 000003 177324 ;NOTHING IS DONE HERE EXCEPT TO CONTINUOUSLY TEST TO SEE IF ENOUGH TIME HAS PASSED
450 000720 012767 000003 177324 ;FOR AN END OF PASS, CHECK FOR ERRORS AND CALL THE
451 000720 012767 000003 177324 ;INTERRUPT SERVICE ROUTINE TO CLEAR OUT THE LOG IN CASE IT HAS STUFF
452 000720 012767 000003 177324 ;IN IT, BUT NOT ENOUGH STUFF TO CAUSE AN INTERRUPT
453 000720 012767 000003 177324
454 000720 012767 000003 177324
455 000720 012767 000003 177324
456 000720 012767 000003 177324
457 000720 012767 000003 177324
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464 000720 012767 000003 177324
465 000720 012767 000003 177324
466 000720 012767 000003 177324
467 000720 012767 000003 177324
468 000720 012767 000003 177324
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469 001250 012167 176626 MOV (R1), ACSR ;SETUP THE CONTROL REG CONTENTS FOR PRINTING
470 001250 012167 176626 MOV (R1), ACSR ;SETUP THE STATUS REG CONTENTS FOR PRINTING
471 001250 012167 176626 MSGNS, BEGIN, ERRRE ;ASCII MESSAGE CALL WITH COMMON HEADER
472 001250 012167 176626 MOV #1, ERRRTYP ;RECEIVER ERROR
473 001250 012167 176626 BRDRS, BEGIN, NULL ;RECEIVE ERROR MSG FATAL
474 001250 012167 176626 ;*****
475 001250 012167 176626 MOV #1, ERRRT1 ;UPDATE THE ERROR QUEUE POINTER
476 001250 012167 176626 BNE #1, ERRRT2 ;IS THE ERROR QUEUE EMPTY?
477 001250 012167 176626 MOV #0, ERRRT1 ;ERROR QUEUE IS EMPTY, REINITIALIZE THE
478 001250 012167 176626 MOV #0, ERRRT2 ;ERROR QUEUE POINTERS
479 001250 012167 176626 TST ERRRT ;HAVE WE HAD ANY FATAL ERRORS?
480 001250 012167 176626 BNE #0, ERRRT ;IF SO, GO REINIT IT AND DROP THIS MODULE
481 001250 012167 176626 TST DUMCNT ;HAVE ALL ACTIVE LINES FINISHED RECEIVING?
482 001250 012167 176626 BVC #0, DUMCNT ;IF SO GO CLEAN UP AND END THIS PASS
483 001250 012167 176626 INC WAIT1M ;INC TICK GOES THE WAIT TIMER
484 001250 012167 176626 MOV #0, MLOOP ;HAVE THESE DEVICES HAD ENOUGH TIME TO FINISH?
485 001250 012167 176626 DEC WAIT1M ;HAD ENOUGH TIME TO FINISH?
486 001250 012167 176626 BNE #0, MLOOP ;IF NOT
487 001250 012167 176626 MOV #0, MLOOP ;ENOUGH TIME! PREVENT FURTHER CH11 INTERRUPTS
488 001250 012167 176626 BVC #0, MLOOP ;SETUP A LINE INDEX REGISTER TO 0
489 001250 012167 176626 MOV #0, MLOOP ;FIND OUT IF THE LINE IS SELECTED
490 001250 012167 176626 BVC #0, MLOOP ;IF NOT GO TEST FOR ERRORS ON IT
491 001250 012167 176626 MOV #0, MLOOP ;IF IT IS CHECK THAT IT HAS FINISHED RECEIVING
492 001250 012167 176626 BVC #0, MLOOP ;IF IT DID AT LEAST A TRANSFER, DON'T PRINT AN ERROR
493 001250 012167 176626 MOV #0, MLOOP ;DID IT FINISH THE LINE TO CROPPED MODE
494 001250 012167 176626 MOV #0, MLOOP ;SETUP FOR PRINTING THE ADDRESS OF THE CONTROL/STATUS REG
495 001250 012167 176626 MOV #0, MLOOP ;SETUP FOR PRINTING THE CONTENTS OF THE CONTROL/STATUS REG
496 001250 012167 176626 MOV #0, MLOOP ;SETUP TO PRINT CONTENTS OF THE NEXT CHAR RECEIVED REG
497 001250 012167 176626 MOV #0, MLOOP ;SAVE IT
498 001250 012167 176626 MOV #0, MLOOP ;*****
499 001250 012167 176626 MOV #0, MLOOP ;CONVERT NUMBA1 TO ASCII AND
500 001250 012167 176626 MOV #0, MLOOP ;STORE AT MESNMO
501 001250 012167 176626 ;*****
502 001250 012167 176626 MSGNS, BEGIN, ERRRT ;ASCII MESSAGE CALL WITH COMMON HEADER
503 001250 012167 176626 MOV #2, ERRRTYP ;FAILED TO INTERRUPT
504 001250 012167 176626 BRDRS, BEGIN, NULL ;A DB11 LINE HUNG
505 001250 012167 176626 ;*****
506 001250 012167 176626 BR 35 ;GO TRY THE NEXT LINE FOR ERRORS
507 001250 012167 176626
508 001250 012167 176626
509 001250 012167 176626
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519 001250 012167 176626
520 001250 012167 176626
521 001250 012167 176626
522 001250 012167 176626
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001536* 017405
001540* 005726
001542* 010046
001546* 000018
001550* 011667
001554* 104420
001562* 017414
001564* 005726
001566* 104403
001574* 005726
001600* 017405
001604* 017405
001606* 005367
001612* 000167
001620* 104413
001624* 016767
001626* 017405
001634* 017405
001646* 104403
001662* 012767
001670* 104405
001676* 104410

749 017156* 042040 042111 047040
750 017156* 042040 042111 047040
751 017156* 042040 042111 047040
752 017156* 042040 042111 047040
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756 017156* 042040 042111 047040
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773 017156* 042040 042111 047040
774 017156* 042040 042111 047040
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780 017156* 042040 042111 047040
781 017156* 042040 042111 047040
782 017156* 042040 042111 047040
783 017156* 042040 042111 047040
784 017156* 042040 042111 047040
785 017156* 042040 042111 047040
786 017156* 042040 042111 047040
787 017156* 042040 042111 047040
788 017156* 042040 042111 047040
789 017156* 042040 042111 047040
790 017156* 042040 042111 047040
791 017156* 042040 042111 047040
792 017156* 042040 042111 047040
793 017156* 042040 042111 047040
794 017156* 042040 042111 047040
795 017156* 042040 042111 047040
796 017156* 042040 042111 047040
797 017156* 042040 042111 047040
798 017156* 042040 042111 047040
799 017156* 042040 042111 047040
800 017156* 042040 042111 047040
801 017156* 042040 042111 047040
802 017156* 042040 042111 047040
803 017156* 042040 042111 047040
804 017156* 042040 042111 047040

MESDH1: .ASCIZ / DID NOT COMPLETE 1 TRANSFER IN TIME/
MESDH2: .ASCIZ /SO WE ARE DROPPING THE LINE/
MESUS: .ASCIZ / ERRORS ON UNSELECTED LINE/
MESSO: .ASCIZ / SILO OVERFLOW... ITS FATAL/
MESRE: .ASCIZ / A RECEIVER ERROR BIT WAS SET/

MESBE: .ASCIZ /3/
MESNM0: .ASCIZ /000000/
MESNM1: .ASCIZ /000000/
.EVEN

TABLE OF ERROR MESSAGE POINTERS & TERMINATORS (COMMENTED)
ERRMS: MESBE ;POINTS TO A CARRIAGE RETURN-LINE FEED
ERRNR: MESBE ;POINTS TO AN ASCIZ ERROR MESSAGE
ERRDH: MESBE ;POINTS TO A CARRIAGE RETURN-LINE FEED
ERRSO: MESBE ;POINTS TO A CARRIAGE RETURN-LINE FEED
ERRRE: MESBE ;POINTS TO A CARRIAGE RETURN-LINE FEED
ERRUS: MESBE ;POINTS TO A CARRIAGE RETURN-LINE FEED

001702* 032777
001712* 012777
001720* 012767
001736* 000002
001736* 000004
001744* 010067
001750* 012767
001756* 017767
001764* 100077
001774* 016700
002000* 005726
002006* 045700
002012* 001011
002018* 005726
002024* 005726
002030* 016700
002036* 104400
002042* 016700
002048* 016700
002054* 032767
002060* 014255
002066* 005726
002072* 005726
002078* 001003
002084* 014767
002090* 017777
002096* 002767
002102* 016777
002108* 005726
002114* 005726
002120* 005726
002126* 005726
002132* 005726
002138* 005726
002144* 005726
002150* 005726
002156* 016700
002162* 016700
002168* 104400
002174* 032777
002202* 000002

805 017474* 017405*
806 017476* 017256*
807 017500* 017414*
808 017502* 177777*
810 000001

MESNM0 ;POINTS TO ASCIZ FOR # OF ERRORS
MESUS ;POINTS TO TEXT PART OF MESSAGE
MESNM1 ;POINTS TO ASCIZ FOR LINE #
;END OF MESSAGE TABLE
;END ;SIGNALS END OF MESSAGE TO DECK/11

VTBB DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 12-OCT-78 17:09 PAGE 15
XVTBB0.P11 12-OCT-78 12:25

SEQ 0014

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5943 002414.0 005067 175536 CLR TEMPO 000000 ;INIT THE LINE #
5944 002420.0 005060 000454.0 CLR LOEC(0) ;ASSUME LINE IS UNSELECTED UNTIL PROVEN OTHERWISE
5945 002424.0 006001 1S: ROR R1 ;PUT A BIT INTO THE C-BIT
5946 002430.0 003693 BCC Z8 ;IS THE LINE TO BE USED ?
5947 002436.0 000274* 000354* R1,LIST(0),LOSW(0) ;YES, ASSIGN A BUFFER PCOUNTER TO THAT LINE
5948 002444.0 042777 000617 BIC #1,BIT0 ;CLEAR OUT LINE SELECTION BITS
5949 002448.0 005367 175600 DUNCNT ;ADD 1 TO COUNT OF ACTIVE DEVICES
5950 002452.0 009740 BIC NOB ;SET LINE SELECTION BITS FOR THIS LINE
5951 002456.0 016077 175600 LOBR(0,X04 ;SETUP BAD BYTES FOR THIS LINE
5952 002460.0 000414.0 175652 ROR R1 ;PARITY-FULL DUPLEX-8 BIT
5953 002464.0 052777 175644 BIC #2,BIT04 ;CLEAR OUT THIS LINE CHARACTER COUNT
5954 002468.0 003060 000504.0 LOCC(0) ;START OUT THE LINE RECEIVE WCODE
5955 002472.0 005267 000280.0 CLR WNC ;REDUCE COUNT OF SELECTED VT20'S
5956 002476.0 005267 000280.0 ;CC ON TO THE NEXT LINE
5957 002480.0 062700 000002 2S: ADD #2,NO ;NEXT LINE
5958 002484.0 002457 175534 TEMPO ;CHECKED ALL LINES FOR SELECTION YET ?
5959 002488.0 002457 175534 CNG #17 ;NOT GO BACK AND CHECK THE NEXT ONE
5960 002492.0 002457 175534 BNE PC ;RETURN
5961 002496.0 000209 RTS

7102 002434.0 000000 NUMBA1: -WORD 0
7103 002438.0 000000 NUMBA2: -WORD 0
7104 002442.0 000000 NUMBA3: -WORD 0
7105 002446.0 000000 NUMBA4: -WORD 0
7106 002450.0 000303 VTRF0: -BLKW 195. ;BUFFER SPACE FOR 1ST SELECTED CH11 LINE
7107 002454.0 000303 VTRF1: -BLKW 195. ;BUFFER SPACE FOR 2ND SELECTED CH11 LINE
7108 002458.0 000303 VTRF2: -BLKW 195. ;BUFFER SPACE FOR 3RD SELECTED CH11 LINE
7109 002462.0 000303 VTRF3: -BLKW 195. ;BUFFER SPACE FOR 4TH SELECTED CH11 LINE
7110 002466.0 000303 VTRF4: -BLKW 195. ;BUFFER SPACE FOR 5TH SELECTED CH11 LINE
7111 002470.0 000303 VTRF5: -BLKW 195. ;BUFFER SPACE FOR 6TH SELECTED CH11 LINE
7112 002474.0 000303 VTRF6: -BLKW 195. ;BUFFER SPACE FOR 7TH SELECTED CH11 LINE
7113 002478.0 000303 VTRF7: -BLKW 195. ;BUFFER SPACE FOR 8TH SELECTED CH11 LINE
7114 002482.0 000303 VTRF8: -BLKW 195. ;BUFFER SPACE FOR 9TH SELECTED CH11 LINE
7115 002486.0 000303 VTRF9: -BLKW 195. ;BUFFER SPACE FOR 10TH SELECTED CH11 LINE
7116 002490.0 000303 VTRF10: -BLKW 195. ;BUFFER SPACE FOR 11TH SELECTED CH11 LINE
7117 002494.0 000303 VTRF11: -BLKW 195. ;BUFFER SPACE FOR 12TH SELECTED CH11 LINE
7118 002498.0 000303 VTRF12: -BLKW 195. ;BUFFER SPACE FOR 13TH SELECTED CH11 LINE
7119 002502.0 000303 VTRF13: -BLKW 195. ;BUFFER SPACE FOR 14TH SELECTED CH11 LINE
7120 002506.0 000303 VTRF14: -BLKW 195. ;BUFFER SPACE FOR 15TH SELECTED CH11 LINE
7121 002510.0 000303 VTRF15: -BLKW 195. ;BUFFER SPACE FOR 16TH SELECTED CH11 LINE
7122 002514.0 000303 VTRF16: -BLKW 195. ;BUFFER SPACE FOR 17TH SELECTED CH11 LINE
7123 002518.0 000303 VTRF17: -BLKW 195. ;BUFFER SPACE FOR 18TH SELECTED CH11 LINE
7124 002522.0 000000 ERQUEU: -BLKW 60. ;RECEIVE ERROR QUEUE SPACE
7125 002526.0 000000 ERQDFL: -WORD 0. ;ERRR QUEUE OVERFLOW BOUNDARY

7402 017074.0 047516 046040 047111 ;FOLLOWING IS THE TEXT FOR ALL ASCII ERROR MESSAGES
7403 017110.0 051500 051040 046505 MESNR: -ASCII /NO LINES REMAIN ACTIVE/
7404 017114.0 044501 020116 041501
7405 017118.0 000000 044216 000
7406 017122.0 044044 023446 020116 MESWS: -ASCII / NO VT20'S SELECTED/
7407 017126.0 031124 023446 020116
7408 017130.0 044216 042514 052103
7409 017134.0 000000 000000 000
7410 017138.0 002040 044514 042516 MESDH: -ASCII / LINE /
7411 017142.0 000000 044514 042516

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749 017156- 042040 042111 047040 MESDH1: .ASCIZ / DID NOT COMPLETE 1 TRANSFER IN TIME/
750 017156- 042111 042111 046517
751 017156- 042111 042111 046517
752 017156- 042111 042111 046517
753 017156- 042111 042111 046517
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802 017156- 042111 042111 046517
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809 017156- 042111 042111 046517
810 017156- 042111 042111 046517
```

```
805 017156- 042111 042111 046517 MESDH1: .ASCIZ / DID NOT COMPLETE 1 TRANSFER IN TIME/
806 017156- 042111 042111 046517
807 017156- 042111 042111 046517
808 017156- 042111 042111 046517
809 017156- 042111 042111 046517
810 017156- 042111 042111 046517
```

SEQ 0017

SEQ 0016

[illegible]

[illegible]

[illegible]

. ABS. 000000 000
017504 001

```

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

```

XVTBBO,XVTBBO/SQL/CRF:SYM=DDXCOM,XVTBBO
RUN-TIME: 1 2 .4 SECONDS
RUN-TIME RATIO: 20/5=4.0
CORE USED: 7K (13 PAGES)

DEC/X11 SYSTEM EXERSIZER MACRO DEFINITION MODULE

IDENTIFICATION

PROGRAM CODE: AC-T375A-MC
PROGRAM TITLE: CXKMDA0 KMV11A/B DEC-X11 MOD
PRODUCT DATE: JAN 83
AUTHOR: G MICHELET
MAINTAINER: COMPUTER SPECIAL SYSTEMS,
DIGITAL EQUIPMENT CO.
Z.A.E LES GLAISINS
ANNECY
FRANCE

000000

.REPT 0
.ENABL LC
DEC/X11 MODULE SPECIFICATION

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PROGRAM HISTORY:

VO.	AUTHOR	DATE	PURPOSE
01	C.P TAYLOR	1-SEP-1982	ORIGINAL
02	G MICHELET	27-SEP-1982	
02	G MICHELET	10-NOV-82	MODIFICATION FOR 11/23A COMPATIBILITY

1. ABSTRACT

TST IS AN IOMOD THAT EXERCISES THE KMV11-A OR KMV11-B, IN

DEC/X11 SYSTEM EXENSIZER MACRO DEFINITION MODULE

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INTERNAL DMA MODE. THE MODULE GENERATES AND CHECKS THE DATA SENT AND RECEIVED AS WELL AS MONITORING THE STATUS OF THE KMV11 CONTROLLER.

2. REQUIREMENTS

2.1 HARDWARE

KMV11-A OR KMV11-B PROGRAMMABLE COMMUNICATIONS CONTROLLER.

2.2 STORAGE

TST REQUIRES A MINIMUM OF <> WORDS OF STORAGE.

3. PASS DEFINITION

A PASS OF TST IS COMPLETE WHEN ALL OF THE KMV11 INTERNAL MEMORY HAS BEEN USED TO HOLD THE DMA DATA.

4. EXECUTION TIME

<>

5. CONFIGURATION REQUIREMENTS

5.1 DEFAULT PARAMETERS

DVADR: 177000
VECTUR: <NOT USED>
BM1: <NOT USED>
BR2: <NOT USED>

5.2 REQUIRED PARAMETERS

NONE.

6. DEVICE/OPTION SETUP

THE KMV MODULE MUST BE SET UP WITH THE ENDLESS LOOPING OF SELF TEST DISABLED, AND WITH THE ABILITY TO RUN THE INTERNAL TESTS.

7. MODULE OPERATION

- A. SET UP THE DEVICE ADDRESS AND ITS PHYSICAL 22 BIT ADDRESS OR TO ITS 18 BIT ADDRESS ACCORDING TO THE MONITOR USED.
- B. CLEAR ALL THE ADDRESSES AND CHECK THAT THEY CLEARED
- C. SET THE INTERFACE INTO MAINTENANCE MODE
- D. CLEAR OUT RECEIVER DATA AREA AND SET UP TRANSMIT DATA
- E. LOAD THE DEVICE REGISTERS WITH THE BUS ADDRESSES AND WORD COUNTS FOR THE TRANSFER
- F. SET THE TEST NUMBER TO ACTIVATE INTO THE CSR REGISTER
- G. WAIT FOR THE TEST TO COMPLETE

DEC/X11 SYSTEM EXERCISER MACRO DEFINITION MODULE

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115      1.      CHECK THE RESULTS OF THE OPERATION
116      J.      IF THERE WERE ANY ERRORS, REPORT THEM AND GO TO M
117      K.      IF THERE WERE NO ERRORS, CHECK THE DATA SENT AGAINST
118              THAT RECEIVED.
119      L.      IF THERE WERE ERRORS, REPORT THE FIRST TEN ONLY, AND
120              ABANDON THE CHECKING IF THERE ARE 10 OR MORE ERRORS
121      M.      SELECT THE NEXT BLOCK OF KMY MEMORY TO USE
122      N.      IF THERE IS MORE MEMORY TO TEST GO TO C.
123      O.      IF ALL THE MEMORY HAS BEEN USED SIGNAL THE END OF PASS
124              AND RESTART AT B
125
126      8.      OPERATION OPTIONS
127
128      NONE.
129
130      9.      NON STANDARD PRINTOUT
131
132      10.     DECK11 MONITOR
133              TO RUN KMY11 DECK11 MODULE ON PDP11/23A ,MONITOR C MUST BE USE.
134              TO RUN KMY11 DECK11 MODULE ON PDP11/23B ,MONITOR Q MUST BE USE.
135
136      NONE.
137      .ENDR
138

```

SEQ 5

DEC/X11 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

140 000000      IUMOD <KMDA >,177000,300,4,4,1,1,0
      000000      MODULE 140000,KMDA,177000,300,4,4,1,1,0,,,,
      ;          .TITLE KMDA DEC/X11 SYSTEM EXERCISER MODULE
      ;          .DXXCUM VERSION 6.4 28-JAN-82
      ;          .LIST BIN
      ;*****
      BEGIN:
      MODNAM: .ASCII /KMDA / ;MODULE NAME.
      000000      113      115      104
      000003      101      040
      000005      000
      XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF #BUFF USAGE
      ADDR: 177000+0 ;1ST DEVICE ADDR.
      VECTOR: 300+0 ;1ST DEVICE VECTOR.
      BR1: .BYTE PRTY4+0 ;1ST BR LEVEL.
      BR2: .BYTE PRTY4+0 ;2ND BR LEVEL.
      DVID1: 1+1 ;DEVICE INDICATOR 1.
      SR1: OPEN ;SWITCH REGISTER 1
      SR2: OPEN ;SWITCH REGISTER 2
      SR3: OPEN ;SWITCH REGISTER 3
      SR4: OPEN ;SWITCH REGISTER 4
      ;*****
      000026      140000      STAT: 140000 ;STATUS WORD.
      000030      000224'      INIT: START ;MODULE START ADDR.
      000037      000224'      SPOINI: MODSP ;MODULE STACK POINTER.
      000034      000000      PASCNT: 0 ;PASS COUNTER.
      000036      000001      ICONF: 1 ;# OF ITERATIONS PER PASS=1
      000040      000000      ICOUNT: 0 ;LOC TO COUNT ITERATIONS
      000042      000000      SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
      000044      000000      HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
      000046      000000      SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
      000050      000000      HKDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
      000052      000000      SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
      000054      000000      RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
      000056      000000      CONFIG: ;RESERVED FOR MONITOR USE
      RES1: 0 ;RESERVED FOR MONITOR USE
      RES2: 0 ;RESERVED FOR MONITOR USE
      SVR0: OPEN ;LOC TO SAVE R0,
      SVR1: OPEN ;LOC TO SAVE R1.
      SVR2: OPEN ;LOC TO SAVE R2.
      SVR3: OPEN ;LOC TO SAVE R3.
      SVR4: OPEN ;LOC TO SAVE R4.
      SVR5: OPEN ;LOC TO SAVE R5.
      SVR6: OPEN ;LOC TO SAVE R6.
      CSRA: OPEN ;ADDR OF CURRENT CSR.
      SBADR: ;ADDR OF GOOD DATA, OR
      000102      000000      ACSR: OPEN ;CONTENTS OF CSR.
      000104      000000      BASADR: ;ADDR OF BAD DATA, OR
      000104      000000      ASTAT: OPEN ;STATUS REG CONTENTS.
      000106      000000      ERRIYP: ;TYPE OF ERROR
      000106      000000      ASR: OPEN ;EXPECTED DATA.
      000110      000000      AAS: OPEN ;ACTUAL DATA.
      000112      000256'      RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
      000114      000000      WDTO: OPEN ;WORDS TO MEMORY PER ITERATION
      000116      000000      WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
      000120      000000      INTR: OPEN ;# OF INTERRUPTS PER ITERATION
      000122      000000      IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0
      .REPT SPSIZ ;MODULE STACK STARTS HERE.
      .NLIST

```

DEC/X11 SYSTEM EXENSIZER MACRO DEFINITION MODULE

```

                                .WORD    0
                                .LIST
                                .ENDR

                                MODSP:
                                ;*****
000224
141      054000
142      044000
143 000224
144 000224 012767 065000 001454
145 000232 012702 001656
146 000236 016700 177544
147 000242 012701 000010
148 000246
149 000246 010022
150 000250 005720
151 000252 005301
152 000254 001374
153 000256
154
155
156 000256 016767 177576 001454
157
158 000264 005067 001412
159 000270 012767 001740 001416
160 000276 104415 000000 001714
161
162 000304 012767 005740 001414
163 000312 104415 000000 001726
164
165 000320 032767 000010 001412
166 000326 001021
167
168
169 000330 006267 001364
170 000334 006267 001360
171 000340 006267 001354
172 000344 006267 001350
173
174
175
176 000350 006267 001356
177 000354 006267 001352
178 000360 006267 001346
179 000364 006267 001342
180 000370 000406
181
182 000372
183 000372 104416 000000 001716
184 000400 104416 000000 001730
185 000406 005067 001272
186 000412
187 000412 004767 000472
188 000416 004767 000564
189
190 000422 012702 002000
191 000426 012701 005740

                                START:
                                MOV      #65000,MAXCNT      ;RAM MEMORY MAX LENGTH
                                MOV      #KMVCSP,R2          ;POINT TO CSR ADDRESS TABLE
                                MOV      ADDR,R0             ; GET THE FIRST ADDRESS
                                MOV      #8,,R1              ;LOOP FOR ALL 8 ADDRESSES

                                1S:
                                MOV      R0,(R2)+
                                TST      (R0)+              ;CHECK FOR VALID ADDRESS
                                DEC      R1
                                BNE      1S

                                RESTHT:
                                MOV      RES2,QMON          ;SAVE CONFIGURATION WORD 2

                                CLR      FLAG              ; RESET THE ERRUR FLAG
                                MOV      #ITABLE,TVAD       ; GET VIRTUAL ADDRESS FOR TX
                                GETPAS,BEGIN, TVAD          ;GET PHYSICAL ADDRESS FROM 16-BIT TVAD

                                MOV      #RTABLE,RVAD       ; GET VIRTUAL ADDRESS FOR RX
                                GETPAS,BEGIN, RVAD          ;GET PHYSICAL ADDRESS FROM 16-BIT RVAD

                                BIT      #10,QMON           ;TEST IF Q 22 BUS MONITOR
                                BNE      2S                ;IF IT IS ,CONVERT TO 22 BITS

                                ASR      TEA                ;IF NOT PREPARE THE 2 EXTENDED ADDR BIT
                                ASR      TEA                ;FOR TX TABLE
                                ASR      TEA
                                ASR      TEA

                                ASR      REA                ;PREPARE THE 2 EXTENDED ADDR BIT
                                ASR      REA                ;FOR RX TABLE
                                ASR      REA
                                BR       2S

                                2S:
                                MAP22S,BEGIN,TPA          ; GET 22-BIT ADDR FROM 18-BIT ADDR
                                MAP22S,BEGIN,RPA          ; GET 22-BIT ADDR FROM 18-BIT ADDR

                                22S: CLR      COUNT          ;SELECT RAM STARTING ADDRESS FOR TX
                                TWODMA: JSR      PC,CLKM4V   ;CLEAR REG
                                JSR      PC,MAINM1          ;SET MAINT 1

                                MOV      #2000,R2
                                MOV      #RTABLE,R1

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DEC/X11 SYSTEM EXENSIZER MACRO DEFINITION MODULE

```

192 000432 005021
193 000434 005302
194 000436 001375
195
196 000440 012702 002000
197 000444 012701 001740
198 000450
199 000450 104417 000000
200 000454 016721 177374
201 000460 005302
202 000462 001372
203
204 000464 032767 000010 001246
205 000472 001015
206
207 000474 016777 001216 001166
208 000502 016777 001222 001152
209 000510 016777 001204 001150
210 000516 016777 001210 001134
211 000524 000414
212
213 000526 016777 001172 001132
214 000534 016777 001176 001116
215 000542 016777 001154 001120
216 000550 016777 001160 001104
217
218 000556 016777 001122 001110
219
220 000564 012777 002000 001100
221 000572 052777 000022 001056
222 000600 104407 000000
223 000604 104407 000000
224 000610 104407 000000
225 000614 104407 000000
226 000620 104407 000000
227 000624 104407 000000
228 000630 104407 000000
229 000634 104407 000000
230 000640 004767 000370
231 000644 000441
232 000646 000402
233 000650 000407
234 000652 000414
235
236 000654
237 000654 104404 000000
238
239 000660 104405 000000 001636
240
241 000666 000473
242
243 000670
244 000670 104404 000000

                                10S: CLR      (R1)+          ;CLEAR RX TABLE
                                DEC      R2
                                BNE      10S

                                MOV      #2000,R2
                                MOV      #ITABLE,R1

                                1S:
                                RANDS,BEGIN
                                MOV      RANNUM,(R1)+
                                DEC      R2
                                BNE      1S

                                BIT      #10,QMON           ;TEST IF Q22 MONITOR
                                BNE      6S                ;IF IT IS ,LOAD EXT ADDRESS ON 22 BITS

                                MOV      TPA,#KMVP12        ;SEND TTABLE ADDRESS
                                MOV      RPA,#KMVP04        ;SEND RTABLE ADDRESS
                                MOV      TEA,#KMVP10        ;EXT ADDR FOR TX TABLE ON 18 BITS
                                MOV      REA,#KMVP02        ;EXT ADDR FOR RX TABLE ON 18 BITS
                                BR       40S

                                MOV      TEA22,#KMVP10      ;EXT ADDR FOR TX TABLE ON 22 BITS
                                MOV      REA22,#KMVP02      ;EXT ADDR FOR RX TABLE ON 22 BITS
                                MOV      TPA22,#KMVP12      ;SEND TTABLE ADDRESS
                                MOV      RPA22,#KMVP04      ;SEND RTABLE ADDRESS

                                MOV      COUNT,#KMVP16     ;LOAD STATING ADDRESS IN RAM

                                MOV      #2000,#KMVP14     ;SEND TABLE LENGTH
                                BIS      #22,#KMVCSP        ; LOAD TEST NUMBER 22
                                BREAKS,BEGIN              ;TEMPORARY RETURN TO MONITOR....
                                BREAKS,BEGIN              ;THEN CONTINUE AT NEXT INSTRUCTION.
                                BREAKS,BEGIN              ;TEMPORARY RETURN TO MONITOR....
                                BREAKS,BEGIN              ;THEN CONTINUE AT NEXT INSTRUCTION.
                                BREAKS,BEGIN              ;TEMPORARY RETURN TO MONITOR....
                                BREAKS,BEGIN              ;THEN CONTINUE AT NEXT INSTRUCTION.
                                BREAKS,BEGIN              ;TEMPORARY RETURN TO MONITOR....
                                BREAKS,BEGIN              ;THEN CONTINUE AT NEXT INSTRUCTION.
                                JSR      PC,TSERR           ;CHECK BSELO;WHICH ERROR
                                BR       3S                ;TEST OK
                                BR       6S                ;TIME OUT
                                BR       7S                ;NO KMV11 ANSWER
                                BR       20S               ;DATA CMP ERROR

                                6S:
                                ;*****
                                DATERS,BEGIN              ;DATA ERROR!!!
                                ;*****
                                HRDERS,BEGIN,DUMP          ;TIMEOUT ERROR
                                ;*****
                                BR       15S

                                7S:
                                ;*****
                                DATERS,BEGIN              ;DATA ERROR!!!
                                ;*****

```

DEC/X11 SYSTEM EXENSIZER MACRO DEFINITION MODULE

```

237      000674 104405 000000' 001636' ;*****
      HDRS,BEGIN,DUMP ;NO KMV11 ANSWER
      ;*****
238      000702 000465 BR 15S
239
240      000704 016767 177200 177172 20S: MOV AWAS,WASADR ;HEAD ADD IN ERROR
241      000712 017767 000740 177162 MOV #KMVP06,SBADR ;GOOD VALUE
242      000720 017767 000734 177160 MOV #KMVP02,ASB ;READ WRONG VALUE
243      000726 017767 000730 177154 MOV #KMVP04,AWAS ;DATA ERROR!!!
244      000734 104404 000000' ;*****
      DATERS,BEGIN ;DATA ERROR!!!
      ;*****
245      000740 104405 000000' 001636' ;*****
      HDRS,BEGIN,DUMP ;DATA CMP ERROR IN RAM DURING TRANSFER
      ;*****
246      000746 000443 BR 15S
247
248      000750 012701 001740' 3S: MOV #TABLE,R1 ;TX TABLE ADDRESS
249      000754 012704 005740' MOV #TABLE,R4 ;RX "
250      000760 012702 002000 MOV #2000,R2 ;TABLE LENGTH
251      000764 016767 177120 177112 MOV AWAS,WASADR
252      000772 012167 177110 4S: MOV (R1)+,ASB
253      000776 012467 177106 MOV (R4)+,AWAS ;GET THE DATA TO COMPARE
254      001002 026767 177100 177100 CMP ASB,AWAS ;CMP RX TABLE AND TX TABLE
255      001010 001420 BEQ 5S ;JK TEST NEXT LOCATION
256      001012 010467 177064 MOV R4,SBADR ;ERROR, REPORT WHERE IN THE TABLE
257      001016 162767 005740' 177056 SUB #TABLE,SBADR ;THE ERROR OCCURRED
258      001024 026727 000652 000012 CMP FLAG,#10. ;REPORT ONLY THE FIRST 10 ERRORS
259      001032 001411 BEQ 15S
260      001034 104404 000000' ;*****
      DATERS,BEGIN ;DATA ERROR!!!
      ;*****
261      001040 104405 000000' 001636' ;*****
      HDRS,BEGIN,DUMP ;REPORT 10 FIRST ERROR
      ;*****
262      001046 005267 000630 INC FLAG
263
264      001052 005302 5S: DEC R2 ;ALL MEMORY TESTED?
265      001054 001346 BNE 4S ;NO BRANCH
266
267      001056 15S: ADD #2000,COUNT ;USE OTHER PART OF RAM
268      001056 062767 002000 000620 CMP COUNT,MAXCNT ;IS ALL RAM USED?
269      001064 026767 000614 000614 BPL 30S
270      001072 100002 JMP INQD*A
271      001074 000167 177312
272
273      001100 30S: ENDITS,BEGIN ;SIGNAL END OF ITERATION.
      001100 104413 000000' ;MONITOR SHALL TEST END OF PASS
274      001104 000167 177146 JMP PESTKI

```

SUBROUTINES

```

276      ;SBTTL SUBROUTINES
277
278      001110 005077 000542 ;
279      001114 012777 054000 000534 CLRKMV: CLR #KMVCSR
280      001122 104407 000000' MOV #MAINT0,#KMVCSR
281      001126 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
282      001132 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
283      001136 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
284      001142 012702 000010 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
285      001146 016701 000504 MOV #10,R2
286      001152 005021 1S: MOV #KMVCSR,R1 ;LOAD ADDRESS
287      001154 104407 000000' CLR (R1)+ ;CLEAR
288      001160 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
289      001164 005302 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
290      001166 001371 DEC R2 ;ALL DONE
291      001170 004567 000116 RNE 1S ;NO
292      001174 000403 JSR R0,CKALL ;CHECK ALL REG = 0
293      001176 104405 000000' 000000 BR 2S ;OK BRANCH AT END
294      001204 000207 ;*****
      HDRS,BEGIN,NULL ;CSR'S REGISTERS CAN'T BE CLEARED
      ;*****
295      001206 000207 2S: RTS PC
296
297      001206 012777 044000 000442 MAINM1: MOV #MAINT1,#KMVCSR ;LOAD ADDRESS
298      001214 004567 000260 JSR R5,CKSELO ;CHECK SELO=0 BUT MODE BIT =1
299      001220 004000 ,WORD 4000
300      001222 000403 BR 1S ;OK BRANCH
301      001224 104405 000000' 000000 ;*****
      HDRS,BEGIN,NULL ;CANNOT GO INTO MAINT MODE 1
      ;*****
302      001232 000207 1S: RTS PC
303
304      001234 004567 000322 ;TSTERR: JSR R5,CBSELO ;LOOK IF BSELO=0
305      001240 000000 ,WORD 0 ;CHECK SELO=0 BUT MODE BIT =1
306      001242 000411 BR 1S ;TEST IS OK ,RTS PC
307
308      001244 122767 000200 176636 CMPB #200,AWAS ;LOOK IF BSELO=200
309      001252 001406 BEQ 2S ;TIMEOUT DURING TEST,RTS PC+2
310
311      001254 122767 000100 176626 CMPB #100,AWAS ;LOOK IF BSELO=100
312      001262 001405 BEQ 3S ;DATA CMP ERROR,RTS PC+6
313
314      001264 000407 BR 4S ;NO KMV11 ANSWER ,RTS PC+4
315
316      001266 000207 1S: RTS PC ;TEST OK
317
318      001270 062716 000002 2S: ADD #2,(SP)
319      001274 000207 RTS PC ;TIMEOUT ERROR
320
321      001276 062716 000006 3S: ADD #6,(SP)
322      001302 000207 RTS PC ;DATA CMP ERROR
323
324      001304 062716 000004 4S: ADD #4,(SP)
325      001310 000207 RTS PC ;NO KMV11 ANSWER
326
327      001312 ;CKALL:

```

SUBROUTINES

```

326 001312 016767 000340 176560      MOV      KMVCSR,CSRA      ;READ SEL0
327 001320 017767 176554 176554      MOV      @CSHA,ACSR
328 001326 001061                BNE      1$
329 001330 016767 000324 176542      MOV      KMVP02,CSRA      ;READ SEL2
330 001336 017767 176536 176536      MOV      @CSHA,ACSR
331 001344 001052                BNE      1$
332 001346 016767 000310 176524      MOV      KMVP04,CSRA      ;READ SEL4
333 001354 017767 176520 176520      MOV      @CSHA,ACSR
334 001362 001043                BNE      1$
335 001364 016767 000274 176506      MOV      KMVP06,CSRA      ;READ SEL6
336 001372 017767 176502 176502      MOV      @CSHA,ACSR
337 001400 001034                BNE      1$
338 001402 016767 000260 176470      MOV      KMVP10,CSRA      ;READ SEL10
339 001410 017767 176464 176464      MOV      @CSHA,ACSR
340 001416 001025                BNE      1$
341 001420 016767 000244 176452      MOV      KMVP12,CSRA      ;READ SEL12
342 001426 017767 176446 176446      MOV      @CSHA,ACSR
343 001434 001016                BNE      1$
344 001436 016767 000230 176434      MOV      KMVP14,CSRA      ;READ SEL14
345 001444 017767 176430 176430      MOV      @CSHA,ACSR
346 001452 001007                BNE      1$
347 001454 016767 000214 176416      MOV      KMVP16,CSRA      ;READ SEL16
348 001462 017767 176412 176412      MOV      @CSHA,ACSR
349 001470 001402                BEQ      2$,R5
350 001472 062705 000002          1$: ADD      #2,R5
351 001476 000205                2$: RTS      R5
352
353 001500 012567 176400          : CKSEL0: MOV      (R5)+,ASTAT      ;WRITE GOOD
354 001504 012767 177700 000176      MOV      #177700,DELCT1      ; SET TIMEOUT VALUE
355 001512
356 001512 104407 000000'          3$: BREAKS,BEGIN      ;TEMPORARY RETURN TO MONITOR....
357 001516 104407 000000'          BREAKS,BEGIN      ;THEN CONTINUE AT NEXT INSTRUCTION.
358 001522 016767 000130 176350      MOV      KMVCSR,CSRA
359 001530 017767 000122 176344      MOV      @KMVCSR,ACSR      ;READ SEL 0
360 001536 026767 176340 176340      CMP      ACSR,ASTAT
361 001544 001405                BEQ      2$,R5
362 001546 005267 000136          INC      DELCT1      ; NOT YET KEEP TIMING
363 001552 001357                BNE      3$
364 001554 062705 000002          1$: ADD      #2,R5
365 001560 000205                2$: RTS      R5
366
367 001574
368 001574 104407 000000'          : CBSEL0: MOV      (R5)+,ASH
369 001600 104407 000000'          MOV      #170000,DELCT1      ; SET TIMEOUT VALUE
370 001604 117767 000046 176276      3$: BREAKS,BEGIN      ;TEMPORARY RETURN TO MONITOR....
371 001612 126767 176270 176270      BREAKS,BEGIN      ;THEN CONTINUE AT NEXT INSTRUCTION.
372 001620 001405                MOV      @KMVCSR,AWAS
373 001622 005267 000062          CMPB     ASB,AWAS
374 001626 001362                BEQ      2$,R5
375 001630 062705 000002          INC      DELCT1      ; NOT YET KEEP TIMING
376 001634 000205                BNE      3$
377 001636 000000'          1$: ADD      #2,R5
378 001640 000000'          2$: RTS      R5
379 001642 001720'
380 001644 001722'
381 001646 001730'
382 001648 001732'
383 001650 001734'
384 001652 001736'
385 001654 001738'
386 001656 000000'
387 001660 000000'
388 001662 000000'
389 001664 000000'
390 001666 000000'
391 001670 000000'
392 001672 000000'
393 001674 000000'
394 001676 001704'
395 001700 177777
396
397 001702 000000
398 001704 000000
399 001706 000000
400 001710 000000
401 001712 000000
402
403 001714 000000
404 001716 000000
405 001720 000000
406 001722 000000
407 001724 000000
408
409 001726 000000
410 001730 000000
411 001732 000000
412 001734 000000
413 001736 000000
414
415 001740
416 005740
417 001740 000000
418
419 000001

```

KMDA DEC/X11 SYSTEM EXERCISER 4 MACRO M1200 04-JAN-83 09:37 PAGE 5
DATA STORAGE

```

377
378 001636 001716'      DUMP: .SBTTL DATA STORAGE
379 001640 001720'      .WORD 1PA
380 001642 001722'      .WORD TEA
381 001644 001724'      .WORD 1PA22
382 001646 001730'      .WORD TEA22
383 001648 001732'      .WORD RPA
384 001650 001734'      .WORD REA
385 001652 001736'      .WORD RPA22
386 001654 001738'      .WORD REA22
387 001660 000000      KMVCSR: .WORD 0
388 001662 000000      KMVP02: .WORD 0
389 001664 000000      KMVP04: .WORD 0
390 001666 000000      KMVP06: .WORD 0
391 001670 000000      KMVP10: .WORD 0
392 001672 000000      KMVP12: .WORD 0
393 001674 000000      KMVP14: .WORD 0
394 001676 001704'      KMVP16: .WORD 0
395 001700 177777      .WORD COUNT
396
397 001702 000000      : FLAG: .WORD 0
398 001704 000000      COUNT: .WORD 0
399 001706 000000      MAXCNT: .WORD 0
400 001710 000000      DELCT1: .WORD 0
401 001712 000000      NUB: .WORD 0
402
403 001714 000000      : TVAD: .WORD 0
404 001716 000000      TPA: .WORD 0
405 001720 000000      TEA: .WORD 0
406 001722 000000      TPA22: .WORD 0
407 001724 000000      TEA22: .WORD 0
408
409 001726 000000      : RVAD: .WORD 0
410 001730 000000      RPA: .WORD 0
411 001732 000000      REA: .WORD 0
412 001734 000000      RPA22: .WORD 0
413 001736 000000      REA22: .WORD 0
414
415 001740      : TTABLE: .BLKW 2000
416 005740      RTABLE: .BLKW 2000
417 001740 000000      QMON: .WORD 0
418
419 000001      : .END

```

KMDA DEC/X11 SYSTEM EXERCISER M MACRO M1200 04-JAN-83 09:37 PAGE 5-1
SYMBOL TABLE

ACSR 000102R	CKSEL0 001500R	KMVP10 001666R	PTY = 000000	SOFERS= 104406
ADDR 000006R	CLKPRE= 000001	KMVP12 001670R	PRY0 = 000000	SDFPAS 000046R
ADDR22= 001000	CLKSPS= 104422	KMVP14 001672R	PRY1 = 000040	SP0INI 000032R
APIPRE= 000200	CLRMV 001110R	KMVP16 001674R	PRY2 = 000100	SPSIZ = 000040
ASB 000106R	COUNFIG 000056R	KIPRES= 000400	PRY3 = 000140	SR1 000016R
ASTAT 000104R	COUNT 001704R	KITND= 040000	PRY4 = 000200	SR2 000020R
AUTO = 000010	CSHA 000100R	MAINM1 001206R	PRY5 = 000240	SR3 000022R
AWAS 000110R	DAICKS= 104411	MAINT0= 054000	PRY6 = 000300	SR4 000024R
BEGIN 000000R	DATERS= 104404	MAINT1= 044000	PRY7 = 000340	START 000224R
BIT0 = 000001	DECLT1 001710R	MAP22= 104416	PS = 177776	STAT 000026R
BIT1 = 000002	JUMP 001636R	MAXCNT 001706R	PS = 177776	SVR0 000062R
BIT10 = 002000	DVID1 000014R	MODNAM 000000R	PUSH = 005746	SVK1 000064R
BIT11 = 004000	CCMEM= 000100	MODSP 000224R	PUSH2 = 024646	SVK2 000066R
BIT12 = 010000	ENDITS= 104413	MSGNS = 104403	PRFLG= 000002	SVR3 000070R
BIT13 = 020000	ENDS = 104410	MSGSS = 104402	CMUN 011740R	SVR4 000072R
BIT14 = 040000	ERRTYP 000106R	MSGS = 104401	CMUN22= 000010	SVK5 000074R
BIT15 = 100000	EXITS = 104400	NCPUUP= 000020	RANDS = 104417	SVK6 000076R
BIT2 = 000004	FLAG 001702R	NOAPT= 000002	RANUM 000054R	SYSCNT 000052R
BIT3 = 000010	GETPAS= 104415	NUR 001712R	REA 001732R	TEA 001720R
BIT4 = 000020	GWBUS= 104414	NULL = 000000	REA22 001736R	TEA22 001724R
BIT5 = 000040	HRDCNT 000044R	OPEN = 000000	RESTR 000256R	TPA 001716R
BIT6 = 000100	HRDRS= 104405	OIOAS = 104420	RES1 000056R	TPA22 001722R
BIT7 = 000200	HRDPAS 000050R	PAKPRE= 002000	RES2 000060R	TRPOFD= 000023
BIT8 = 000400	ICONT 000036R	PASCNT 000034R	RH70 = 001000	ISTERR 001234R
BIT9 = 001000	ICOUNT 000040R	PDPF11= 000002	RPA 001730R	ITABLE 001740R
BLEANS= 104407	IUNUM 000122R	PDPFS1= 020000	RPA22 001734R	TYAO 001714R
BR1 000012R	INDPAR= 000040	PDP44 = 100000	RSTR 000112R	TWOMA 000412R
BR2 000013R	INIT 000030R	PDP60 = 004000	RTABLE 005740R	USTACK= 000001
BT00S = 104421	INTR 000120R	PDP70 = 010000	RYAD 001726R	VECTOR 000010R
CAPRES= 000004	KMVCSS 001656R	PIRUS = 000004	R6 = 0000006	MASADH 000104R
CBSSEL0 001562R	KMVP02 001660R	PJPSP = 005726	R7 = 0000007	WDFR 000116R
CDATAS= 104412	KMVP04 001662R	PJPSP2= 022626	SRADR 000102R	WDT0 000114R
CKALL 001312R	KMVP06 001664R	PRHMS= 000002	SUFcnt 000042R	XFLAG 000005R
CKHNGS= 000001				

. ARS. 000000 000
011742 001
ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 15281 WORDS (60 PAGES)
DYNAMIC MEMORY: 16644 WORDS (64 PAGES)
ELAPSED TIME: 00:00:16

DIAGNOSTIC ENGINEERING

digitalDECO ☐ DEPO ☒ SUBMISSION ☐☐ NEW

FOR RELEASE ENG. USE

☐ CHANGE ☐ DELETE

PRODUCT IDENTIFICATION

LIBRARY	PRODUCT NUMBER	REV	PATCH	ECO FULLY	PRODUCT DATE	STATUS	DISTRIBUTION	1ST COPY - RIGHT YEAR	LAST COPY - RIGHT YEAR
ZZ	CXDMC	B	1	01	24 APR 79	OBSELETE	X G	R	1976 1979

TITLE CXDMCB1 DMC-11 MODULE

AUTHOR D. BUTENHOF MAINTAINING GROUP SUPT GRP MAINTAINER D. BUTENHOF SUBMITTING ENGINEER D. BUTENHOF

PRODUCT COMPONENTS

CK	DESCRIPTION	PRODUCT NO.	REV	CK	DESCRIPTION	PRODUCT NO.	REV
	DOCUMENT				INDEX		
	LISTING				SOURCE MEDIA		
	OBJECT MEDIA				TEST MEDIA		
		AF-E953B-M1					

PRODUCTS OBSOLETE (other than previous version)

LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV
MD			MD			MD		

PRODUCT CHARACTERISTICS

PROCESSORS PRODUCT OPERATES WITH (Enter all applicable 2-digit codes representing the Processor the product operates with. See separate instructions.)

OPERATIONAL CODES (Enter all applicable 2-digit codes that describe the product. See separate instructions.)

ACT/APT/XXDP	EXT	ACT SEQ NUMBER	ACT/XXDP COMPATIBLE?	APT COMPATIBLE?	1ST PASS RUN TIME	SUBSEQUENT PASS RUN TIME
INFORMATION FIELD			<input type="checkbox"/> Y <input type="checkbox"/> N	<input type="checkbox"/> Y <input type="checkbox"/> N	SECONDS	SECONDS

DECO/DEPO INFORMATION

PROBLEM REPORTS CLOSED:								
TIME AFFECTED			MULTIMEDIA AFFECTED?	<input checked="" type="checkbox"/> YES	<input type="checkbox"/> NO			

KIT NUMBERS	ZJ130-RB						
	ZJ129-RZ, FR						

PROBLEM: THE "DDCMP ERROR COUNTERS ARE NON-ZERO" SOFT ERROR IS MISLEADING SINCE IT REFERS TO A RECOVERABLE AND SOFTWARE-TRANSPARENT SITUATION. NEVERTHELESS, IF 40 SUCH OCCUR, THE MODULE WILL BE DROPPED BY THE MONITOR.

SOLUTION: THE SOFT ERROR CALL AND MESSAGE ARE DELETED FROM THE MODULE BY THIS PATCH, AND THE HARD ERROR CALL IS EXTENDED FOR CLARIFICATION OF THE SITUATION, IF THE DMC-11 SHOULD BE UNABLE TO RECOVER.

DEPO PATCH AREA

CHANGE LOC	FROM	TO	CHANGE LOC	FROM	TO
1226	104403	402	1726	51122	51040
1334	104406	402	1730	51117	52105
1716	22400	20072	1732	26440	44522
1720	47523	53117	1734	42040	51505
1722	52106	51105	1736	41504	0
1724	42440	33440			

MANUFACTURING ENGINEER J.E. CASSELLA	SUPPORT ENGINEER	CHARGE DECO/DEPO TO DISCRETE PROJECT NUMBER
DATE: 24 APR 79	DATE: 10-MAY-79	098-05460
MAINTAINER	WAIVERING MANAGER	COORDINATION NO. 3130
DATE:	DATE:	

Y
IEAA DEC/X11 SYSTEM EXERCISER M MACRO M1113 29-JUL-82 08:03

000000

.REPT 0

IDENTIFICATION

PRODUCT CODE: AC-T241A-MC

PRODUCT NAME: CXIEAA0 IEU/IEQ11- DEC/X MOD

DATE CREATED: JULY 1982

MAINTAINER: CSS MUNICH

AUTHOR: PETER SEEBACH

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SOFTWARE ON EQUIPMENT NOT SUPPLIED BY
DEC.


```

45
46
47      1.0  ANSTRACT
48      -----
49
50      IEQ/IEU IS AN IOMODX DEC/X11 SOFTWARE MODULE WHICH
51      EXERCISES THE IEU-11 OR THE IEQ-11.
52      THE MAIN CHARACTERISTICS OF THIS MODULE ARE:
53
54      1.  DOES DMA DATA TRANSFER FROM CHANNEL 1 TO CHANNEL 2.
55      2.  DOES DMA DATA TRANSFER FROM CHANNEL 2 TO CHANNEL 1.
56
57      THE MODULE CAN SUPPORT UP TO 8 DEVICES BY SETTING DVC TO
58      THE APPROPRIATE VALUE AT CONFIGURATION TIME. IF MORE THAN ONE
59      DEVICE IS SPECIFIED, THE ADDRESSES ARE INCREMENTED BY 20 OCTAL
60      FROM THAT GIVEN AS DVA.
61
62      ALL ERRORS DETECTED ARE REPORTED ON THE CONSOLE.
63
64
65      2.0  REQUIREMENTS
66      -----
67
68      HARDWARE:
69
70
71      A.  PDP11 OR LS111 CAPABLE OF SUPPORTING DEC/X11.
72      B.  UP TO 8 IEU-11 (FOR PDP11) OR IEQ-11 (FOR LS111) INTERFACES.
73      C.  A BC08 TEST CABLE IS REQUIRED BETWEEN THE TWO
74          CHANNELS OF THE MODULE.
75      D.  ANY USER IEC BUS DEVICES MUST BE DISCONNECTED.
76
77      SOFTWARE:
78      DEC/X11 MONITOR
79
80      STORAGE:
81
82      1K OF MEMORY
83
84
85      3.  PASS DEFINITION
86      -----
87
88      ONE PASS OF THE IEQ/IEU MODULE CONSISTS OF TEN CYCLES
89      OF THE TEST.
90
91
92      4.  EXECUTION TIME
93      -----
94
95      ONE PASS OF THE IEQ/IEU RUNNING ALONE ON A PDP-11/60
96      TAKES APPROXIMATELY 10 SECONDS.
97
98
99
100
101

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102
103      5.  CONFIGURATION REQUIREMENTS
104      -----
105
106      DEFAULT PARAMETERS:
107
108      DEVADR:160140, VECTDP:420, BR1:4, DEVCHT:1, ICONST:10, RBUFVA:RUFIN,
109      RBUFSZ:256., RBUFRQ:256.
110
111      REQUIRED PARAMETERS:
112
113      NONE
114
115
116      6.  DEVICE/OPTION SETUP
117      -----
118
119      MAKE CERTAIN THAT THE IEU-11 OR IEQ-11 ARE CONNECTED AND READY.
120      THIS ENTAILS INSTALLING A BC08S-01 CABLE FROM J1 TO J2.
121
122
123      7.  MODULE OPERATION
124      -----
125
126      TEST SEQUENCE:
127
128      A.  TEST DATA TRANSFER FROM CHANNEL 1 TO 2
129
130      1.  SET CHANNEL 1 IN CONTROLLER ACTIVE STATE
131      2.  SELECT CHANNEL 1 AS TAKER (TACS)
132      3.  SELECT CHANNEL 2 AS LISTENER (SENDING MDA2)
133      4.  LOADING DMA SETUP
134      5.  START DMA BY LOADING GTS INTO ACRI
135      6.  INTERRUPT OCCURS WHEN AC OF OF CHANNEL 1 IS SET
136      7.  DATA COMPARE
137
138      B.  TEST DATA TRANSFER FROM CHANNEL 2 TO 1
139
140      1.  SELECT CHANNEL 1 AS LISTENER (LOAD LUN)
141      2.  SELECT CHANNEL 2 AS TAKER (LOAD TUN)
142      3.  LOAD DMA SET UP AND START DMA (SET DMA ENB)
143      4.  INTERRUPT OCCURS WHEN AC OF OF CHANNEL 2 IS SET
144      5.  DATA COMPARE
145
146
147
148      8.  OPERATION OPTIONS
149      -----
150
151      NONE
152
153
154
155      9.  NO STANDARD PRINTOUTS
156      -----
157
158

```

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159
160
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171
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174
175
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177

```

ALL ERROR PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED
IN THE DEC/X11 DOCUMENT.

10. STARTING PROCEDURE

A. LOAD AND START THE DEC/X11 PROGRAM ACCORDING TO
THE DEC/X11 USER'S MANUAL.

B. ENSURE THAT THE DEVICE ADDRESS AND VECTOR ARE
CORRECT FOR THE IEU/IEQ INTERFACE.

11. LISTING

ENDR

```

179
180 000000
000000

```

DDXCOM HEADER

```

1000000
1000000

```

SBTTL DDXCOM HEADER
CIEAA >,160140,420,4,0,0,10,,0,BUFIN,256,,256.
MODULE 150000,IEAA,160140,420,4,0,0,10,,0,BUFIN,256,,256.
TITLE IEAA DEC/X11 SYSTEM EXERCISER MODULE
DDXCOM VERSION 6 23-MAY-78
LIST 014

BEGIN:
MODNAM: ASCII / IEAA / MODULE NAME.

111 105 101
101 040

XFLAG: BYTE OPEN ;USED TO KEEP TRACK OF *BUFF USAGE
ADDR: 160140*0 ;1ST DEVICE ADDR.
VECTOR: 420+0 ;1ST DEVICE VECTOR.
RR1: BYTE PRTY4+0 ;1ST RR LEVEL.
RR2: BYTE PRTY0+0 ;2ND RR LEVEL.
DEV101: 0+1 ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1
SR2: OPEN ;SWITCH REGISTER 2
SR3: OPEN ;SWITCH REGISTER 3
SR4: OPEN ;SWITCH REGISTER 4

STAT: 150000 ;STATUS WORD.
INIT: START ;MODULE START ADDR.
SPOINT: MODSP ;MODULE STACK PUNTER.
PASSCNT: 0 ;PASS COUNTER.
ICONT: 10. ;# OF ITERATIONS PER PASS=10.
ICOUNT: 0 ;LOC TO COUNT ITERATIONS
SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
RANRND: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
CONFIG: ;RESERVED FOR MONITOR USE
RES1: 0 ;RESERVED FOR MONITOR USE
RES2: 0 ;RESERVED FOR MONITOR USE
SVP0: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVP3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVR5: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSR.
SBADH: ;ADDR OF GOOD DATA, OR
ACSR: OPEN ;CONTENTS OF CSR.
WASADR: ;ADDR OF BAD DATA, OR
ASTAT: OPEN ;STATUS REG CONTENTS.
ERRTYP: ;TYPE OF ERROR
ASR: OPEN ;EXPECTED DATA.
AWAS: OPEN ;ACTUAL DATA.
HSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
INTR: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0
RBUFVA: BUFIN ;READ BUFFER VIRTUAL ADDRESS

000026 150000
000030 000336
000032 000252
000034 000000
000036 000012
000040 000000
000042 000000
000044 000000
000046 000000
000050 000000
000052 000000
000054 000000
000056 000000
000060 000000
000062 000000
000064 000000
000066 000000
000070 000000
000072 000000
000074 000000
000076 000000
000100 000000
000102
000102 000000
000104
000104 000000
000106
000106 000000
000110 000000
000112 000372
000114 000000
000116 000000
000120 000000
000122 000000
000124 001544

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000126 000000          RBUFA: OPEN          ;READ BUFFER PHYSICAL ADDRESS
000130 000000          RBUFEA: OPEN          ;READ BUFFER EA BITS
000132 000400          RBUFSZ: 256.         ;SIZE OF THE READ BUFFER
000134 000000          WRBUFA: OPEN          ;WRITE BUFFER PHYSICAL ADDRESS
000136 000000          WRBUFEA: OPEN         ;WRITE BUFFER EA BITS
000140 000400          WRBUFSZ: 256.         ;WRITE BUFFER SIZE REQUESTED
000142 000000          CDECT: OPEN           ;WRITE BUFFER SIZE AVAILABLE
000144 000000          CDWDCI: OPEN          ;CDATA/DATCK ERROR COUNT
000146 000000          CDWDCI: OPEN          ;CDATA/DATCK WORD COUNT
000150 000000          FREF: OPEN            ;RESERVED FOR FUTURE USE
                                .REPT SPSIZ    ;MODULE STACK STARTS HERE.
                                .MLIST
                                .WORD 0
                                .LIST
                                .ENDP
000257          MODSP:
;*****

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182          .SBTTL  CONSTANTS AND VARIABLES.
183 000252 000000          WCNT1: .WORD 0      ;
184 000254 000000          WCNT2: .WORD 0      ;
185 000256 000000          XMEM: .WORD 0        ;
186 000260 000000          FUNC: .WORD 0        ;
187 000262 000000          DVICE: .WORD 0        ;
188 000264 000000          MSK: .WORD 0          ;
189 000266 000000          INTFLA: .WORD 0       ;
190 000270 000000          INTFLB: .WORD 0       ;
191
192 000272 000000          IIRX: .WORD 0          ;POINTER TO IEEE INTERRUPT REGISTER
193 000274 000000          ADH: .WORD 0          ;POINTER TO ADDRESS REGISTER
194 000276 000000          IIRHX: .WORD 0        ;POINTER TO HIGH BYTE OF THE INTERRUPT REGISTER
195 000300 000000          ISRX: .WORD 0          ;POINTER TO IEEE STATUS REGISTER
196 000302 000000          ISRLX: .WORD 0        ;POINTER TO IEEE STATUS REGISTER ,LOW BYTE
197 000304 000000          SPR: .WORD 0           ;POINTER TO SERIAL POLL REGISTER
198 000306 000000          ACR: .WORD 0           ;POINTER TO AUXILIARY COMMAND REGISTER
199 000310 000000          TCRA: .WORD 0          ;POINTER TO IEEE COMMAND REGISTER
200 000312 000000          MASK0: .WORD 0         ;POINTER TO MASK0 REGISTER
201 000314 000000          MASK1: .WORD 0         ;POINTER TO MASK1 REGISTER
202 000316 000000          IDRX: .WORD 0          ;POINTER TO DATA REGISTER
203 000320 000000          PPR: .WORD 0           ;POINTER TO PARALLEL POLL REGISTER
204 000322 000000          DOR: .WORD 0           ;POINTER TO THE DATA OUT REGISTER
205 000324 000000          DIR: .WORD 0           ;POINTER TO DATA IN REGISTER
206 000326 000000          CSRX: .WORD 0          ;POINTER TO CONTROL STATUS REGISTER
207 000330 000000          RARX: .WORD 0          ;POINTER TO BUS ADDRESS REGISTER
208 000332 000000          RCRX: .WORD 0          ;POINTER TO BYTE COUNT REGISTER
209 000334 000000          MCRX: .WORD 0          ;POINTER TO MATCH COUNT REGISTER

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211 .SBITL START-RESTART-TEST
212 00033b START:
213 00033b MOV DVID1,DVICE ;SAVE DEVICE INDICATOR TO A WORK LOCATION
214 000344 MOV #1,MSK ;SET UP FOR FIRST DEVICE
215 000352 MOV ADDR,H5 ;GET FIRST DEVICE ADDRESS
216 000356 MOV R5,CSRA ;LOAD CSRA FOR CKDATA ERROR PRINTOUT
217 000362 BIT MSK,DVICE ;THIS DEVICE SELECTED?
218 000370 BNE CHECK ;IF YES, BRANCH
219 000372 ADD #20,CSRA ;GET NEXT DEVICE ADDRESS
220 000400 MOV CSRA,H5 ;LOAD NEXT DEVICE ADDRESS
221 000404 ASLB MSK ;MOVE DEVICE MASK BIT
222 000410 BNE SET ;DONT CHECK MORE THAN 8 DEVICES
223 000412 NOP
224 000414 JMP START ;IF 8 DEVICES ARE CHECKED JUMP TO START
225 000420 JSR PC,SETUP ;GET DEVICE ADDRESSES
226 000424 CLR INTFLA ;CLEAR INTERRUPT FLAG 1
227 000430 CLR INTFLB ;CLEAR INTERRUPT FLAG 2
228 000434 JSR PC,CACS ;SET IEX CHANNEL 1 INTO CACS
229 *****
230 ; TEST DATA TRANSFER FROM CHANNEL 1 TO 2 WITH INTERRUPT
231 *****
232 000440 MOVB #212,#ACH ;---LOAD TON INTO ACR 1---
233 000446 MOVB #41,#DOR ;---LOAD M42 INTO DOR 1---
234 000454 JSR PC,LOOP ;WAIT A LITTLE
235 *****
236 000460 ;+++DMA SET UP FOR CHANNEL 1++++
237 JSR PC,WRITE ;DMA SET UP FOR WRITE CHANNEL (TX)
238 *****
239 000464 ;+++DMA SET UP FOR CHANNEL 2++++
240 BIS #10,#CSRX ;SELECT CHANNEL 2
241 JSR PC,READ ;DMA SET UP FOR READ CHANNEL (RX)
242 *****
243 000476 ;+++START DMA AND CHECK DATA *****
244 BIC #10,#CSRX ;SELECT CHANNEL 1
245 MOVB #13,#ACR ;LOAD GTS (START DMA)
246 EXITS,BEGIN ;EXIT TO MONITOR, MODULE WAIT FOR INTERRUPT.
247 *****
248 INTSV1:
249 ;-----
250 PIRQS,BEGIN,ITP1 ; QUEUE UP TO CONTINUE AT ITP1 AND RTI
251 ;-----
252 00051b 000004 000000 000524
253 ITR1: INC INTFLA ;INTERUPT HAS OCCURRED
254 NOP ;RETURN ADDRESS FROM INTERRUPT SERVICE ROUTINE
255 CDATAS,BEGIN,RBUFPA ; REQUEST FOR MONITOR TO CHECK DATA
256 .+2 ; IF ERROR, CONTINUE
257 NOP
258 *****
259 ; TEST DATA TRANSFER FROM CHANNEL 2 TO CHANNEL 1
260 *****
261 000544 CLR INTFLB ;
262 000550 CLR INTFLA ;
263 000554 CLR #CSRX ;CLEAR CSR ON CHANNEL 1
264 MOV #DIR,XMEM ;READ DIR FOR CLEAR 50 BIT AND ACCRU
265 MOVB #211,#ACH ;LOAD LON INTO ACR 1
266 BIS #10,#CSRX ;SELECT CHANNEL 2
267 MOV #10,#CSRX ;CLEAR CSR ON CHANNEL 2
268 MOVB #212,#ACH ;LOAD TON INTO ACR 2
269 *****
270 ;+++DMA SET UP FOR CHANNEL 1-----
271 BIC #10,#CSRX ;SELECT CHANNEL 1
272 JSR PC,READ ;LOAD DMA SET UP FOR READ CHANNEL (RX)
273 NOP
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264 ;+++DMA SET UP AND START FOR CHANNEL 2-----
265 000632 BIS #10,#CSRX ;SELECT CHANNEL 2
266 000640 NOP
267 000642 JSR PC,WRITE ;LOAD DMA SET UP FOR WRITE CHANNEL (TX)
268 000646 EXITS,BEGIN ;EXIT TO MONITOR, MODULE WAIT FOR INTERRUPT.
269 000652 INTSV2:
270 ;-----
271 PIRQS,BEGIN,ITR2 ; QUEUE UP TO CONTINUE AT ITR2 AND RTI
272 ;-----
273 000660 ITR2: INC INTFLB ;INTERUPT HAS OCCURRED
274 000664 NOP ;RETURN ADDRESS FROM INTERRUPT SERVICE ROUTINE
275 000666 CDATAS,BEGIN,RBUFPA ; REQUEST FOR MONITOR TO CHECK DATA
276 000674 .+2 ; IF ERROR, CONTINUE
277 000676 NOP
278 MOV #DIR,XMEM ;READ DIR
279 ENDITS,BEGIN ;SIGNAL END OF ITERATION.
280 ;MONITOR SHALL TEST END OF PASS
281 ;MONITOR SHALL TEST END OF PASS
282 JMP AGIAN ;IF NOT EQUAL JUMP TO AGIAN
```


IEAA OFC/X11 SYSTEM EXERCISER M MACRO M1113 29-JUL-82 08:03 PAGE 7
SUBROUTINES

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282          .SHTTL SUBROUTINES
283          :*****
284          : SUBROUTINE TO SETUP THE DEVICE ADDRESSES, VECTOR AND BH.
285          :*****
286          SETUP: MOV    R5,ISRX      ;GENERATE REGISTER ADDRESSES
287                  MOV    R5,MASK0    ;LOAD ADDRESS 0
288                  TSTB   (R5)+       ;TEST ADDRESS 0
289                  MOV    R5,MASK1    ;LOAD ADDRESS 1
290                  INC     R5         ;
291                  MOV    R5,IIRX     ;LOAD ADDRESS 2
292                  MOV    R5,ISRLX    ;
293                  TSTB   (R5)+       ;TEST ADDRESS 2
294                  MOV    R5,ADR      ;LOAD ADDRESS 3
295                  INC     R5         ;
296                  MOV    R5,ICRX     ;LOAD ADDRESS 4
297                  MOV    R5,SPH      ;
298                  TSTB   (R5)+       ;TEST ADDRESS 4
299                  MOV    R5,ACH      ;LOAD ADDRESS 5
300                  INC     R5         ;
301                  MOV    R5,DIR      ;LOAD ADDRESS 6
302                  MOV    R5,PPH      ;
303                  TSTB   (R5)+       ;TEST ADDRESS 6
304                  MOV    R5,DOR      ;LOAD ADDRESS 7
305                  INC     R5         ;
306                  MOV    R5,CSRX     ;LOAD ADDRESS 10
307                  TST    (R5)+       ;TEST ADDRESS 10
308                  MOV    R5,HAPX     ;LOAD ADDRESS 12
309                  TST    (R5)+       ;TEST ADDRESS 12
310                  MOV    R5,BCRX     ;LOAD ADDRESS 14
311                  TST    (R5)+       ;TEST ADDRESS 14
312                  MOV    R5,VCRX     ;LOAD ADDRESS 16
313                  MOV    VECTOR,R5   ;GET FIRST VECTOR ADDRESS
314                  MOV    #INTSV1,(R5)+ ;POINT VECTOR TO INTERRUPT SERVICE ROUTINE
315                  MOV    BR1,(R5)    ;SET PRIORITY FOR FIRST VECTOR
316                  MOV    VECTOR,R5   ;GET SECOND VECTOR ADDRESS
317                  ADD     #4,R5       ;...
318                  MOV    #INTSV2,(R5)+ ;POINT VECTOR TO INTERRUPT SERVICE ROUTINE
319                  MOV    BR1,(R5)    ;SET PRIORITY FOR SECOND VECTOR
320                  RTS     PC         ;RETURN TO USER

```

IEAA OFC/X11 SYSTEM EXERCISER M MACRO M1113 29-JUL-82 08:03 PAGE 8
SUBROUTINES

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322          :*****
323          : SUBROUTINE TO CLEAR AND SETUP BOTH IEX CHANNELS
324          :*****
325          CACS: CLR     #CSRX        ;SELECT CHANNEL 1
326                  MOV    #200,#ACH   ;LOAD SAKST INTO ACR 1
327                  MOV    #0,#ACR     ;LOAD NOT SAKST INTO ACR 1
328                  MOV    #0,#MASK0   ;CLEAR MASK REGISTER 0
329                  MOV    #0,#MASK1   ;CLEAR MASK REGISTER 1
330                  MOV    #0,#ADR      ;LOAD DEVICE PRIM. ADDR.1 INTO ADR 1
331                  MOV    #10,#CSRX   ;SELECT CHANNEL 2
332                  MOV    #200,#ACR   ;LOAD SAKST INTO ACR 2
333                  MOV    #0,#ACR     ;LOAD NOT SAKST INTO ACR 2
334                  MOV    #0,#MASK0   ;CLEAR MASK REGISTER 0
335                  MOV    #0,#MASK1   ;CLEAR MASK REGISTER 1
336                  MOV    #1,#ADR      ;LOAD DEVICE PRIM. ADDR.2 INTO ADR 2
337                  CLR     #CSRX      ;SELECT CHANNEL 1
338                  MOV    #217,#ACH   ;LOAD SIC INTO ACR 1
339                  MOV    #100,R1     ;WAIT 100 US
340          WAIT: BKA     BREAKS,REGIN ;TEMPORARY RETURN TO MONITOR....
341                  BKA     BREAKS,REGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
342                  DEC     R1         ;...
343                  BNE     WAIT       ;...
344                  MOV    #17,#ACH    ;LOAD NOT SIC INTO ACR 1
345                  RTS     PC         ;RETURN

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347 ;+++++*****
348 ; SUBROUTINE LOOP WAIT (AT LEAST) 1 US.
349 ;+++++*****
350 001252 012701 000001 LOOP: MOV #1,R1 ;LOAD LOOP COUNTER
351 001256 1S: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
      001256 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
      001262 104407 000000' DEC R1 ;DECREMENT LOOP COUNTER
352 001266 005301 BVE 1S ;LOOP UNTIL DONE
353 001270 001372 RTS PC ;RETURN
354 001272 000207
355

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357 ;+++++*****
358 ; SUBROUTINE WRITE (DMA SET UP)
359 ;+++++*****
360 001274 WRITE: G*BUFS, BEGIN ;GET WRITE BUFFER INFORMATION
      001274 104414 000000' MOV #BUFSZ,%CNT1 ;GET WRITE BUFFER SIZE TO A WORK LOCATION
361 001300 016767 176636 176744 ASL %CNT1 ;CORRECTION FOR CADATA (COMPARED *ORD)
362 001306 006367 176740 NEG %CNT1 ;GET 2'S COMPLEMENT
363 001312 005467 176734 MOV %CNT1,%CRCA ;LOAD BYTE COUNT REGISTER OF SELECTED CHANNEL
364 001316 016777 176730 177006 MOV #BUFPA,%BAPX ;LOAD BUFFER ADDRESS
365 001324 016777 176604 176776 MOV #BUFEA,%MEM ;GET EXTENDED MEMORY BITS
366 001332 016767 176600 176716 MOV #105,FUNC ;LOAD DMA FUNCTION
367 001340 012767 000105 176712 BIS %MEM,FUNC ;SET TO THE DMA FUNCTION EXTENDED MEMORY BITS
368 001346 056767 176704 176704 BIS FUNC,%CSFX ;LOAD DMA FUNCTION INTO CSX
369 001354 056777 176700 176744 RTS PC ;RETURN
370 001362 000207
371
372
373
374 ;+++++*****
375 ; SUBROUTINE READ (DMA SET UP)
376 ;+++++*****
377
378 001364 READ: GETPA,BEGIN, RBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
      001364 104415 000000' 000124' MOV RBUFVA,%CNT2 ;SAVE WRITE BUFFER SIZE
379 001372 016767 176534 176654 INC %CNT2 ;%CNT1+1 NO INTERRUPT ON RECEIVE CHANNEL
380 001400 005267 176650 ASL %CNT2 ;CORRECTION FOR CADATA (WORD COMPARE)
381 001404 006367 176644 NEG %CNT2 ;GET 2'S COMPLEMENT
382 001410 005467 176640 MOV %CNT2,%CRCA ;LOAD BYTE COUNT REGISTER
383 001414 016777 176634 176710 MOV #BUFPA,%BAPX ;LOAD BUFFER ADDRESS
384 001422 016777 176500 176700 MOV #BUFEA,%MEM ;LOAD EXTENDED MEMORY BITS
385 001430 016767 176474 176620 MOV #101,FUNC ;LOAD DMA FUNCTION
386 001436 012767 000101 176614 BIS %MEM,FUNC ;SET EXTENDED MEMORY BITS
387 001444 056767 176606 176606 NOP
388 001452 000240 BIS FUNC,%CSFX ;LOAD DMA FUNCTION INTO CSX
389 001454 056777 176600 176644 RTS PC ;RETURN
390 001462 000207
391
392

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IEAA DEC/X11 SYSTEM EXERCISER * MACRO *1113 29-JUL-82 08:03 PAGE 11
 BUFFER AREA

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394                                .SBTTL  BUFFER AREA
395
396
397 001464                        .BLK#  30                :PATCH AREA
398
399 001544                        BUFIN: .BLKB  1024.        :BUFFER IS 1024. BYTES = 512. *WORDS
400
401
402
403                000001                .END                :END OF PROGRAM

```

IEAA DEC/X11 SYSTEM EXERCISER * MACRO *1113 29-JUL-82 08:03 PAGE 11-1
 SYMBOL TABLE

ACR	000306RG	BUFIN	001544R	IIRX	000272R	PRTY0	= 000000	SPR	000304RG
ACSR	000102R	CACS	001102R	IMUDY	= 000000	PRTY1	= 000040	SPSIZ	= 000040
ADDR	000006R	CDATAS	= 104412	INIT	000030R	PRTY2	= 000100	SP1	000016R
ADDR22	= 001000	CDECT	000144R	INTFLA	000266R	PRTY3	= 000140	SR2	000020R
ADR	000274RG	CDADCT	000146R	INTFLB	000270R	PRTY4	= 000200	SR3	000022R
AGIAN	000424R	CHFCR	000420R	INTR	000120R	PRTY5	= 000240	SR4	000024R
ASR	000106R	CONFIG	000056R	INTSV1	000516R	PRTY6	= 000300	SIANT	000336R
ASTAT	000104R	CSRA	000100R	INTSV2	000652R	PRTY7	= 000310	STAT	000026R
AWAS	000110R	CSRX	000326R	ISRLX	000302R	PS	= 177776	SVRO	000062R
HARX	000330R	DATCKS	= 104411	ISRX	000300RG	PS*	= 177776	SVK1	000064R
HCWX	000332R	DATERS	= 104404	ITR1	000524R	PUSH	= 005746	SVK2	000066R
REGIN	000000R	DIR	000324R	ITR2	000660R	PUSH2	= 024646	SVK3	000070R
BIT0	= 000001	DJR	000322RG	LOOP	001252R	KANDS	= 104417	SVK4	000072R
BIT1	= 000002	DVICE	000262R	MAP27S	= 104416	KANNUN	000054R	SVK5	000074R
BIT10	= 002000	DVID1	000014R	MASK0	000312RG	KRUFEA	000130R	SVK6	000076R
BIT11	= 004000	ENDITS	= 104413	MASK1	000314RG	KRUFPA	000126R	SYSCNT	000052R
BIT12	= 010000	ENDS	= 104410	MCRX	000334R	KRUFSA	000132R	TRPDFD	= 000022
BIT13	= 020000	ERRTYP	000106R	MODNAM	000000R	KRUFVA	000124R	VECTOR	000010R
BIT14	= 040000	EXITS	= 104400	MODSP	000252R	READ	001364R	WAIT	001226R
BIT15	= 100000	FREE	000150R	MSGNS	= 104403	RESTFT	000372R	WASADR	000104R
BIT2	= 000004	FUNC	000260R	MSGSS	= 104402	RFS1	000056R	WBUFEA	000136R
BIT3	= 000010	GETPAS	= 104415	MSGS	= 104401	RFS2	000060R	WBUFFA	000134R
BIT4	= 000020	GWHUFS	= 104414	MSA	000264R	RSTRT	000112R	WBUFFJ	000140R
BIT5	= 000040	HRDCNT	000044R	NULL	= 000000	R6	= %000006	WBUFFSZ	000142R
BIT6	= 000100	HRDFRS	= 104405	OPEN	= 000000	R7	= %000007	ACR11	000252R
BIT7	= 000200	HRDPAS	000050R	OTUAS	= 104420	SeADP	000102R	ACR12	000254R
BIT8	= 000400	ICONT	000036R	PASCNT	000034R	SET	000356R	ADFR	000116R
BIT9	= 001000	ICOUNT	000040R	PIRQS	= 000004	SL1UP	000716R	ADTO	000114R
BREAKS	= 104407	ICRX	000310RG	POPSP	= 005726	SOFCNT	000042R	WRITE	001274R
BR1	000012R	IDNUM	000122R	POPSP2	= 022626	SUFERS	= 104406	XFLAG	000005R
BR2	000013R	IDRX	000316RG	PPR	000320RG	SUFFPAS	000046R	XMEM	000256R
BTDS	= 104421	IIRXA	000276RG	PRTY	= 000000	SPOINT	000032R		

.ARS. 000000 000
 003544 001
 ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 6962 WORDS (28 PAGES)
 DYNAMIC MEMORY: 8102 WORDS (31 PAGES)
 ELAPSED TIME: 00:00:14

1- 3 DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

RNAA DEC/X11 SYSTEM EXERCISER M MACRO V04.00 12-JAN-82 11:52:29 PAGE 2
DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

SEQ 0002

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.REM %

IDENTIFICATION

PRODUCT CODE: AC-T124A-MC
PRODUCT NAME: RM80 MODULE
PRODUCT DATE: JANUARY 1982
MAINTAINER: CX DIAGNOSTIC GROUP
AUTHOR: MIKE LEAVITT

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1. ABSTRACT

 CXRNAAO IS AN IUMODX THAT EXERCISES RM80 DISK DRIVES ON AN RH70
 CONTROLLER. IT EXERCISES THE DRIVES BY DOING SEEKS, WRITES,
 WRITE-CHECKS, READS, AND IN-CORE COMPARISONS. ALL ERRORS DETECTED
 ARE REPORTED ON THE CONSOLE TTY.

2. REQUIREMENTS

 PDP-11 PROCESSOR
 PROGRAM LOADING DEVICE
 TERMINAL
 RH70 CONTROLLER
 1 TO 8 DISK DRIVES (RM80'S)
 TEST MEDIA:
 THE RM80 DISK PACK(HDA) MUST BE FORMATTED IN 16 BIT DATA FORMAT.
 LOAD DEVICE:
 ANY DEVICE THAT IS SUPPORTED BY THE XXDP SOFTWARE PACKAGE.

3. START-UP

 ON THE INITIAL START, THE PROGRAM WILL CLEAR BIT0 OF 'SRI' AND TYPE
 THE FOLLOWING MESSAGES.
 'IF YOU WISH TO EXERCISE THE WHOLE DISK, SET BIT0
 IN SWITCH REGISTER 1(SRI) EQUAL TO 1.'
 'RNAAO ! OPERATING ON FE CYLINDERS ONLY !'
 THIS WILL OCCUR REGARDLESS OF THE CONDITION OF SRI (BIT0) AT
 CONFIGURE TIME.
 IF THE OPERATOR WISHES TO EXERCISE THE DRIVE ON ANY ADDRESS OUTSIDE
 THE "FE" CYLINDERS, SRI (BIT0) MUST BE MODIFIED AT LOCATION 16 OF
 CXRNAAO MODULE (SEE SECTION 6). THIS CAN BE ACCOMPLISHED BY USING THE
 'MOD' COMMAND SUPPLIED BY THE DECX RUN TIME SYSTEM. UNLESS THE
 PROGRAM IS RELOADED OR THE OPERATOR MODIFIES THE LOCATION AGAIN, THE
 CONTENTS OF SRI WILL REMAIN THE SAME ON ALL SUBSEQUENT STARTS.
 ON ALL SUBSEQUENT STARTS, THE CONDITION OF SRI (BIT0) WILL TYPE TO
 TERMINAL IN THE FOLLOWING MANNER.
 IF BIT0 OF SRI IS EQUAL TO 0 (ZERO), THE FOLLOWING WARNING WILL BE
 TYPED.
 'RNAAO ! OPERATING ON FE CYLINDERS ONLY !'

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IF BIT0 OF SRI IS EQUAL TO 1 (ONE), THE FOLLOWING WARNING WILL BE
 TYPED.
 'RNAAO ! CUSTOMER DATA WILL BE OVERWRITTEN !'

4. PASS DEFINITION

 ONE PASS OF THE CXRNAAO MODULE CONSISTS OF 200 CYCLES OF THE FOLLOWING
 TEST SEQUENCE FROM STEP 1 TO STEP 9.
 1. SELECT A STARTING BLOCK ADDRESS. (SEE SECTION 11.)
 2. SELECT A DRIVE FOR TEST. (ROUND-ROBIN)
 3. IF NO DRIVE IS SELECTED, DROP THE CXRNAAO MODULE.
 4. DO AN EXPLICIT SEEK TO A CYLINDER. IF ERRORS, REPORT AND RETRY
 UP TO RETRY LIMIT.
 5. DO A WRITE DATA - IF ERRORS, REPORT AND RETRY UP TO RETRY LIMIT.
 6. DO A WRITE CHECK DATA - IF ERRORS, REPORT AND RETRY UP TO
 RETRY LIMIT.
 7. DO A READ DATA - IF ERRORS, REPORT AND RETRY UP TO RETRY LIMIT.
 8. DO AN IN-CORE DATA COMPARE - IF ERRORS, REPORT.
 9. IF COMPLETED CYCLE OF ALL DRIVES, GO TO STEP 1, ELSE STEP 2.
 NOTE: THE RETRY LIMIT IS 3 TIMES. THE MODULE CLEARS THE ERROR,
 BEFORE EACH RETRY. IF THE MODULE FAILS TO CLEAR THE
 ERROR, THE MODULE WILL BE DROPPED.

5. EXECUTION TIME

 ONE PASS OF CXRNAAO RUNNING ALONE ON A PDP-11/70 TAKES APPROXIMATELY
 20 SECONDS.

6. CONFIGURATION REQUIREMENTS

 DEFAULT PARAMETERS:
 DEVADM: 170700, VECTOR: 254, BRI: 5, DEVCNT: 1
 REQUIRED PARAMETERS:
 NONE

7. DEVICE/OPTION SETUP

115 MAKE CERTAIN THAT ALL DRIVES ARE POWERED UP, WRITE ENABLED, AND READY
116
117
118
119
120 8. OPERATION OPTIONS
121 -----
122 MEANING OF SR1:
123
124 BIT00 = 1 ALLOW ACCESS TO ANY CYLINDER ADDRESS
125 BIT00 = 0 ALLOW ACCESS TO FE CYLINDER ADDRESSES ONLY
126
127 BIT02 = 1 COUNT DATA LATE ERRORS, BUT DON'T TYPE THEM.
128 BIT02 = 0 COUNT AND TYPE DATA LATE ERRORS.
129
130 BIT04 = 1 PORT B SELECTED
131 BIT04 = 0 PORT A SELECTED
132
133 BIT10 = 1 SELECT RANDOM BLOCK ADDRESSING.
134 BIT10 = 0 SELECT SEQUENTIAL BLOCK ADDRESSING.
135
136 BIT12 = 1 DUAL PORT OPERATION.
137 BIT12 = 0 SINGLE PORT OPERATION.
138
139 BIT15 = 1 32 REGISTERS ON RH70
140 BIT15 = 0 22 REGISTERS ON RH70
141
142 NOTE: BIT04 HAS NO MEANING IN SINGLE PORT OPERATION.
143
144 9. NON-STANDARD PRINTOUTS
145 -----
146
147 A. MUST PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN
148 THE DEC/X11 DOCUMENT.
149
150 B. ERROR MESSAGES DUMP THE CONTENTS OF THE 20 OR 22 REGISTERS.
151 (DEPENDING ON WHICH RH CONTROLLER IS USED)
152
153 IN THE FOLLOWING ORDER:
154
155 RMCS1 RMAC RMBA RMDA RMCS2 PMDS RMEH1 RMAS
156 RMLA RMD8 RMMH1 RMDT RMSN RMUF RMDC RMHR
157 RMMR2 RMRP2 RMEC1 RMEC2 *RMBAL *RMCS3
158
159 * PRINTED ON ERROR REPORT IF USING AN RH70 CONTROLLER
160
161
162 10. DUAL PORT SETUP:
163 -----
164 TO RUN A DUAL PORT SYSTEM, SR1 HAS TO BE MODIFIED TO INDICATE TO
165 THE MODULE, WHICH PORT THE MODULE IS LOCATED ON. (SEE SECTION 8)
166 FOR SR1 OPTIONS.
167
168 THE CONTROLLER SELECT SWITCH ON THE RM DRIVE MUST BE IN THE A/B
169 POSITION. THIS SWITCH IS ACTIVATED WHEN THE DRIVE IS CYCLED UP.
170 IF THE SWITCH WAS NOT IN THIS POSITION WHEN DRIVE WAS POWERED UP,
171

172 THE FOLLOWING STEPS MUST BE TAKEN. PLACE THE DUAL PORT SELECT
173 SWITCH IN THE A/B POSITION, CYCLE THE DRIVE DOWN TO THE LOAD
174 STATE AND THEN CYCLE THE DRIVE UP AND WAIT FOR READY. THIS WILL
175 PUT THE DRIVE IN THE DUAL PORT MODE.
176
177
178 11. BLOCK ADDRESS
179 -----
180
181 EACH SECTOR ON THE RM80 PACK IS CALLED A BLOCK.
182
183 AT THE BEGINNING OF EACH TEST CYCLE, THE CXRNA00 MODULE SELECTS
184 A STARTING BLOCK ADDRESS SEQUENTIALLY OR RANDOMLY FOR EACH
185 DATA TRANSFER.
186 WHEN BIT10 OF SR1 IS RESET(0), CXRNA00 MODULE SELECTS THE BLOCK
187 ADDRESS SEQUENTIALLY FROM BLOCK 0 TO THE LAST BLOCK.
188 WHEN BIT10 OF SR1 IS SET(1), CXRNA00 MODULE SELECTS THE BLOCK
189 ADDRESS RANDOMLY BETWEEN BLOCK 0 AND THE LAST BLOCK.
190
191 IN SINGLE PORT OPERATION, CXRNA00 MODULE ACCESSES EVERY BLOCK
192 ON TRACKS 0 THRU 13.
193
194 IN DUAL PORT OPERATION, CXRNA00 MODULE ON THE PORT A ACCESSES
195 EVERY BLOCK ON TRACKS 0 THRU 6.
196
197 IN DUAL PORT OPERATION, CXRNA00 MODULE ON THE PORT B ACCESSES
198 EVERY BLOCK ON TRACKS 7 THRU 13.
199
200 NOTE: THE MANUFACTURE/USER BAD SECTOR FILES AND THE SKIP
201 SECTOR FILE WILL NEVER BE ACCESSED IN ANY WAY BY
202 THIS MODULE.
203
204
205 12. BAD SECTOR TABLE
206 -----
207
208 32. LOCATIONS STARTING AT THE LABEL "BADSPT" ARE RESERVED
209 FOR 16. BAD SECTOR ENTRIES BY THE USER. EACH BAD SECTOR IS
210 SPECIFIED THROUGH ENTERING ITS CYLINDER ADDRESS TO THE
211 FIRST WORD AND ITS TRACK AND SECTOR ADDRESS TO THE SECOND
212 WORD. THE HIGH BYTE ON THE SECOND WORD IS THE TRACK ADDRESS.
213 WHILE THE LOW BYTE ON THE SECOND WORD IS THE SECTOR ADDRESS.
214 WHEN THE MODULE DETECTS A DATA TRANSFER ERROR, ON ANY OF
215 THESE SPOTS, THE MODULE WILL SKIP OVER THE BAD SECTOR AND
216 START ANOTHER TEST CYCLE.
217
218 THE MODULE ALSO DETECTS BAD SECTORS THROUGH THE "BSE" ERROR BIT
219 (BIT15 OF RMR2). WHENEVER THE "BSE" BIT IS SET AFTER EXECUTING
220 A DATA TRANSFER COMMAND, THE MODULE WILL IGNORE THE ERROR.
221
222 THE MODULE ALSO DETECTS BAD SECTORS THROUGH THE "SSE" ERROR BIT
223 (BIT05 OF RMR2). WHENEVER THE "SSE" BIT IS SET AFTER EXECUTING
224 A DATA TRANSFER COMMAND, THE MODULE WILL IGNORE THE ERROR, SET
225 "SSEI" (BIT09 OF RMOF), INCREMENT THE DISK ADDRESS BY ONE SECTOR
226 AND EXECUTE THE DATA COMMAND AGAIN.
227
228

229
 230
 231
 232
 233
 234

*

```

1
6
;LAST REVISION 04-AUG-81
; .TITLE RNAA DEC/X11 SYSTEM EXERCISER MODULE
; .DDACOM VERSION 6 23-MAY-78
; .LIST BIN
;*****
BEGIN:
MODNAM: .ASCII /RNAA / ;MODULE NAME.
XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUF USAGE
ADDR: 176700+0 ;1ST DEVICE ADDR.
VECTOR: 254+0 ;1ST DEVICE VECTOR.
BR1: .BYTE PRIY5+0 ;1ST BR LEVEL.
BR2: .BYTE PRIY0+0 ;2ND BR LEVEL.
DVID1: 0+1 ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1
SR2: OPEN ;SWITCH REGISTER 2
SR3: OPEN ;SWITCH REGISTER 3
SR4: OPEN ;SWITCH REGISTER 4
;*****
STAT: 150000 ;STATUS WORD.
INIT: START ;MODULE START ADDR.
SPOINT: MODSP ;MODULE STACK POINTER.
PASCNT: 0 ;PASS COUNTER.
ICOUNT: 200. ;# OF ITERATIONS PER PASS=200.
ICOUNT: 0 ;LOC TO COUNT ITERATIONS
SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
CONFIG: ;RESERVED FOR MONITOR USE
RES1: 0 ;RESERVED FOR MONITOR USE
RES2: 0 ;RESERVED FOR MONITOR USE
SVR0: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVRS: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSR.
SBADR: ;ADDR OF GOOD DATA, OR
ACSR: OPEN ;CONTENTS OF CSR.
ASADR: ;ADDR OF BAD DATA, OR
ASTAT: OPEN ;STATUS REG CONTENTS.
ERRTYP: ;TYPE OF ERROR
ASB: OPEN ;EXPECTED DATA.
AWAS: OPEN ;ACTUAL DATA.
RSTRT: RESTRI ;RESTART ADDRESS AFTER END OF PASS
WOTO: OPEN ;WORDS TO MEMORY PER ITERATION
WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
INTR: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0
RHUFVA: RBUF ;HEAD BUFFER VIRTUAL ADDRESS
RHUFPA: OPEN ;HEAD BUFFER PHYSICAL ADDRESS
RHUFEA: OPEN ;HEAD BUFFER EA BITS
RBUFSZ: 256. ;SIZE OF THE HEAD BUFFER
    
```



```

000134 000000      #BUFPA: OPEN      ;WRITE BUFFER PHYSICAL ADDRESS
000136 000000      #BUFEA: OPEN      ;WRITE BUFFER EA BITS
000140 000400      #BUFRW: 256.      ;WRITE BUFFER SIZE REQUESTED
000142 000000      #BUFSZ: OPEN      ;WRITE BUFFER SIZE AVAILABLE
000144 000000      CDEACT: OPEN      ;C/DATA/DATCK ERROR COUNT
000146 000000      CDWDOCT: OPEN     ;C/DATA/DATCK WORD COUNT
000150 000000      FREE: OPEN        ;RESERVED FOR FUTURE USE
000252 000040      .REPT      SPSIZ  ;MODULE STACK STARTS HERE.

MODSP:
;*****
7
8      ;USER DEFINED BAD SPOT TABLE
15 000252 177777    BADSP1: .WORD 177777
    000254 177777    .WORD 177777
    000256 177777    .WORD 177777
    000260 177777    .WORD 177777
    000262 177777    .WORD 177777
    000264 177777    .WORD 177777
    000266 177777    .WORD 177777
    000270 177777    .WORD 177777
    000272 177777    .WORD 177777
    000274 177777    .WORD 177777
    000276 177777    .WORD 177777
    000300 177777    .WORD 177777
    000302 177777    .WORD 177777
    000304 177777    .WORD 177777
    000306 177777    .WORD 177777
    000310 177777    .WORD 177777
    000312 177777    .WORD 177777
    000314 177777    .WORD 177777
    000316 177777    .WORD 177777
    000320 177777    .WORD 177777
    000322 177777    .WORD 177777
    000324 177777    .WORD 177777
    000326 177777    .WORD 177777
    000330 177777    .WORD 177777
    000332 177777    .WORD 177777
    000334 177777    .WORD 177777
    000336 177777    .WORD 177777
    000340 177777    .WORD 177777
    000342 177777    .WORD 177777
    000344 177777    .WORD 177777
    000346 177777    .WORD 177777
    000350 177777    .WORD 177777

17
18      ;DO NOT CHANGE THE ORDER OF THE NEXT 4 LOCATIONS, THEY ARE
19      ;NEEDED FOR MAP22 ROUTINE.
20 000352 000000    PA18: 0
21 000354 000000    XMEM: 0
22 000356 000000    PA22: 0
23 000360 000000    EA22: 0
24
25 000362 000000    DRPDV: 0      ;DROP DRIVE = 1
26 000364 000000    FLAG70: 0     ;BIT15 SET INDICATES THAT AN 11/70 PRESENT
27 000366 000000    CYLIND: 0     ;CYLINDER ADDRESS FOR DRIVER
28 000370 000000    DSKADR: 0     ;COMPOSITE TRACK AND SECTOR ADDRESS FOR DRIVER
29 000372 000000    DVICE: 0      ;DEVICE NUMBER

```

```

30 000374 000000    UNITNG: 0      ;# OF UNIT BEING TESTED
31 000376 000000    BADSEC: 0      ;HAD SECTOR FLAG, USER BAD SECTOR DETECTED = -1,
32                                     ;BSE = 100000 AND SSE = 40
33 000400 000000    DLTCNT: 0      ;DATA LATE ERROR COUNT
34 000402 000000    FUNC: 0        ;FUNCTION TO BE PERFORMED
35 000404 000000    ZERO: 0
36 000406 000000    FERADR: 0
37 000410 000000    CLK: 0        ;USED IN TIMEOUT LOOPS
38 000412 000000    TRY: 0
39 000414 000000    DRVTP: 0      ;DRIVE TYPE FLAG, RMH0=26
40 000416 000000    RMOP1: 0      ;INPUT STORAGE FOR OFFSET REGISTER
41 000420 000000    TRKMAP: 0     ;MAPPING FACTOR FOR TRACK ADDRESS
42 000422 000000    CYLMAP: 0     ;MAPPING FACTOR FOR CYLINDER ADDRESS
43
44 000424      RMUF: .BLKW 256.
45
46      ;RM/RM SAVED REGISTER POINTERS
47
48 001424 001606'    TABLE: S+NCS1
49 001426 001610'    S+NWC
50 001430 001612'    S+NHA
51 001432 001614'    S+NDA
52 001434 001616'    S+NCS2
53 001436 001620'    S+NDS
54 001440 001622'    S+NCR1
55 001442 001624'    S+NAS
56 001444 001626'    S+NLA
57 001446 001630'    S+NDB
58 001450 001632'    S+NMR1
59 001452 001634'    S+NOT
60 001454 001636'    S+NSN
61 001456 001640'    S+NDF
62 001460 001642'    S+NDC
63 001462 001644'    S+NHR
64 001464 001646'    S+NMR2
65 001466 001650'    S+NCR2
66 001470 001652'    S+NEC1
67 001472 001654'    S+NEC2
68 001474 001656'    S+NBAE
69 001476 001660'    S+NCS3
70 001500 177777    177777
71
72 001502 000000    RMCS1: 0      ;CONTROL STATUS REGISTER #1
73 001504 000000    RMWC: 0      ;WORD COUNT REGISTER
74 001506 000000    RMBA: 0      ;BUS ADDRESS REGISTER
75 001510 000000    RMDA: 0      ;DESIRED TRACK/SECTOR ADDRESS REGISTER
76 001512 000000    RMCS2: 0     ;CONTROL STATUS REGISTER #2
77 001514 000000    RMDS: 0      ;DRIVE STATUS REGISTER
78 001516 000000    RMR1: 0      ;ERROR REGISTER #1
79 001520 000000    RMAS: 0      ;ATTENTION SUMMARY REGISTER
80 001522 000000    RMLA: 0      ;LOOK AHEAD REGISTER
81 001524 000000    RMDB: 0      ;DATA BUFFER REGISTER
82 001526 000000    RMMR1: 0     ;MAINTENANCE REGISTER #1
83 001530 000000    RMDT: 0      ;DRIVE TYPE REGISTER
84 001532 000000    RMSN: 0      ;SERIAL NUMBER REGISTER
85 001534 000000    RMOF: 0      ;OFFSET REGISTER
86 001536 000000    RMDC: 0      ;DESIRED CYLINDER ADDRESS REGISTER

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87 001540 000000      RMHR: 0          ;HOLDING REGISTER
88 001542 000000      RMMR2: 0         ;MAINTENANCE REGISTER #2
89 001544 000000      RMER2: 0         ;ERROR REGISTER #2
90 001546 000000      RMEC1: 0         ;ECC POSITION REGISTER
91 001550 000000      RMFC2: 0         ;ECC PATTERN REGISTER
92 001552 000000      RMAE: 0          ;BUS ADDRESS EXTENSION REGISTER (RH70)
93 001554 000000      RMCS3: 0         ;CONTROL STATUS REGISTER #3 (RH70)
94
95 001556 000406'      XFERAD: FERADM
96 001560 177777      177777
97
98                      ;REGISTER ADDRESS INDEX VALUE
99
100 000000      NCS1 = 0
101 000002      NWC = 2
102 000004      NHA = 4
103 000006      NDA = 6
104 000010      NCS2 = 10
105 000012      NDS = 12
106 000014      NFR1 = 14
107 000016      NAS = 16
108 000020      NLA = 20
109 000022      NDB = 22
110 000024      NMR1 = 24
111 000026      NDT = 26
112 000030      NSN = 30
113 000032      NDF = 32
114 000034      NDC = 34
115 000036      NHR = 36
116 000040      NMR2 = 40
117 000042      NER2 = 42
118 000044      NEC1 = 44
119 000046      NEC2 = 46
120 000050      NHAE = 50
121 000052      NCS3 = 52
122
123                      ;DISK ADDRESS EQUATES FOR LIMITS TABLES
124
125 000000      FT = 0          ;FIRST TRACK
126 000002      LT = 2          ;LAST TRACK
127 000004      LS = 4          ;LAST SECTOR
128 000006      LC = 6          ;LAST CYLINDER
129 000010      RST = 10        ;BAD SECTOR TRACK (DEC 144 FILE)
130 000012      SEC = 12        ;CURRENT SECTOR
131 000014      TRK = 14        ;CURRENT TRACK
132 000016      CYL = 16        ;CURRENT CYLINDER
133 000020      FFE = 20        ;FIRST FE CYLINDER
134 000022      LFE = 22        ;LAST FE CYLINDER
135
136                      ;TABLE OF ADDRESS LIMITS FOR RM80
137
138                      ;NOTES: (1) WORD 1 (FIRST TRACK) AND WORD 2 (LAST TRACK) WILL BE
139                      ;      INITIALIZED AT PROGRAM START.
140                      ;
141                      ;      (2) DO NOT ALTER THE BAD SECTOR TRACK OR LAST CYLINDER
142                      ;      VALUES. IF EITHER VALUE IS CHANGED, THE BAD SECTOR
143                      ;      FILE(DEC 144 FILE) MAY BE DESTROYED.

```

```

144
145 001562 000000      TAR80: .WORD 0      ;FIRST TRACK
146 001564 000000      .WORD 0          ;LAST TRACK
147 001566 000036      .WORD 30         ;LAST SECTOR
148 001570 001056      .WORD 558        ;LAST CYLINDER
149 001572 000015      .WORD 13         ;BAD SECTOR TRACK (DEC 144 FILE)
150 001574 000000      .WORD 0          ;CURRENT SECTOR ADDRESS
151 001576 000000      .WORD 0          ;CURRENT TRACK ADDRESS
152 001600 000000      .WORD 0          ;CURRENT CYLINDER ADDRESS
153 001602 001057      .WORD 559        ;FIRST FE CYLINDER
154 001604 001060      .WORD 560        ;LAST FE CYLINDER
155
156 001606      S: .BLKW 22         ;STORE THE RM/RM REGISTER CONTENTS
157
158                      ;CONTROL STATUS REGISTER #1
159
160 100000      SC = 100000        ;SPECIAL CONDITION
161 040000      TRE = 40000        ;TRANSFER ERROR
162 200000      MCPE = 20000       ;CONTROL BUS PARITY ERROR
163 040000      DVA = 4000         ;DRIVE AVAILABLE
164 000200      RDY = 200          ;CONTROL READY
165 000001      GO = 1            ;GO bit
166
167                      ;CONTROL STATUS REGISTER #2
168
169 000040      CLR = 40           ;MASSBUS CLEAR COMMAND
170 000100      IR = 100          ;SILU INPUT READY
171 000200      OR = 200          ;SILU OUTPUT READY
172 000400      MDPE = 400        ;MASS BUS PARITY
173 001000      MXF = 1000        ;MISSED TRANSFER ERROR
174 002000      PGF = 2000        ;PROGRAM ERROR (RH)
175 004000      NEM = 4000        ;NON-EXISTENCE MEMORY
176 010000      NED = 10000       ;NON-EXISTENCE DRIVE
177 020000      UPE = 20000       ;UNINHUS PARITY ERROR
178 040000      WCE = 40000       ;WRITE CHECK ERROR
179 100000      DTL = 100000      ;DATA LATE ERROR
180
181                      ;RM DRIVE STATUS REGISTER
182
183 000001      OM = 1            ;OFFSET MODE
184 000100      VV = 100          ;VOLUME VALID
185 000200      DRY = 200         ;DRIVE READY
186 000400      DPR = 400         ;DRIVE PRESENT
187 001000      PGM = 1000        ;PROGRAMMABLE STATE
188 002000      LBT = 2000        ;LAST BLOCK TRANSFERRED
189 004000      WRL = 4000        ;WRITE LOCK
190 010000      MOL = 10000       ;MEDIUM ONLINE
191 020000      PIP = 20000       ;POSITIONING OPERATION IN PROGRESS
192 040000      ERR = 40000       ;DRIVE ERROR
193 100000      ATA = 100000      ;DRIVE ATTENTION
194
195                      ;RM ERROR REGISTER #1
196
197 000001      ILF = 1            ;ILLEGAL FUNCTION
198 000002      ILR = 2            ;ILLEGAL REGISTER
199 000004      RMR = 4            ;REGISTER MODIFICATION REFUSE
200

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```

201      000020      FER      = 20      ;FORMAT ERROR
202      000040      WCF      = 40      ;WRITE CLOCK FAIL
203      000100      ECH      = 100     ;HARD ERROR ECC
204      000200      HCE      = 200     ;HEADER COMPARE FAIL
205      000400      HCRC     = 400     ;HEADER CRC ERROR
206      001000      AOE      = 1000    ;ADDRESS OVERFLOW ERROR
207      002000      IAE      = 2000    ;INVALID ADDRESS ERROR
208      004000      WLE      = 4000    ;WRITE LOCK ERROR
209      010000      DTE      = 10000   ;DRIVE TIMING ERROR
210      020000      OPI      = 20000   ;OPERATION INCOMPLETE
211      040000      UNS      = 40000   ;DRIVE UNSAFE
212      100000      DCK      = 100000  ;DATA CHECK ERROR
213
214      ;RM ERROR REGISTER #2
215
216      000010      DPE      = 10      ;DATA PARITY DURING WRITE
217      000040      SSE      = 40      ;SKIP SECTOR ERROR
218      000200      DVC      = 200     ;DEVICE CHECK
219      002000      LBC      = 2000    ;LOST BIT CLOCK
220      004000      LSC      = 4000    ;LOST SYSTEM CLOCK
221      040000      SKI      = 40000   ;SEEK INCOMPLETE
222      100000      BSE      = 100000  ;BAD SECTOR ERROR
223
224      ;RM OFFSET REGISTER
225
226      010000      FMT      = 10000   ;16 BIT DATA FORMAT
227      001000      SSE1     = 1000    ;SKIP SECTOR ERROR INHIBIT

```

```

1      ;SBTTL START OF PROGRAM
2
3      001062 012767 000400 176224 START: MOV      #256,,WDTO      ;256 WORDS TO MEM/ITERATION
4      001070 012767 000400 176220 MOV      #256,,WDFM      ;256 WORDS FROM MEM/ITERATION
5      001076 012767 000012 176214 MOV      #10,,INTR      ;10 INTERRUPTS/ITERATION
6      001104 016767 176104 176460 MOV      DVID1,DVICE      ;GET DRIVES TO TEST
7      001112 016706 176114 MOV      SP0INT,R6      ;INITIATE STACK POINT
20
21      ;SETUP DRIVE PARAMETERS TABLE ACCORDING TO 'SR1' CONTENTS, ISSUE A
22      ;PACK ACKNOWLEDGE AND RECALIBRATE COMMAND TO DRIVES
23
24      001066 012700 001562' SETBL: MOV      #TAB80,R0      ;GET POINTER TO TABLE
25      001772 012760 000007 000000 MOV      #7,,FT(R0)      ;USE THE UPPER PORTION OF THE DISK
26      002000 012760 000015 000002 MOV      #13,,LT(R0)      ;IF THE DUAL PORT AND PORT B ARE SELECTED
27      002006 016701 176004 MOV      SR1,R1      ;GET SWITCH REGISTER #1
28      002012 042701 167757 BIC      #C<H12:BIT4>,R1      ;
29      002016 022701 010020 CMP      #BIT12:BIT4,R1      ;DUAL PORT AND PORT B ?
30      002022 001411 BEQ      1$      ;RR IF YES
31      002024 012760 000000 000000 MOV      #0,,FT(R0)      ;OTHERWISE SET THE FIRST TRACK TO 0
32      002032 032701 010000 BIT      #BIT12,R1      ;DUAL PORT OPERATION ?
33      002036 001403 BEQ      1$      ;RR IF NO
34      002040 012760 000006 000002 MOV      #0,,LT(R0)      ;SET UP THE UPPER TRACK LIMIT
35
36      002046 012760 177777 000012 1$: MOV      #-1,SEC(R0)      ;INITIALIZE THE SECTOR ADDRESS
37      002054 011060 000014 MOV      (R0),TRK(R0)      ;INITIALIZE THE TRACK ADDRESS
38      002060 012760 000000 000016 MOV      #0,CYL(R0)      ;INITIALIZE THE CYLINDER ADDRESS
39      002066 032767 000001 175722 HIT      #BIT0,SR1      ;FE CYLINDER ONLY ?
40      002074 001003 BNE      2$      ;RR IF NO
41      002076 016060 000020 000016 MOV      FFE(R0),CYL(R0) ;GET STARTING ADDRESS FOR FE CYLINDERS
42      002104 005067 176270 CLR      DLTCTR      ;RESET THE DATA LATE ERROR COUNT
43      002110 004767 003560 JSH      PC,SETUP      ;SETUP REGISTER ADDRESSES AND PSEL BIT
44      002114 012767 177777 176252 MOV      #-1,UNITNO      ;PRE-SET UNIT NUMBER
45
46      002122 004767 001502 3$: JSH      PC,GETDRV      ;GO GET DRIVE
47      002126 000415 BR      4$      ;RETURN HERE WHEN ALL DRIVES ARE DONE
48      002130 004567 JSH      R5,READY      ;SEE IF DRIVE IS READY FOR COMMANDS
49      002134 000772 BR      3$      ;RETURN HERE ON ERROR
50      002136 012777 000023 177330 MOV      #23,,RMCS1      ;GO ISSUE A PACK ACKNOWLEDGE COMMAND TO
51      ;SET "VV" BIT IN DRIVE STATUS.
52      002144 012777 000007 177330 MOV      #7,,RMCS1      ;ISSUE RECALIBRATE COMMAND AND
53      002152 004767 003234 JSH      PC,WICRDY      ;WAIT FOR CONTROLLER READY
54      002156 000240 NOP      ;RETURN HERE IF ERROR
55      002160 000760 BR      3$      ;RETURN--DO NEXT DRIVE
56      002162 000404 BR      4$      ;START THE PASS
57
58      002164 005767 175644 RESTRT: TST      PASCNT      ;SUPPORT DT03
59      002170 001001 HNE      RSTRT1
60      002172 000633 BR      START
61
62      002174 RSTR11: GETPAS,BEGIN, RBUFVA      ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
63      002174 104415 000000' 000124'
64      002202 012777 000040 177302 LOOP1: MOV      #CLR,,RMCS2      ;ISSUE A CLEAR COMMAND
65      002210 012767 177777 176156 MOV      #-1,UNITNO      ;PRE-SET UNIT NUMBER
66      002216 004767 000752 JSH      PC,GENADR      ;GENERATE BLOCK ADDRESS
67
68      002222 012767 010000 176166 LOOP2: MOV      #FMT,RMOFI      ;SET 16 BIT FORMAT

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69	002230	004767	001374	JSR	PC,GETDRV	;GO GET DRIVE
70	002234	000413		BR	ZS	;RETURNS HERE IF ALL DRIVES DONE
71	002236	004567	002672	JSR	RS,READY	;SEE IF DRIVE IS READY
72	002242	000767		BR	LOOP2	;LOOP BACK IF NOT READY
73						
74	002244	004767	001330	JSR	PC,LOADR	;LOAD BLOCK ADDRESS
75	002250	004767	000022	JSR	PC,CYCLE	;COMMAND SEQUENCE ROUTINE
76	002254	000240		NOP		;ERROR EXIT FOR CYCLE ROUTINE
77						
78	002256					
	002256	104413	000000'	1S:	ENDITS,BEGIN	;SIGNAL END OF ITERATION.
						;MONITOR SHALL TEST END OF PASS
79	002262	000757		BR	LOOP2	;DO IT TO NEXT DRIVE
80						
81	002264	005767	176102	2S:	TSI	;ARE ALL DEVICES DROPPED ?
82	002270	001344		BNE	LOOP1	;BR IF NO
83						
84	002272	104410	000000'		ENDS,BEGIN	;

				.SBTTL	SUBROUTINES	
1						
2						
3						
4						
5						
6						
7						
8	002276	005067	176110			
9	002302	104414	000000'			
10	002306	004767	002736			
11	002312	000437				
12	002314	004567	000334			
13	002320	000434				
14	002322	004567	000106			
15	002326	000431				
16	002330	004767	002714			
17	002334	000426				
18	002336	004567	000312			
19	002342	000423				
20	002344	004567	000144			
21	002350	000420				
22	002352	004767	002672			
23	002356	000415				
24	002360	004567	000270			
25	002364	000412				
26	002366	004567	000202			
27	002372	000407				
28	002374	104412	000000' 000126'			
	002402	002432'				
29	002404	062716	000002			
30	002410	000410				
31						
32	002412	005767	175744	1S:	TSI	;DROP THE DRIVE ?
33	002416	001405		BEG	ZS	;BR IF NO
34	002420	004767	001252	JSR	PC,DROP	;DROP THE DRIVE
35	002424	104403	000000' 007522'	MSGNS,BEGIN,DRM	;ASCII	MESSAGE CALL WITH COMMON HEADER
36	002432	000207		RTS	PC	;EXIT
37						

```

1      .SHTTL  COMMAND SUBROUTINES
2
3      ;CALL
4      ;
5      ;      JSH      R5,COMMAND      ;CALL COMMAND ROUTINE
6      ;      BR       ?              ;ERROR RETURN
7      ;      RET              ;NORMAL RETURN
8
9      ;THE ROUTINE COMMAND NAMES:
10     ;WRITE = *WRITE DATA, USING *WRITE BUFFER,
11     ;WRITCK = *WRITE CHECK DATA, USING *WRITE BUFFER,
12     ;READ = *READ DATA, USING READ BUFFER
13     ;SEEK = *SEEK COMMAND
14
15     002434 012767 000161 175740 WRITE: MOV      #161,FUNC      ;LOAD WRITE FUNCTION
16     002442 012767 002434' 175736 MOV      #WRITE,FERADH    ;SAVE WHERE WE WERE
17     002450 016746 175466 MOV      #BUFSZ,-(SP)    ;GET WRITE SIZE
18     002454 005416 NEG      (SP)              ;NEGATE IT
19     002456 012677 177022 MOV      (SP)+,@RMC      ;LOAD WORD COUNT
20     002462 016777 175446 17701b MOV      #BUFPA,@RMA    ;LOAD BUFFER ADDRESS
21     002470 016746 175442 MOV      #BUFEA,-(SP)    ;GET EXTENDED MEMORY BITS
22     002474 006316 ASL      (SP)              ;SHIFT 4 PLACES TO THE LEFT
23     002476 006316 ASL      (SP)              ;TO LINE UP WITH RMCS1
24     002500 006316 ASL      (SP)
25     002502 006316 ASL      (SP)
26     002504 012667 175644 MOV      (SP)+,XMEM      ;SAVE THE SHIFTED BITS
27     002510 000167 000160 JMP      GO1              ;CONTINUE
28
29     002514 012767 000151 175660 WRITCK: MOV      #151,FUNC      ;LOAD WRITE-CHECK FUNCTION
30     002522 012767 002514' 175656 MOV      #WRITCK,FERADH    ;SAVE WHERE WE WERE
31     002530 016746 175406 MOV      #BUFSZ,-(SP)    ;GET WRITE SIZE
32     002534 005416 NEG      (SP)              ;NEGATE IT
33     002536 012677 176742 MOV      (SP)+,@RMC      ;LOAD WORD COUNT
34     002542 016777 175366 176736 MOV      #BUFPA,@RMA    ;LOAD BUFFER ADDRESS
35     002550 016746 175362 MOV      #BUFEA,-(SP)    ;GET EXTENDED MEMORY BITS
36     002554 006316 ASL      (SP)              ;SHIFT 4 PLACES TO THE LEFT
37     002556 006316 ASL      (SP)              ;TO LINE UP WITH RMCS1
38     002560 006316 ASL      (SP)
39     002562 006316 ASL      (SP)
40     002564 012667 175564 MOV      (SP)+,XMEM      ;SAVE THE SHIFTED BITS
41     002570 000167 000100 JMP      GO1              ;CONTINUE
42
43     002574 012767 000171 175600 READ:  MOV      #171,FUNC      ;LOAD READ FUNCTION
44     002602 012767 002574' 175576 MOV      #READ,FERADH    ;SAVE WHERE WE WERE
45     002610 016746 175316 MOV      #BUFSZ,-(SP)    ;GET READ SIZE
46     002614 005416 NEG      (SP)              ;NEGATE IT
47     002616 012677 176662 MOV      (SP)+,@RMC      ;LOAD WORD COUNT
48     002622 016777 175300 176656 MOV      #BUFPA,@RMA    ;LOAD BUFFER ADDRESS
49     002630 016746 175274 MOV      #BUFEA,-(SP)    ;GET EXTENDED MEMORY BITS
50     002634 006316 ASL      (SP)              ;SHIFT 4 PLACES TO THE LEFT
51     002636 006316 ASL      (SP)              ;TO LINE UP WITH RMCS1
52     002640 006316 ASL      (SP)
53     002642 006316 ASL      (SP)
54     002644 012667 175504 MOV      (SP)+,XMEM      ;SAVE THE SHIFTED BITS
55     002650 000167 000020 JMP      GO1              ;CONTINUE
56
57     002654 012767 000105 175520 SEEK: MOV      #105,FUNC      ;SEEK COMMAND

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77 002662 012767 002654' 175516 MOV      #SEEK,FERADH    ;CALLING ADDRESS
78 002670 000167 000104 JMP      GO2              ;EXECUTE THE SEEK COMMAND

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1      .SBTTL  COMMAND EXECUTE ROUTINE
2
3 002674 005767 175464      GD1: 1ST  FLAG70      ;11/70???
4 002700 100034              BPL  1$            ;NO
5 002702 017767 176600 175442  MOV  @RMBA,PA1H    ;GET 18 BIT ADDR
6 002710 006267 175440      ASR  XMEM            ;SHIFT EA BITS TO POSITION 4,5
7 002714 006267 175434      ASR  XMEM
8 002720 006267 175430      ASR  XMEM
9 002724 006267 175424      ASR  XMEM
10 002730 104416 000000' 000352' MAP22$, BEGIN,PA1H      ; GET 22-BIT ADDR FROM 18-BIT ADDR
11 002736 016777 175414 176542  MOV  PA22,@RMBA    ;LOAD BA REG
12 002744 016777 175410 176600  MOV  EA22,@RMBAE    ;LOAD HAE REG
13 002752 042767 000074 175400  RLC  #74,EA22    ;CLEAR UNWANTED BITS
14 002760 000367 175374      SWAH  EA22            ;LOAD INTO BITS 8,9
15 002764 016767 175370 175362  MOV  EA22,XMEM    ;LOAD XMEM TO SET INTO FUNCTION CODE
16 002772 056767 175356 175402 1$: BIS  XMEM,FUNC    ;LOAD EXTENDED MEMORY BITS
17
18 003000 016777 175412 176526  GD2: MOV  RMOF1,@RMUF    ;LOAD OFFSET REGISTER
19 003006 016777 175354 176522  MOV  CYLIND,@RMDC    ;LOAD THE CYLINDER ADDRESS
20 003014 016777 175350 176466  MOV  DSKADR,@RMDA    ;LOAD THE TRACK AND SECTOR ADDRESS
21 003022 016777 175354 176452  MOV  FUNC,@RMCS1    ;EXECUTE THE FUNCTION
22 003030 104400 000000'      EXITS,BEGIN      ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
  
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1      .SBTTL  CHECK FOR ERRORS
2
3 003034      NTRUPT:
4
5 003034 000004 000000' 003042'  ;-----
6                                ;PIRMS,BEGIN,1$      ; QUEUE UP TO CONTINUE AT 1$ AND RTI
7                                ;-----
8
9
10 003056 005767 175300      2$: 1ST  DRPDRV      ;DROP DRIVE ?
11 003062 001043              BNE  6$            ;BR IF YES
12 003064 004567 002044      JSR  R5,READY      ;SEE IF DRIVE IS READY FOR COMMAND AGAIN
13 003070 000440              BR   6$            ;BR TO 6$ ON ERROR
14 003072 022767 177777 175276  CMP  #-1,BADSEC    ;USR DEFINED BAD SPOT ?
15 003100 001434              BEQ  6$            ;BR IF YES
16 003102 032767 000040 175266  BIT  #SSE,bADSEC    ;SKIP SECTOR ERROR ?
17 003110 001406              BEQ  3$            ;BR IF NO
18 003112 012767 011000 175276  MOV  #FMTISSEI,RMOF1    ;SET 16 BIT FORMAT WITH SKIP SECTOR INHIBIT
19 003120 005267 175244      INC  DSKADR      ;PERFORM COMMAND ON NEXT SECTOR
20 003124 000420              BR   5$
21 003126 032767 100000 175242 3$: BIT  #BSE,bADSEC    ;BAD SECTOR ERROR ?
22 003134 001016              BNE  6$            ;BR IF YES
23
24 003136 005267 175250      INC  TRY            ;INCREMENT THE RETRY COUNT
25 003142 026727 175244 000003  CMP  TRY,#3      ;OVER 3 RETRIES ?
26 003150 103406              BLO  5$            ;BR IF NOT
27 003152 104403 000000' 007376' MSGMS,BEGIN,EXCED    ;ASCII MESSAGE CALL WITH COMMON HEADER
28 003160 005067 175226      4$: CLM  IPY
29 003164 000402              BR   6$            ;EXIT
30
31 003166 162705 000004      5$: SUB  #4,R5
32 003172 000205              RTS   R5            ;ADJUST ADDRESS FOR RETRY
  
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.SBTTL GENERATE DISK ADDRESS			
;THIS ROUTINE IS USED TO GENERATOR A DISK ADDRESS ON WHICH THE			
;DRIVE WILL SEEK, WRITE, WRITE CHECK AND READ ON.			
;			
;CALL			
JSR	PC,GENADR	;CALL GENERATOR ROUTINE	
GENADR:	MOV	#TAHR0,R0	;GET RM80 LIMITER AND STORAGE TABLE
15:	BIT	#BIT0,SR1	;CHECK SR1 FOR RANDOM ACCESS MODE
	BEQ	6S	;BR IF NO
	RANDS,BEGIN		
	MOV	RANRUM,SEC(R0)	;GENERATE THE SECTOR ADDRESS
	BIC	#177400,SEC(R0)	;CHUP OFF HIGH BYTE
	RANDS,BEGIN		
	MOV	RANRUM,=(SP)	;GENERATE THE TRACK ADDRESS
	BIC	#177400,(SP)	;CHUP OFF HIGH BYTE
	RANDS,BEGIN		
	MOV	RANRUM,=(SP)	;GENERATE THE CYLINDER ADDRESS
	BIC	#176000,(SP)	;CHUP OFF HIGH BITS
25:	JSR	PC,GETMAP	;CALL MAPPING ROUTINE
	SUB	CYLMAP,(SP)	;OVER THE CYLINDER LIMIT ?
	BGE	2S	;BR IF YES
	ADD	CYLMAP,(SP)	;ADD MAPPING FACTOR BACK
	BIT	#BIT0,SR1	;FE CYLINDER ONLY ?
	BNE	3S	;BR IF NO
	ADD	FFE(R0),(SP)	;ADJUST TO STARTING CYLINDER
35:	MOV	(SP)+,CYL(R0)	;SAVE CYLINDER ADDRESS
45:	SUB	TRKMAP,(SP)	;OVER THE TRACK LIMIT ?
	BGE	4S	;BR IF YES
	ADD	TRKMAP,(SP)	;ADD MAPPING FACTOR BACK
	ADD	FT(R0),(SP)	;ADJUST TO STARTING TRACK
	MOV	(SP)+,TRK(R0)	;SAVE TRACK ADDRESS
55:	CMP	LS(R0),SEC(R0)	;OVER THE SECTOR LIMIT ?
	BHS	5S	;BR IF NOT
	ASH	SEC(R0)	;REDUCE TO HALF
	BR	5S	;CHECK AGAIN
;GENERATE DISK ADDRESS BY SEQUENTIAL ADDRESSING			
65:	INC	SEC(R0)	;SEQUENTIAL ACCESS TO ALL SECTORS
	CMP	LS(R0),SEC(R0)	;TIME TO ADJUST THE TRACK ADDRESS
	BHS	6S	;BR IF NOT
	CLR	SEC(R0)	;RESET THE SECTOR ADDRESS
	INC	TRK(R0)	;INCREMENT THE TRACK ADDRESS
	LT(R0),TRK(R0)		;OVER LIMIT ?
	BHS	7S	;BR IF NOT
	MOV	FT(R0),TRK(R0)	;RESET THE STARTING TRACK ADDRESS
	INC	CYL(R0)	;ADJUST THE CYLINDER ADDRESS
	CMP	LFE(R0),CYL(R0)	;OVER LIMIT ?
	BHS	8S	;BR IF NOT
	BIT	#BIT0,SR1	;FE CYLINDER ONLY ?

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BEQ	7S	;BR IF YES	
CLR	CYL(R0)	;ELSE, RESET CYLINDER ADDRESS	
BR	8S		
MOV	FFE(R0),CYL(R0)	;GET FIRST FE CYLINDER	
;CHECK FOR MFG/USR AND SKIP SECTOR FILES IN TRACK ADDRESS			
85:	CMP	LC(R0),CYL(R0)	;IS IT THE LAST CYLINDER ?
	BNE	9S	;BR IF NO
	HST(R0),TRK(R0)	;IS IT THE BAD SECTOR TRACK (DEC 144) ?	
	BEQ	1S	;YES, THIS IS BAD SECTOR FILE
		;GET ANOTHER ADDRESS	
95:	CMP	FFE(R0),CYL(R0)	;FIRST FE CYLINDER ?
	BNE	10S	;BR IF NO
	CMP	TRK(R0),#1	;IS THIS TRACK 0 OR 1 ON FFE CYLINDER ?
	HLE	1S	;GET ANOTHER ADDRESS
105:	RTS	PC	
;THIS ROUTINE IS USED TO MAP OUT THE RANGE OF TRACK AND CYLINDER			
;ADDRESSES USED IN THE RANDOM GENERATING ROUTINE.			
;			
;CALL			
JSR	PC,GETMAP	;CALL ROUTINE	
GETMAP:	BIT	#BIT0,SR1	;FE CYLINDER ONLY ?
	BEQ	1S	;BR IF YES
	CLR	R2	;GET FIRST CYLINDER
	BR	2S	
15:	MOV	FFE(R0),R2	;GET FIRST FE CYLINDER
25:	MOV	LFE(R0),R3	;GET LAST FE CYLINDER
	DEC	R2	;START ONE LESS THAN FIRST CYLINDER
	SUB	R2,R3	;CALCULATE MAPPING FACTOR AND
	MOV	R3,CYLMAP	;SAVE IT.
	MOV	FT(R0),R2	;GET FIRST TRACK AND
	DEC	R2	;START ONE TRACK LESS.
	MOV	LT(R0),R3	;GET LAST TRACK
	SUB	R2,R3	;CALCULATE MAPPING FACTOR AND
	MOV	R3,TRKMAP	;SAVE IT.
	RTS	PC	
;THIS ROUTINE IS USED TO LOAD THE SECTOR, TRACK AND CYLINDER ADDRESSES			
;FOR THE DEVICE UNDER TEST.			
;			
;CALL			
JSR	PC,LOADR	;CALL LOAD ADDRESS ROUTINE	
LOADR:	MOV	#TAB80,R0	;GET RM80 TABLE POINTER
	MOV	SEC(R0),DSKADR	;LOAD SECTOR ADDRESS
	MOV	TRK(R0),DSKADR+1	;LOAD TRACK ADDRESS
	MOV	CYL(R0),CYLIND	;LOAD CYLINDER ADDRESS
	RTS	PC	;EXIT
;ROUTINE TO GET A DRIVE FOR TESTING, FROM LOCATION 'DEVICE'			
;			
;CALL			
JSR	RC,GETDRV	;CALL ROUTINE	
RET		;RETURN HERE DRIVE IS NOT SELECTED	

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ERROR HANDLER ROUTINE

SEQ 0024

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1      .SBTTL  ERROR HANDLER ROUTINE
2
3      :CALL
4      :
5      :   JSH      RS,ERRORS      :CHECK IF ANY ERROR UN RH/RM
6      :   RET
7      :   RET2     :ERROR RET
8      :           :NORMAL RET
9
10     ERRORS: CLR      BADSEC      :CLEAR BAD SPOT FLAG
11             JSH      PC,ERSUB1   :SET UP FOR ERROR REPORT
12             JSH      PC,GET      :READ AND STORE ALL REGISTERS
13             MOV      #40011,WRMCS1 :ISSUE DRIVE CLEAR COMMAND
14             MOV      S+NC51,R0    :GET CONTROL STATUS
15             BIC      #*C<RDY>:GU>,R0
16             CMP      #RDY,R0      :RDY = 1 AND GU = 0 ?
17             BEQ      1S           :BR IF YES
18             MOV      #1,DRPDREV   :SET DROP DRIVE FLAG
19             MSGNS,BEGIN,NUTRDY    :ASCII MESSAGE CALL WITH COMMON HEADER
20             MOV      #3,ERRTYP    :SAVE THE ERROR TYPE
21             ;*****
22             HRDEKS,BEGIN,TABLE    :CONTROLLER NOT READY
23             ;*****
24             JMP      32S
25
26     004022 104405 000000' 001424'
27
28     20 004030 000167 001076
29
30     22 004034 016700 175560      1S: MOV      S+ND5,R0      :GET DRIVE STATUS
31             COM      R0
32             BIT      #DRY:IDPR:VV:MOL,R0      :DRIVE READY ?
33             BEQ      2S           :BR IF YES
34             MOV      #1,DRPDREV   :SET DROP DRIVE FLAG
35             MSGNS,BEGIN,NUTRDY    :ASCII MESSAGE CALL WITH COMMON HEADER
36             MOV      #0,ERRTYP    :SAVE THE ERROR TYPE
37             ;*****
38             HRDEKS,BEGIN,TABLE    :DRIVE NOT READY
39             ;*****
40             JMP      32S
41
42     30 004100 000167 001026
43
44     32 004104 016700 175476      2S: MOV      S+NC51,R0      :CHECK IF SEEK COMMAND IS EXECUTED
45             BIC      #177701,R0    :LEFT ONLY FUNCTION BITS
46             CMP      #4,R0        :A SEEK COMMAND ?
47             BNE      3S           :BR IF NOT
48             MOV      S+ND5,R0      :CHECK THE STATUS
49             BIC      #*C<ATA>:PIP>,R0
50             CMP      #ATA,R0      :ATA = 1 AND PIP = 0 ?
51             BEQ      4S           :BR IF YES
52             MOV      #13,WRMCS1    :RELEASE THE DRIVE
53             MSGNS,BEGIN,SEEKER    :ASCII MESSAGE CALL WITH COMMON HEADER
54             JMP      31S
55
56     32 004110 016700 175476
57             MOV      S+ND5,R0      :CHECK THE STATUS
58             BIC      #*C<ATA>:PIP>,R0
59             CMP      #ATA,R0      :ATA = 1 AND PIP = 0 ?
60             BEQ      4S           :BR IF YES
61             MOV      #13,WRMCS1    :RELEASE THE DRIVE
62             MSGNS,BEGIN,SEEKER    :ASCII MESSAGE CALL WITH COMMON HEADER
63             JMP      31S
64
65     34 004114 022700 000004
66             MOV      S+ND5,R0      :CHECK THE STATUS
67             BIC      #*C<ATA>:PIP>,R0
68             CMP      #ATA,R0      :ATA = 1 AND PIP = 0 ?
69             BEQ      4S           :BR IF YES
70             MOV      #13,WRMCS1    :RELEASE THE DRIVE
71             MSGNS,BEGIN,SEEKER    :ASCII MESSAGE CALL WITH COMMON HEADER
72             JMP      31S
73
74     36 004122 016700 175472
75             MOV      S+ND5,R0      :CHECK THE STATUS
76             BIC      #*C<ATA>:PIP>,R0
77             CMP      #ATA,R0      :ATA = 1 AND PIP = 0 ?
78             BEQ      4S           :BR IF YES
79             MOV      #13,WRMCS1    :RELEASE THE DRIVE
80             MSGNS,BEGIN,SEEKER    :ASCII MESSAGE CALL WITH COMMON HEADER
81             JMP      31S
82
83     38 004136 001413
84             MOV      #13,WRMCS1    :RELEASE THE DRIVE
85             MSGNS,BEGIN,SEEKER    :ASCII MESSAGE CALL WITH COMMON HEADER
86             JMP      31S
87
88     40 004140 012777 000013 175334
89             MOV      #13,WRMCS1    :RELEASE THE DRIVE
90             MSGNS,BEGIN,SEEKER    :ASCII MESSAGE CALL WITH COMMON HEADER
91             JMP      31S
92
93     42 004154 000167 000736
94
95     44 004160 012777 000013 175314 3S: MOV      #13,WRMCS1    :RELEASE THE DRIVE
96             BIT      #MCP:ITRE,S+NC51 :ANY RH CONTROLLER ERROR DETECTED ?
97             BNE      5S           :BR IS SO
98             BIT      #ERR,S+ND5      :ANY DRIVE ERROR ?
99             BNE      5S           :BR IS SO
100            ADD      #2,R5           :ELSE,ADJUST FOR GOOD RETURN
101            JMP      32S           :EXIT
102
103     46 004176 032767 040000 175414 4S: MOV      #13,WRMCS1    :RELEASE THE DRIVE
104             BIT      #MCP:ITRE,S+NC51 :ANY RH CONTROLLER ERROR DETECTED ?
105             BNE      5S           :BR IS SO
106             BIT      #ERR,S+ND5      :ANY DRIVE ERROR ?
107             BNE      5S           :BR IS SO
108             ADD      #2,R5           :ELSE,ADJUST FOR GOOD RETURN
109             JMP      32S           :EXIT
110
111     48 004204 001004
112             MOV      #13,WRMCS1    :RELEASE THE DRIVE
113             MSGNS,BEGIN,SEEKER    :ASCII MESSAGE CALL WITH COMMON HEADER
114             JMP      31S
115
116     49 004206 062705 000002
117             MOV      #13,WRMCS1    :RELEASE THE DRIVE
118             MSGNS,BEGIN,SEEKER    :ASCII MESSAGE CALL WITH COMMON HEADER
119             JMP      31S
120
121     50 004212 000167 000714
122             MOV      #13,WRMCS1    :RELEASE THE DRIVE
123             MSGNS,BEGIN,SEEKER    :ASCII MESSAGE CALL WITH COMMON HEADER
124             JMP      31S
125
126     51
127     52 :CHECK TO SEE IF USER DEFINED ANY BAD SPOTS
128     53

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54 004216 012700 000252' 5S: MOV #BADSPTR,RO ;TABLE ADDRESS
55 004222 012701 000020 MOV #16,,R1 ;TOTAL BAD SECTOR COUNT
56 004226 021067 174134 6S: CMP (R0),CYLIND ;ON THE SAME CYLINDER ?
57 004232 001014 BNE 7S ;BR IF NOT
58 004234 126067 000003 174127 CMPH 3(R0),DSKADR+1 ;ON THE SAME TRACK ?
59 004242 001010 BNE 7S ;BR IF NOT
60 004244 126067 000002 174116 CMPH 2(R0),DSKADR ;ON THE SAME SECTOR ?
61 004252 001004 BNE 7S ;BR IF NOT
62 004254 012767 177777 174114 MOV #1,BADSEC ;SET USR BAD SPOT FLAG
63 004262 000423 BR 9S ;EXIT
64 004264 062700 000004 7S: ADD #4,R0 ;ADJUST TABLE ADDRESS
65 004270 005301 DEC R1 ;DECREMENT COUNT
66 004272 001355 BNE 6S ;BR IF NOT END OF TABLE
67
68 ;CHECK FOR HARDWARE DETECTED BAD SPOTS
69
70 004274 032767 000400 175320 8S: HIT #HCRCL,S+NER1 ;HEADER CRC ERROR ?
71 004302 001015 BNE 10S ;BR IF YES
72 004304 022767 000200 175310 CMP #ACE,S+NER1 ;IS IT ONLY A HEADER COMPARE ERROR ?
73 004312 001411 BEW 10S ;BR IF YES
74 004314 032767 100040 175326 BIT #HSEISSE,S+NER2 ;ANY MEDIA DETECTED ERRORS ?
75 004322 001405 BEW 10S ;BR IF NO
76 004324 016767 175320 174044 MOV S+NER2,HAESEC ;YES--GET RMER2 FOR HSE OR SSE DETECT
77
78 004332 000167 000574 9S: JMP 32S ;TAKE LEAVE EXIT, BUT NOT REPORT THE ERROR
79
80 ;CHECK FOR OTHER ERRORS
81
82 004336 032767 020000 175242 10S: BIT #MCPE,S+NCS1 ;RM CONTROL BUS PARITY ERROR ?
83 004344 001405 BEW 11S ;BR IF NOT
84 004346 104403 000000' 007366' MSGNS,BEGIN,MCPEER ;ASCII MESSAGE CALL WITH COMMON HEADER
85 004354 000167 000536 JMP 31S ;EXIT
86 004360 032767 000400 175230 11S: HIT #MUPE,S+NCS2 ;MASS BUS DATA PARITY ERROR ?
87 004366 001405 BEW 12S ;BR IF NOT
88 004370 104403 000000' 007372' MSGNS,BEGIN,MUPEEP ;ASCII MESSAGE CALL WITH COMMON HEADER
89 004376 000167 000514 JMP 31S ;EXIT
90 004402 032767 037000 175206 12S: BIT #MAF1PGE1,NEMINDEDUPE,S+NCS2 ;RM CONTROL FAILS ?
91 004410 001411 BEW 13S ;BR IF NOT
92 004412 032767 040000 175200 BIT #ERR,S+NDS ;ANY DRIVE ERROR ?
93 004420 001005 BNE 13S ;BR IF SO
94 004422 104403 000000' 007402' MSGNS,BEGIN,RHFAIL ;ASCII MESSAGE CALL WITH COMMON HEADER
95 004430 000167 000462 JMP 31S ;EXIT
96 004434 032767 100000 175154 13S: BIT #DTL,S+NCS2 ;DATA LATE ERROR ?
97 004442 001423 BEW 15S ;BR IF NOT
98 004444 005267 173730 INC DLTCN1 ;INCREMENT DATA LATE ERROR COUNT
99 004450 032767 000004 173340 BIT #BIT2,SR1 ;ALLOW TO TYPE OUT DATA LATE ERROR ?
100 004456 001402 BEW 14S ;BR IF SO
101 004460 000167 000446 JMP 32S ;EXIT
102
103 004464 14S: 004464 104403 000000' 007476' MSGNS,BEGIN,DLTFEH ;ASCII MESSAGE CALL WITH COMMON HEADER
104 004472 012767 000602 173406 MOV #2,ERRTYP ;SAVE THE ERROR TYPE
105 ;*****
;SUFERS,BEGIN,TALE ;DATA LATE ERROR
;*****
106 004506 000167 000420 JMP 32S ;EXIT
107 004512 032767 040000 175100 15S: BIT #ERR,S+NDS ;ANY DRIVE ERROR ?

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108 004520 001027 BNE 18S ;BR IF ANY
109 004522 032767 040000 175066 BIT #ACE,S+NCS2 ;WRITE CHECK ERROR ?
110 004530 001404 BEW 16S ;BR IF NOT
111 004532 104403 000000' 007462' MSGNS,BEGIN,WRICK ;ASCII MESSAGE CALL WITH COMMON HEADER
112 004540 000566 BR 31S ;EXIT
113 004542 032767 040000 175036 16S: BIT #TRE,S+VCS1 ;THEN RM TRANSFER ERROR ?
114 004550 001404 BEW 17S ;BR IF NOT
115 004552 104403 000000' 007362' MSGNS,BEGIN,TRERR ;ASCII MESSAGE CALL WITH COMMON HEADER
116 004560 000556 BR 31S ;EXIT
117 004562 012767 000000 173316 17S: MOV #0,ERRTYP ;UNKNOWN ERROR
118 ;*****
;HNDERS,BEGIN,TALE ;UNKNOWN ERROR
;*****
004570 104405 000000' 001424'
119 004576 000555 BR 32S ;EXIT
120 004600 032767 040000 175014 18S: BIT #UNS,S+NER1 ;RM DEVICE UNSAFE ?
121 004606 001404 BEW 19S ;BR IF NOT
122 004610 104403 000000' 007406' MSGNS,BEGIN,UNSAFE ;ASCII MESSAGE CALL WITH COMMON HEADER
123 004616 000537 BR 31S ;EXIT
124 004620 032767 000600 174774 19S: BIT #HCRCLHCF,S+NER1 ;HEADER INFORMATION ERROR ?
125 004626 001404 BEW 20S ;BR IF NOT
126 004630 104403 000000' 007412' MSGNS,BEGIN,HEADER ;ASCII MESSAGE CALL WITH COMMON HEADER
127 004636 000527 BR 31S ;EXIT
128 004640 032767 000020 174754 20S: BIT #FEW,S+NER1 ;DATA FORMAT ERROR ?
129 004646 001404 BEW 21S ;BR IF NOT
130 004650 104403 000000' 007416' MSGNS,BEGIN,FORMAT ;ASCII MESSAGE CALL WITH COMMON HEADER
131 004656 000517 BR 31S ;EXIT
132 004660 032767 003000 174734 21S: BIT #IAE1AOE,S+NER1 ;ADDRESSING ERROR ?
133 004666 001410 BEW 22S ;BR IF NOT
134 004670 032767 002000 174722 BIT #LRT,S+NDS ;LAST BLOCK TRANSFER SET ?
135 004676 001115 BNE 32S ;EXIT, IF SO
136 004700 104403 000000' 007422' MSGNS,BEGIN,ADRRES ;ASCII MESSAGE CALL WITH COMMON HEADER
137 004706 000503 BR 31S ;EXIT
138 004710 032767 020000 174704 22S: BIT #UPI,S+NER1 ;POSITIONER FAILURE ?
139 004716 001404 BEW 23S ;BR IF NOT
140 004720 104403 000000' 007472' MSGNS,BEGIN,POSIT ;ASCII MESSAGE CALL WITH COMMON HEADER
141 004726 000473 BR 31S ;EXIT
142 004730 032767 040000 174712 23S: BIT #SKI,S+NER2 ;SEEK ERROR ?
143 004736 001404 BEW 24S ;BR IF NOT
144 004740 104403 000000' 007426' MSGNS,BEGIN,SEKERR ;ASCII MESSAGE CALL WITH COMMON HEADER
145 004746 000463 BR 31S ;EXIT
146 004750 032767 004000 174672 24S: BIT #LSC,S+NER2 ;LOSS OF SYSTEM CLOCK ?
147 004756 001403 BEW 25S ;BR IF NOT
148 004760 104403 000000' 007432' MSGNS,BEGIN,LUSYSY ;ASCII MESSAGE CALL WITH COMMON HEADER
149 004766 032767 004000 174626 25S: BIT #WLE,S+NER1 ;WRITE LOCK ERROR ?
150 004774 001404 BEW 26S ;BR IF NOT
151 004776 104403 000000' 007436' MSGNS,BEGIN,WTLOCK ;ASCII MESSAGE CALL WITH COMMON HEADER
152 005004 000444 BR 31S ;EXIT
153 005006 032767 010000 174606 26S: BIT #DTE,S+NER1 ;DRIVE TIMING ERROR ?
154 005014 001404 BEW 27S ;BR IF NOT
155 005016 104403 000000' 007466' MSGNS,BEGIN,TIMERR ;ASCII MESSAGE CALL WITH COMMON HEADER
156 005024 000434 BR 31S ;EXIT
157 005026 032767 100100 174566 27S: BIT #DUCK1ECH,S+NER1 ;DATA CHECK ERROR ?
158 005034 001404 BEW 28S ;BR IF NOT
159 005036 104403 000000' 007442' MSGNS,BEGIN,DATERR ;ASCII MESSAGE CALL WITH COMMON HEADER
160 005044 000424 BR 31S ;EXIT
161 005046 032767 002000 174574 28S: BIT #LBC,S+NER2 ;LOST BIT CLOCK ?
162 005054 001404 BEW 29S ;BR IF NOT

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163 005056 104403 000000' 007446' MSGNS,BEGIN,LUSBIT ;ASCII MESSAGE CALL WITH COMMON HEADER
164 005064 000414 BR JIS ;EXIT
165 005066 032767 000040 174526 29S: BIT *WCF,S+NER1 ;WRITE CLOCK ERROR ?
166 005074 001404 BEQ JUS ;BR IF NOT
167 005076 104403 000000' 007452' MSGNS,BEGIN,WICLCK ;ASCII MESSAGE CALL WITH COMMON HEADER
168 005104 000404 BR JIS ;EXIT
169 005106 30S:
005106 104403 000000' 007456' MSGNS,BEGIN,UKVEKK ;ASCII MESSAGE CALL WITH COMMON HEADER
170 005114 000400 BR JIS ;EXIT
171 005116 012767 000000 172762 31S: MOV R0,ERRTYP ;DUMMY ERROR NUMBER
172 005124 104406 000000' 001424' ;*****
SOFERS,BEGIN,TABLE ;DUMP RM/RM REGISTERS
;*****
173
174 005132 000205 32S: RTS R5 ;EXIT
175

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1 ;THIS ROUTINE IS USED TO SEE IF THE DRIVE IS READY FOR TESTING BY
2 ;CHECKING 'DVA' AND THE DRIVE TYPE. IF 'DVA' IS NOT SET OR THE
3 ;DRIVE TYPE IS INCORRECT, THEN AN ERROR IS REPORTED AND THE DRIVE
4 ;IS DROPPED.
5 ;CALL
6 ; JSR R5,READY ;CALL READY ROUTINE
7 ; BR ? ;ERROR RETURN
8 ; RETURN ;NORMAL RETURN
9
10 005134 004767 000110 READY: JSR PC,WAIT ;WAIT FOR "DVA" BIT TO SET
11 005140 000422 BK IS ;RETURN HERE ON ERROR
12 005142 017700 174362 MOV *RMOT,R0 ;READ THE DRIVE TYPE
13 005146 010067 173242 MOV R0,DRVTYP ;GET DRIVE TYPE AND
14 005152 042767 177740 173234 BIC *C37,DRVTYP ;SAVE TYPE BITS
15 005160 022700 024026 CMP #24026,R0 ;DUAL PORT RM80 ?
16 005164 001426 BEQ ZS ;BR IF SO
17 005166 022700 020026 CMP #20026,R0 ;SINGLE PORT RM80 ?
18 005172 001423 BEQ ZS ;BR IF SO
19 005174 004767 000456 JSR PC,ERSUM1 ;SETUP FOR ERROR REPORT
20 005200 104403 000000' 007506' MSGNS,BEGIN,DEVNOT ;ASCII MESSAGE CALL WITH COMMON HEADER
21
22 005206 004767 000316 1S: JSR PC,GET ;READ AND STORE ALL REGISTERS
23 005212 012767 000006 172666 MOV R6,ERRTYP ;SAVE THE ERROR TYPE
24 005220 104405 000000' 001424' ;*****
HRDEKS,BEGIN,TABLE ;DEVICE NOT AVAILABLE OR NOT AN RM80
;*****
25 005226 004767 176444 JSR PC,DROP ;DROP THE DRIVE
26 005232 104403 000000' 007522' MSGNS,BEGIN,DRM ;ASCII MESSAGE CALL WITH COMMON HEADER
27 005240 000205 RTS R5 ;EXIT FOR ERROR RETURN
28
29 005242 062705 000002 2S: ADD #2,R5 ;DRIVE AVAILABLE, UN-LINE, AND NO ERRORS
30 005246 000205 RTS R5 ;NORMAL RETURN
31
32 ;THIS ROUTINE WAITS FOR "DVA" TO SET IN THE RMCS1 REGISTER. IF THE "DVA"
33 ;BIT IS SET, THE DRIVE IS CLEARED AND THE COMMAND SEQUENCE BEGINS. IF THE
34 ;"DVA" BIT IS NOT SET, A DRIVE CLEAR COMMAND IS ISSUED TO THE DRIVE TO SET
35 ;PORT REQUEST. THE PROGRAM THEN CHECKS "DVA" IN RMCS1. IF THE "DVA" BIT
36 ;DOES NOT SET WITHIN A CERTAIN PERIOD OF TIME, AN ERROR IS REPORTED AND
37 ;RETURN IS MADE BACK TO THE MAIN PROGRAM, WHERE THE DRIVE WILL BE DROPPED
38 ;FROM THE TEST.
39 ;
40 ;CALL
41 ; JSR PC,WAIT ;CALL WAIT ROUTINE
42 ; BR ??? ;ERROR RETURN
43 ; RETURN ;NORMAL RETURN
44
45 005250 032777 004000 174224 WAIT: BIT #DVA,RRMCS1 ;IS DRIVE AVAILABLE ?
46 005256 001035 BNE ZS ;BR IF YES
47 005260 012777 000011 174214 MOV #11,RRMCS1 ;ISSUE DRIVE CLEAR TO REQUEST PORT
48 005266 012767 017777 173114 MOV #17777,CLK ;CLOCK COUNT
49 005274 032777 004000 174200 1S: BIT #DVA,RRMCS1 ;IS DRIVE AVAILABLE ?
50 005302 001023 BNE ZS ;BR IF YES
51 005304 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
005310 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
52 005314 005367 173070 DEC CLK ;DECREMENT CLOCK COUNT
53 005320 001365 BNE IS
54 005322 004767 000330 JSR PC,ERSUM1 ;SETUP FOR ERROR REPORT

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55 005326 012767 000000 172552      MOV    #0,ERRTYP      ;UNKNOWN ERROR TYPE
56 005344 104403 000000' 007516'    MSGNS,BEGIN,NODVA    ;ASCII MESSAGE CALL WITH COMMON HEADER
57 005342 012767 000001 173012      MOV    #1,DRPDV      ;SET DROP DRIVE FLAG
58 005350 000207                      RTS     PC             ;ERROR RETURN, TIMEOUT UN "DVA" BIT
59
60 005352 012777 040011 174122 2S:    MOV    #40011,0RMCS1  ;ISSUE A DRIVE CLEAR COMMAND
61 005360 032767 040000 174262      BIT     #SKI,S+NEH2   ;WAS LAST ERROR AN "SKI" ?
62 005366 001406                      BEQ     3S              ;BR IF NO
63 005370 012777 000007 174104      MOV    #7,0HMCS1     ;ISSUE A RECALIBRATE COMMAND AND
64 005376 004767 000010                      JSR     PC,WTCDY      ;WAIT FOR CONTROLLER READY
65 005402 000207                      RTS     PC             ;ERROR RETURN, TIMEOUT ON "CRDY" BIT
66
67 005404 062716 000002 3S:          ADD     #2,(SP)          ;ADJUST FOR GOOD RETURN
68 005410 000207                      RTS     PC             ;RETURN
69
70
71
72
73
74
75
76
77
78 005412 012767 017777 172770  WTCRDY: MOV    #17777,CLK      ;CLOCK COUNT
79 005420 017700 174056 1S:          MOV    0RMCS1,R0      ;GET CONTROL STATUS
80 005424 042700 177576          BIC     #*C<RDY!GD>,R0
81 005430 022700 000200          CMP     #RDY,R0      ;RDY = 1 AND GD = 0 ?
82 005434 001432                      BEQ     2S              ;BR IF YES
83 005436 104407 000000'          BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR...
84 005442 104407 000000'          BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
85 005446 005367 172736          DEC     CLK            ;DECREMENT CLOCK COUNT
86 005452 001362                      HNE     1S              ;BR IF NOT
87 005454 004767 000176          JSR     PC,ERSUB1     ;SETUP FOR ERROR REPORT
88 005460 104403 000000' 007512'    MSGNS,BEGIN,NUTDY    ;ASCII MESSAGE CALL WITH COMMON HEADER
89 005466 004767 000036          JSR     PC,GET         ;READ AND STORE ALL REGISTERS
90 005472 012767 000003 172406      MOV    #3,ERRTYP      ;SAVE THE ERROR TYPE
91
92 005500 104405 000000' 001424'    HDRS,BEGIN,TABLE    ;CONTROLLER READY DID NOT SET
93
94 005506 004767 176164          JSR     PC,DRUP        ;DROP THE DRIVE
95 005512 104403 000000' 007522'    MSGNS,BEGIN,DRM ;ASCII MESSAGE CALL WITH COMMON HEADER
96 005520 000207                      RTS     PC             ;ERROR RETURN
97
98
99
100
101
102
103
104 005530 005000                      ;THIS ROUTINE IS USED TO READ AND STORE ALL REGISTERS FROM THE RH/RM
105 005532 016701 173744          ;WHEN AN ERROR HAS BEEN DETECTED.
106 005536 022701 001524'          ;CALL
107 005542 001006                      ;
108 005544 032711 000200          ;JSR     PC,GET         ;CALL GET ROUTINE
109
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109 005550 001003                      HNE     2S              ;BR IF YES
110 005552 005060 001606'          CLR     S(R0)          ;ELSE, CLEAR RMDR REGISTER
111 005556 000402                      BR      3S              ;
112 005560 012160 001606'          2S:    MOV    (R1)+,S(R0)      ;READ ONE REGISTER
113 005564 005720 3S:            TST     (R0)+          ;ADJUST TABLE INDEX
114 005566 022700 000050          CMP     #50,R0      ;DONE READING ALL REGISTERS ?
115 005572 001361                      BNE     1S              ;BR IF NO
116 005574 012760 177777 001424'    MOV    #-1,TABLE(R0) ;TERMINATE DECK REGISTER TABLE FOR AN RH11
117 005602 005767 172556          TST     FLAG70       ;CHECK FOR ADDITIONAL RH70 REGISTERS
118 005606 000010                      BPL     4S              ;BR IF NONE
119 005610 012760 001656' 001424'    MOV    #SNBAE,TABLE(R0) ;EXTEND THE DECK REGISTER TABLE
120 005616 012160 001606'          MOV    (R1)+,S(R0)      ;GET RMDR REGISTER
121 005622 005720          TST     (R0)+          ;ADJUST INDEX TABLE
122 005624 011160 001606'          MOV    (R1),S(R0)      ;GET RMCS3 REGISTER
123 005630 000207          4S:      RTS     PC             ;RETURN
124
125
126
127
128
129 005632 014167 172250          ;THESE TWO ROUTINES ARE USED TO SETUP FOR ERROR REPORTING
130 005636 010167 172240          ;CALL
131 005642 014267 172242          ;JSR     PC,ERSUB?     ;CALL ROUTINE
132 005646 010267 172232          ;
133 005652 005721          ERSUB2: MOV    -(R1),ASH      ;LOAD THE DATA
134 005654 005722          MOV    R1,SBADR     ;LOAD ADDRESS OF DATA WRITTEN
135
136 005656 016767 173620 172214      MOV    -(R2),AAS      ;LOAD THE DATA
137 005664 017767 173612 172210      MOV    R2,ASADR      ;LOAD ADDRESS OF DATA READ
138 005672 000207          TST     (R1)+          ;RESET REG. 1
139
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166 006010 000207          RIS      PC          ;RETURN
167
168          ,SRTTL  ASCIZ MESSAGE
169
170 006012 045 040 122 MES1: .ASCIZ '% RH CONTROLLER TRANSFER ERROR'
171 006051 045 040 115 MES2: .ASCIZ '% MASSBUS PARITY ERROR MCPE=1'
172 006107 045 040 115 MES3: .ASCIZ '% MASSBUS DATA PARITY ERROR MDPE=1'
173 006152 040 104 122 MES4: .ASCIZ ' DRIVE '
174 006162 040 104 122 MES5: .ASCIZ ' DROPPED%'
175 006174 045 040 105 MES6: .ASCIZ '% EXCEEDED RETRY LIMIT%'
176 006224 045 040 104 MES10: .ASCIZ '% DEVICE UNSAFE'
177 006244 045 040 110 MES11: .ASCIZ '% HEADER INFORMATION ERROR'
178 006277 045 040 104 MES12: .ASCIZ '% DATA FORMAT BIT ERROR'
179 006327 045 040 104 MES14: .ASCIZ '% DRIVE ERROR'
180 006345 045 040 122 MES15: .ASCIZ '% RH CONTROLLER ERROR'
181 006373 045 040 101 MES16: .ASCIZ '% ADDRESSING ERROR'
182 006416 045 040 123 MES17: .ASCIZ '% SEEK ERROR'
183 006433 045 040 114 MES20: .ASCIZ '% LOST SYSTEM CLOCK'
184 006457 045 040 127 MES21: .ASCIZ '% WRITE LOCK ERROR'
185 006502 045 040 104 MES22: .ASCIZ '% DATA CHECK ERROR'
186 006525 045 040 114 MES23: .ASCIZ '% LOST BIT CLOCK'
187 006546 045 040 127 MES24: .ASCIZ '% WRITE CLOCK ERROR'
188 006572 045 040 127 MES25: .ASCIZ '% WRITE CHECK ERROR'
189 006616 045 040 104 MES26: .ASCIZ '% DRIVE TIMING ERROR'
190 006643 045 000 MES27: .ASCIZ '%'
191 006645 045 040 120 MES30: .ASCIZ '% POSITIONING ERROR'
192 006671 045 040 104 MES31: .ASCIZ '% DATA LATE ERROR'
193 006713 045 040 104 MES32: .ASCIZ '% DRIVE NOT READY'
194 006735 045 040 104 MES33: .ASCIZ '% DEVICE NOT AN RM80'
195 006762 045 040 103 MES34: .ASCIZ '% CONTROLLER NOT READY'
196 007011 045 040 104 MES35: .ASCIZ '% DRIVE NOT AVAILABLE'
197 007037 040 041 040 MES36: .ASCIZ '% ! OPERATING ON FE CYLINDERS ONLY !%'
198 007104 007 007 040 MES37: .ASCIZ '<07><07>' ! CUSTOMER DATA WILL BE OVERWRITTEN !%'
199 007155 040 055 055 .ASCIZ '-----%'<07><07>
200 007227 040 111 106 MES40: .ASCIZ ' IF YOU WISH TO EXERCISE THE WHOLE DISK, SET BIT0%'
201 007311 040 111 116 .ASCIZ ' IN SWITCH REGISTER 1(SR1) EQUAL TO 1.%'
202
203          ,EVEN
204 007362 006012'          TRERR: MES1 177777
205 007364 177777          MCPPERR: MES2 177777
206 007366 006051'          MDPEERR: MES3 177777
207 007370 177777          EXCED: MES6 177777
208 007372 006107'          RHFAIL: MES15 177777
209 007374 177777          UNSAFE: MES10 177777
210 007376 006174'          HEADER: MES11 177777
211 007400 177777          FORMAT: MES12 177777
212 007402 006345'          ADDRES: MES16 177777
213 007404 177777          SEEKERR: MES17 177777
214 007406 006224'
215 007410 177777
216 007412 006244'
217 007414 177777
218 007416 006277'
219 007420 177777
220 007422 006373'
221 007424 177777
222 007426 006416'

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223 007430 177777          LOSYSC: MES20 177777
224 007432 006433'          WTLOCK: MES21 177777
225 007434 177777          DATERR: MES22 177777
226 007436 006451'          LOSBIT: MES23 177777
227 007440 177777          WTCLCK: MES24 177777
228 007442 006502'          DRVERR: MES14 177777
229 007444 177777          *RTCK: MES25 177777
230 007446 006525'          TIMERR: MES26 177777
231 007450 177777          POSIT: MES30 177777
232 007452 006546'          DLTERR: MES31 177777
233 007454 177777          NODRDY: MES32 177777
234 007456 006327'          DEVDUT: MES33 177777
235 007460 177777          NUTRDY: MES34 177777
236 007462 006572'          NODVA: MES35 177777
237 007464 177777          DRM: MES4 177777
238 007466 006616'          NUMB 177777
239 007470 177777          MES5 177777
240 007472 006645'          FEONLY: MES36 177777
241 007474 177777          OVRWRI: MES37 177777
242 007476 006671'          WARNING: MES40 177777
243 007500 177777
244 007502 006713'
245 007504 177777
246 007506 006735'
247 007510 177777
248 007512 006762'
249 007514 177777
250 007516 007011'
251 007520 177777
252 007522 006152'
253 007524 007553'
254 007526 006162'
255 007530 177777
256 007532 007037'
257 007534 177777
258 007536 007104'
259 007540 177777
260 007542 007227'
261 007544 177777
262
263 007546          ADRI: .BLKB 5          ;STARTING OF THE ASCII NUMBER STRING
264 007553          NUMB: .BYTE 0          ;LEAST SIGNIFICANT ASCII DIGIT
265 007554          .BYTE 0          ;TERMINATOR OF THE ASCII STRING
266
267          ,EVEN
268          .END
000001

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ACSR 000102R	DRGP 003676R	IR = 000100	NBAE = 000050	PUSH2 = 024646
ADDR 000006R	DRPDRV 000362R	LHC = 002000	NCS1 = 000000	PWRFLG= 000002
ADDRS 007422R	DRVERR 007456R	LBT = 002000	NCS2 = 000010	RANDS = 104417
ADDR22= 001000	DRVTYP 000414R	LC = 000006	NCS3 = 000052	RANNUM 000054R
ADR1 007546R	DRY = 000200	LDADR 003600R	NDA = 000006	RBUF 000424R
ADE = 001000	DSKADR 000370R	LFE = 000022	NDB = 000022	RBUEFA 000130R
ASB 000106R	DTE = 010000	LOOP1 002202R	NDC = 000034	RBUFFA 000126R
ASTAT 000104R	OTL = 100000	LOOP2 002222R	NDS = 000012	RBUSFZ 000132R
ATA = 100000	DVA = 004000	LSH17 007446R	NDT = 000026	RBUFFVA 000124R
AWAS 000110R	DVC = 000200	LOSYS 007432R	NEC1 = 000044	KDY = 000200
BADSEC 000376R	DVICE 000372R	LS = 000004	NEC2 = 000046	READ 002574R
BADSPT 000252R	DVID1 000014R	LSC = 004000	NEO = 010000	READY 005134R
BEGIN 000000R	EA22 000360R	LT = 000002	NEM = 004000	RESTR1 002164R
HITTAH 003666R	ECH = 000100	MAP22S= 104416	NLR1 = 000014	RES1 000056R
BIT0 = 000001	ENDITS= 104413	MCPE = 020000	NEW2 = 000042	RES2 000060R
BIT1 = 000002	ENDS = 104410	MCPEERR 007366R	NHR = 000036	RHFAIL 007402R
BIT10 = 002000	ERR = 040000	MDPE = 000400	NLA = 000020	RMA5 001520R
BIT11 = 004000	ERRORS 003740R	MDPEERR 007372R	NMR1 = 000024	RMA6 001506R
BIT12 = 010000	ERRTYP 000106R	MES1 006012R	NMR2 = 000040	RMAE 001552R
BIT13 = 020000	ERSUB1 005656R	MES10 006224R	NODRUY 007502R	RMCS1 001502R
BIT14 = 040000	ERSUB2 005632R	MES11 006244R	NODVA 007516R	RMCS2 001512R
BIT15 = 100000	EXCED 007376R	MES12 006277R	NDF = 000032	RMCS3 001554R
BIT2 = 000004	EXITS = 104400	MES14 006327R	NDRDY 007512R	RMDA 001510R
BIT3 = 000010	FEONLY 007532R	MES15 006345R	NSN = 000030	RMD 001524R
BIT4 = 000020	FER = 000020	MES16 006373R	NRUPT 003034R	RMDC 001536R
BIT5 = 000040	FERADR 000406R	MES17 006416R	NULL = 000000	RMDS 001514R
BIT6 = 000100	FFE = 000020	MES2 006051R	NUMH 007553R	RMDT 001530R
BIT7 = 000200	FLAG70 000364R	MES20 006433R	NWC = 000002	RMEC1 001546R
BIT8 = 000400	FMT = 010000	MES21 006457R	OM = 000001	RMEC2 001550R
BIT9 = 001000	FORMAT 007416R	MES22 006502R	OPEN = 000000	RMER1 001516R
BREKAS= 104407	FREE 000150R	MES23 006525R	OPI = 020000	RMER2 001544R
BR1 000012R	FT = 000000	MES24 006546R	OK = 000200	RMHR 001540R
BR2 000013R	FUNC 000402R	MES25 006572R	OTOAS = 104420	RMLA 001522R
BSE = 100000	GENADR 003174R	MES26 006616R	UVRWRT 007536R	RMHR1 001526R
BST = 000010	GET = 005530R	MES27 006643R	PAK = 000010	RMHR2 001542R
BTDS = 104421	GETDRV 003630R	MES3 006107R	PASCNT 000034R	RMOF 001534R
CDATAS= 104412	GETMAP 003522R	MES30 006645R	PA18 000352R	RMOFI 000416R
CDERCT 000144R	GETPAS= 104415	MES31 006671R	PA22 000356R	RMR = 000004
CDWOCT 000146R	GO = 000001	MES32 006713R	PGE = 002000	RMSN 001532R
CLK 000410R	GO1 002674R	MES33 006735R	PGM = 001000	RMMC 001504R
CLR = 000040	GO2 003000R	MES34 006762R	PIP = 020000	RSTRT 000112R
CONFIG 000056R	GWRUFS= 104414	MES35 007011R	PIRGS = 000004	RSTRT1 002174R
CSRA 000100R	HCE = 000200	MES36 007037R	POPS = 005726	R6 = 000000R
CYCLE 002276R	HCMC = 000400	MES37 007104R	PUPSP2= 022626	H7 = 0000007
CYL = 000016	HEADER 007412R	MES4 006152R	POST 007472R	S = 001606R
CYLIND 000366R	HRDCNT 000044R	MES40 007227R	PTY = 000000	SBADR 000102R
CYLMAP 000422R	HRDRS= 104405	MES5 006162R	PTY0 = 000000	SC = 100000
DATCKS= 104411	HRDPAS 000050R	MES6 006174R	PTY1 = 000040	SEC = 000012
DATEHR 007442R	IAE = 002000	MUDNAM 000000R	PTY2 = 000100	SEEK 002654R
DATERG= 104404	ICONT 000036R	MODSP 000252R	PTY3 = 000140	SEEKER 007426R
DCK = 100000	ICOUNT 000040R	MUL = 010000	PTY4 = 000200	SETBL 001766R
DEVNOT 007506R	IDNUM 000122R	MSGNS = 104403	PTY5 = 000240	SETUP 005674R
DLTCNT 000400R	ILF = 000001	MSGSS = 104402	PTY6 = 000300	SKI = 040000
DLTERM 007476R	ILR = 000002	MSGS = 104401	PTY7 = 000340	SOFcnt 000042R
DPE = 000010	IMODX= 000000	KMF = 001000	PS = 177776	SOFERS= 104406
DPR = 000400	INIT 000030R	NAS = 000016	PSW = 177776	SOPPAS 000046R
DRM 007522R	INTR 000120R	NHA = 000004	PUSH = 005746	SPOINT 000032R

SPSIZ = 000040	SVR2 000066R	IRK = 000014	WARNIN 007542R	WRITCK 002514R
SR1 000016R	SVR3 000070R	IRKMAP 000420R	WASADR 000104R	WRITE 002434R
SR2 000020R	SVR4 000072R	TRPDFD= 000022	WBUFEA 000136R	WRL = 004000
SR3 000022R	SVR5 000074R	TRY 000412R	WBUFPA 000134R	WRICK 007462R
SR4 000024R	SVR6 000076R	UNITNO 000374R	WBUFRQ 000140R	WICLCK 007452R
SSE = 000040	SYSCT 000052R	UNS = 040000	WBUFSZ 000142R	WICRDY 005412R
SSEI = 001000	TABLE 001424R	UNSAFE 007406R	WCE = 040000	WLOCK 007436R
START 001662R	TAHR0 001562R	UPE = 020000	WCF = 000040	XFERAD 001556R
STAT 000026R	TIMERH 007466R	VECTOR 000010R	WDFR 000116R	XFLAG 000005R
SVR0 000062R	TME = 040000	VV = 000100	WDTU 000114R	XMEM 000354R
SVR1 000064R	TRERR 007362R	WAIT 005250R	WLE = 004000	ZERO 000404R

. ABS. 000000 000
007556 001
ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 12800 WORDS (50 PAGES)
DYNAMIC MEMORY AVAILABLE FOR: 70 PAGES
XRNAA0,XRNAA0/C=[20,0]DDXCOM,[20,16]CXRNAA.DOC,CXRNAA

RNAA DEC/X11 SYSTEM EXERCISER M MACRO V04.00 12-JAN-82 11:52:29 PAGE S-4
CROSS REFERENCE TABLE (CREF V04.00)

SEQ 0038

[illegible]

RNAA DEC/X11 SYSTEM EXERCISER M MACRO V04.00 12-JAN-82 11:52:29 PAGE 5-6
CROSS REFERENCE TABLE (CREF V04.00)

SEQ 0040

RSTRT1	5-56	5-59	5-62*														
S	4-48	4-49	4-50	4-51	4-52	4-53	4-54	4-55	4-56	4-57	4-58	4-59	4-60	4-61			
	4-62	4-63	4-64	4-65	4-66	4-67	4-68	4-69	4-156#	11-12	11-22	11-32	11-36	11-45			
	11-47	11-70	11-72	11-74	11-76	11-82	11-86	11-90	11-92	11-96	11-107	11-109	11-113	11-120			
	11-124	11-128	11-132	11-134	11-138	11-142	11-146	11-149	11-153	11-157	11-161	11-165	12-61	12-110*			
	12-112*	12-119	12-120*	12-122*													
SBADR	4-6*	12-130*															
SC	4-160*																
SEC	4-130*	5-36*	10-13*	10-14*	10-37	10-39*	10-44*	10-45	10-47*	10-105							
SEEK	6-12	6-18	6-24	7-76*	7-77												
SEEKER	11-41	11-144	12-222*														
SETBL	5-24*																
SETUP	5-43	12-145*															
SKI	4-221*	11-142	12-61														
SOFCNT	4-6*																
SOFERS	4-6*	11-105	11-172														
SOPPAS	4-6*																
SPOINT	4-6*	5-7															
SPSIZ	1-23*	4-6															
SR1	4-6*	5-11*	5-13	5-27	5-39	10-10	10-26	10-57	10-82	11-99	12-156						
SR2	4-6*																
SR3	4-6*																
SR4	4-6*																
SSE	4-217*	9-16	11-74														
SSEI	4-227*	9-18															
START	4-6	5-3*	5-60														
STAT	4-6*																
SVR0	4-6*																
SVR1	4-6*																
SVR2	4-6*																
SVR3	4-6*																
SVR4	4-6*																
SVR5	4-6*																
SVR6	4-6*																
SYS CNT	4-6*																
TABBO	4-145*	5-24	10-9	10-104													
TABLE	4-48*	11-19	11-29	11-105	11-118	11-172	12-24	12-90	12-116*	12-119*							
TIMERR	11-155	12-238*															
TRE	4-161*	11-45	11-113														
TRERR	11-115	12-204*															
TRK	4-131*	5-37*	10-35*														

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IDENTIFICATION

PRODUCT CODE: AC-T087A-MC
PRODUCT NAME: CXBTCAU Q22 BUS EXER DEC/X11
PRODUCT DATE: DECEMBER 1981
MAINTAINER: DIAGNOSTIC ENGINEERING

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HISTORY

REVISION	A	FIRST RELEASE OF DIAGNOSTIC
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;*****
;
; Q22BE DEC/X11 MODULE
;
; ABSTRACT
; REQUIREMENTS (HARDWARE AND SOFTWARE)
; PASS DEFINITIONS
; EXECUTION TIME
; CONFIGURATION REQUIREMENTS
; DEVICE/OPTION SETUPS
; TEST SEQUENCE
; OPERATION OPTIONS
; NON-STANDARD PRINTOUTS
;
; 1. ABSTRACT
;
; BICA IS AN IUMODX THAT EXERCISES AND TESTS THE Q22BE IN
; A SYSTEMS ENVIRONMENT. THE PURPOSE OF THE Q22BE IS TO
; CAUSE SYSTEM BUS ACTIVITY AT A HIGH LEVEL. THIS SOFTWARE
; MODULE WILL HAVE THE Q22BE PERFORM DMA TRANSFERS USING
; DATA-OUTS AND DATA-INS. WHEN PERFORMING DATA-OUT
; TRANSFERS THE DATA WILL BE CHECKED FOR VALIDITY. ALL
; AVAILABLE INTERRUPT LEVELS WILL BE EXERCISED. ANY ERRORS
; DETECTED WILL BE REPORTED ON THE SYSTEM CONSOLE.
;
;
; 2. REQUIREMENTS
;
; THIS MODULE WILL REQUIRE APPROXIMATELY 2K WORDS OF MEMORY.
;
; THE SYSTEM MUST CONTAIN AT LEAST 16K OF MEMORY.
;
; A TYPICAL SYSTEM COULD CONTAIN MORE THAN ONE Q22BE,
; ALTHOUGH ONLY ONE IS NECESSARY.
;
; 3. PASS DEFINITION
;
; ONE ITERATION CONSISTS OF SETTING UP AND PERFORMING
; DMA OPERATIONS. ONE PASS WILL CONSIST OF MULTIPLE ITERATIONS.
;
; 4. EXECUTION TIME
;
; ONE PASS WILL RUN IN APPROXIMATELY ONE MINUTE.
;
; 5. CONFIGURATION REQUIREMENTS
;
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; REFERENCE THE Q22 BUS EXERCISER SPECIFICATION FOR THE
; DEVICE PARAMETERS. THE DEFAULT PARAMETERS WILL BE AS FOLLOWS:
;
; ADDR 170000 VECTOR 510 DEVENT 1
;
; 6. DEVICE/OPTION SETUPS
;
; APPROPRIATE MODULE LEVEL DIAGNOSTICS MUST BE RUN TO
; INITIALLY ENSURE THAT THE SYSTEM COMPONENTS ARE FUNCTIONAL.
;
; 7. TEST SEQUENCE
;
; 8. OPERATION OPTIONS
;
; THIS MODULE IS LOADED AND RUN AS SPECIFIED IN THE DEC/X11
; REFERENCE GUIDE.
;
; SKI MUST BE SET UP BY THE OPERATOR PRIOR TO RUNNING
; THIS MODULE.
;
; BIT0 CLEAR MODULE WILL NOT ATTEMPT ANY BLOCK MODE
; TRANSFERS
; BIT0 SET MODULE WILL ATTEMPT BOTH NON-BLOCK AND
; BLOCK MODE TRANSFERS. (ALL BLOCK MODE
; MEMORY)
; BIT1 CLEAR MODULE WILL ATTEMPT ONLY 1K WORD XFRS
; BIT1 SET MODULE WILL ATTEMPT ONE DATA-IN 16K WORD
; TRANSFER IN ADDITION TO OTHER
; TRANSFERS OF 1K WORDS
;
; 9. ERROR REPORTING
;
; ALL ERRORS WILL BE REPORTED AS THEY OCCUR. ERROR REPORTS
; WILL CONFORM TO THE STANDARD DEC/X11 FORMAT.
;
; Q22BE REGISTERS WILL BE REPORTED IN THE FOLLOWING
; SEQUENCE.
;
; CSR1 CSR2 BA WC DATA LATCNT MVLCTI
;
;*****
;
; THIS FILE IS THE BEGINNING OF A NEW SOFTWARE
; MODULE FOR THE Q22 BUS EXERCISER. IT WILL BE
; THE DEC/X11 Q22 BUS EXERCISER PROGRAM. ALL
; NECESSARY SET UP FUNCTIONS WILL BE INCLUDED
; IN THIS SECTION OF THE MODULE.
;
```

```

156 000000' IDMODX <HTCA >,170000,510,7,0,0,10,WWW,RBUF,256,16000
157 000000' MODULE 150000,HTCA,170000,510,7,0,0,10,WWW,RBUF,256,16000
158 .TITLE HTCA DEC/X11 SYSTEM EXERCISER MODULE
159 ; DDXCUM VERSION 6 23-MAY-78
160 .LIST HIN
161 ;*****
162 000000' BEGIN:
163 000000' 052102 040503 040 MODNAM: .ASCII /HTCA / ;MODULE NAME.
164 000005' 000 XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF RBUF USAGE
165 000006' 170000 ADDR: 170000+0 ;1ST DEVICE ADDR.
166 000010' 000510 VECTOR: 510+0 ;1ST DEVICE VECTOR.
167 000012' 340 HR1: .BYTE PRY7+0 ;1ST HR LEVEL.
168 000013' 000 BK2: .BYTE PRY0+0 ;2ND HR LEVEL.
169 000014' 000001 DVID1: 0+1 ;DEVICE INDICATOR 1.
170 000016' 000000 SR1: OPEN ;SWITCH REGISTER 1.
171 000020' 000000 SR2: OPEN ;SWITCH REGISTER 2.
172 000022' 000000 SR3: OPEN ;SWITCH REGISTER 3.
173 000024' 000000 SR4: OPEN ;SWITCH REGISTER 4.
174 ;*****
175 000026' 150000 STAT: 150000 ;STATUS WORD.
176 000030' 000536' INIT: START ;MODULE START ADDR.
177 000032' 000252' SPOINT: MODSP ;MODULE STACK POINTER.
178 000034' 000000 PASCNT: 0 ;PASS COUNTER.
179 000036' 000010 ICOUNT: 10 ;# OF ITERATIONS PER PASS=10
180 000040' 000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
181 000042' 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
182 000044' 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
183 000046' 000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
184 000050' 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
185 000052' 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
186 000054' 000000 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
187 000056' CONFIG:
188 000056' 000000 RES1: 0 ;RESERVED FOR MONITOR USE
189 000060' 000000 RES2: 0 ;RESERVED FOR MONITOR USE
190 000062' 000000 SVR0: OPEN ;LOC TO SAVE R0.
191 000064' 000000 SVR1: OPEN ;LOC TO SAVE R1.
192 000066' 000000 SVR2: OPEN ;LOC TO SAVE R2.
193 000070' 000000 SVR3: OPEN ;LOC TO SAVE R3.
194 000072' 000000 SVR4: OPEN ;LOC TO SAVE R4.
195 000074' 000000 SVR5: OPEN ;LOC TO SAVE R5.
196 000076' 000000 SVR6: OPEN ;LOC TO SAVE R6.
197 000100' 000000 CSMA: OPEN ;ADDR OF CURRENT CSR.
198 000102' SBADR: ;ADDR OF GOOD DATA, OR
199 000102' 000000 ACSR: OPEN ;CONTENTS OF CSR.
200 000104' WASADR: ;ADDR OF BAD DATA, OR
201 000104' 000000 ASTAT: OPEN ;STATUS REG CONTENTS.
202 000106' 000000 ERRTYP: ;TYPE OF ERROR
203 000106' 000000 ASB: OPEN ;EXPECTED DATA.
204 000110' 000000 AWAS: OPEN ;ACTUAL DATA.
205 000112' 001054' RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
206 000114' 000000 WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
207 000116' 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
208 000120' 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
209 000122' 000000G IDNUM: WWW ;MODULE IDENTIFICATION NUMBER=WWW
210 000124' 004240' RBUFVA: RBUF ;READ BUFFER VIRTUAL ADDRESS
211 000126' 000000 RBUFP: OPEN ;READ BUFFER PHYSICAL ADDRESS

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212 000130' 000000 RBUFEA: OPEN ;READ BUFFER EA BITS
213 000132' 000256 RBUFSZ: 256 ;SIZE OF THE READ BUFFER
214 000134' 000000 RBUFP: OPEN ;WRITE BUFFER PHYSICAL ADDRESS
215 000136' 000000 WHUFEA: OPEN ;WRITE BUFFER EA BITS
216 000140' 016000 WHUFRL: 16000 ;WRITE BUFFER SIZE REQUESTED
217 000142' 000000 WHUFSZ: OPEN ;WRITE BUFFER SIZE AVAILABLE
218 000144' 000000 CDRECT: OPEN ;CDATA/DATCK ERROR COUNT
219 000146' 000000 CDWDC1: OPEN ;CDATA/DATCK WORD COUNT
220 000150' 000000 FREE: OPEN ;RESERVED FOR FUTURE USE
221 000040 ;MODULE STACK STARTS HERE.
222 .REPT SPS12
223 .NLST
224 .WORD 0
225 .LIST
226 .ENDR
227 MODSP:
228 ;*****
229 ;*****
230 .LIST MC
231 .NLST ME
232 .MCALL STRUCT
233 000252' STRUCT
234 000001 SLSTIN=1
235 000001 SLSTTAG=1
236 000001 SLOCTAG=1
237 .TITLE DEC/X11 VERSION OF Q22BE
238
239
240 ;*COPYRIGHT (C) 1980
241 ;*DIGITAL EQUIPMENT CORP.
242 ;*MAYNARD, MASS. 0154
243 ;*
244 ;*PROGRAM BY EUGENE S. REDNER
245 ;*
246 ;*THIS PROGRAM WAS ASSEMBLED USING SPMAC
247 ;*
248 .SBTTL BASIC DEFINITIONS
249
250 ;*****
251
252 000252' 000000 SVINLV: .WORD 0 ;SAVE INTERRUPT LEVEL
253 000254' 000000 DV: .WORD 0
254 000256' 000000 MORE: .WORD 0
255 000260' 000011 Q22TB: .BLKW 11 ;TABLE FOR ADRS OF Q22BE
256 000302' 000011 VECT: .BLKW 11 ;DEVICE VECTOR TABLE
257 000324' 000000 DEVAUR: .WORD 0 ;ADDRESS
258 000326' 000000 DEVECT: .WORD 0 ;VECTOR
259 000330' TABLE1:
260 000330' 000000 CSR1: .WORD 0 ;CONTROL REGISTER ONE
261 000332' 000000 CSR2: .WORD 0 ;IDU
262 000334' 000000 RA: .WORD 0 ;ADDRESS REGISTER
263 000336' 000000 WC: .WORD 0 ;WORD COUNTER
264 000340' 000000 DATA: .WORD 0 ;DATA REGISTER
265 000342' 000000 LATCNT: .WORD 0 ;LATENCY COUNTER
266 000344' 000000 MVLNT: .WORD 0 ;MAX VALUE OF ABOVE
267 000346' 000000 SIMGOA: .WORD 0 ;SIMULTANEOUS GO REGISTER

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```
268 000350' 000000      ADR:  .WORD 0
269 000352' 000000      XMEM:  .WORD 0
270 000354' 000000      PA22:  .WORD 0      ;22 BIT STUFF
271 000356' 000000      EA22:  .WORD 0
272 000360' 000000      SVADR:  .WORD 0      ;PLACE TO SAVE ADR
273                      ;*****
274                      ;* DATA BIT DEFINITIONS
275
276          100000      BIT15= 100000
277          040000      BIT14= 40000
278          020000      BIT13= 20000
279          010000      BIT12= 10000
280          004000      BIT11= 4000
281          002000      BIT10= 2000
282          001000      BIT09= 1000
283          000400      BIT08= 400
284          000200      BIT07= 200
285          000100      BIT06= 100
286          000040      BIT05= 40
287          000020      BIT04= 20
288          000010      BIT03= 10
289          000004      BIT02= 4
290          000002      BIT01= 2
291          000001      BIT00= 1
292
293
294                      ;*****
295
296 000362'      VECT0:
297 000362'      LET SVINT := SVINT SET.BY #BIT00
298 000362' 052767 000001 000132      BIS      #BIT00,SVINT
299 000370'      INLINE <JMP      INTSRV>
300 000370' 000167 003026      JMP      INTSRV
301
302                      ;THE DEVICE INTERRUPT VECTORS
303                      ;WILL BE STORED IN THE WORD
304                      ;KNOWN AS SVINT. AS EACH INTRP
305                      ;HAPPENS IT WILL COME THRU THIS
306                      ;AREA BEFORE GOING TO THE INTR
307                      ;SERVICE ROUTINE.
308 000374'      VECT1:
309 000374'      LET SVINT := SVINT SET.BY #BIT01
310 000374' 052767 000002 000120      BIS      #BIT01,SVINT
311 000402'      INLINE <JMP      INTSRV>
312 000402' 000167 003014      JMP      INTSRV
313
314 000406'      VECT2:
315 000406'      LET SVINT := SVINT SET.BY #BIT02
316 000406' 052767 000004 000106      BIS      #BIT02,SVINT
317 000414'      INLINE <JMP      INTSRV>
318 000414' 000167 003002      JMP      INTSRV
319
320 000420'      VECT3:
321 000420'      LET SVINT := SVINT SET.BY #BIT03
322 000420' 052767 000010 000074      BIS      #BIT03,SVINT
323 000426'      INLINE <JMP      INTSRV>
```

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324 000426' 000167 002770      JMP      INTSRV
325
326 000432'      VECT4:
327 000432'      LET SVINT := SVINT SET.BY #BIT04
328 000432' 052767 000020 000062      BIS      #BIT04,SVINT
329 000440'      INLINE <JMP      INTSRV>
330 000440' 000167 002756      JMP      INTSRV
331
332 000444'      VECT5:
333 000444'      LET SVINT := SVINT SET.BY #BIT05
334 000444' 052767 000040 000050      BIS      #BIT05,SVINT
335 000452'      INLINE <JMP      INTSRV>
336 000452' 000167 002744      JMP      INTSRV
337
338 000456'      VECT6:
339 000456'      LET SVINT := SVINT SET.BY #BIT06
340 000456' 052767 000100 000036      BIS      #BIT06,SVINT
341 000464'      INLINE <JMP      INTSRV>
342 000464' 000167 002732      JMP      INTSRV
343
344 000470'      VECT7:
345 000470'      LET SVINT := SVINT SET.BY #BIT07
346 000470' 052767 000200 000024      BIS      #BIT07,SVINT
347 000476'      INLINE <JMP      INTSRV>
348 000476' 000167 002720      JMP      INTSRV
349
350                      ;*****
351
352                      ;*****
353                      ;* MORE EQUATES
354
355
356
357 000502' 000000      BLKMBT: .WORD 0      ;BLOCK MODE BIT
358 000504' 000000      INTRP:  .WORD 0      ;NUM OF INTRs THAT SHOULD HAPPEN
359 000506' 000000      MASK:   .WORD 0
360 000510' 170000      FIRADR: .WORD 170000
361 000512' 000510      FIRVEC: .WORD 510
362 000514' 000000      LPCNTR: .WORD 0      ;LOOP CNTR LOCATION
363 000516' 000000      SIMGBT: .WORD 0      ;SIMULT GO BIT
364 000520' 000000      SVINT:  .WORD 0
365 000522' 000000      INTCNT: .WORD 0      ;PLACE TO SAVE INTRs
366 000524' 000000      TWPSR2: .WORD 0      ;PLACE TO BUILD UP CSR2
367 000526' 000000      TWPRD:  .WORD 0
368 000530' 000000      SVWDCI: .WORD 0
369 000532' 000000      RY:     .WORD 0      ;USED IN SUBROUTINE 'GO'
370 000534' 000000
371
372                      ;*****
373
374
375 000536'      START:
376
377                      ;*****
378
379
```

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380 000536'
381 000536' 012767 004240' 177360 LET RBUFVA := #RBUF
382 000544'
383 000544' 016700 177354 LET R0 := RBUFVA ;THIS CODE IS NECESSARY SINCE
384 000550'
385 000550' 016767 177356 177754 LET SVWDCI := RBUFSZ ;THE Q22BE MUST TRANSFERS START AT ADDRS
386 000556'
387 000556' 042700 177700 LET R0 := R0 CLR.BY #177700 ;ZERO OR FORTY WHEN IN BLOCK MODE
388 000562'
389 000562'
390 000562' 020027 000040 WHILE R0 NE #40 AND R0 NE #0 DO
391 000566' 001414
392 000570' 005700
393 000572' 001412
394 000574'
395 000574' 062700 000002 LET R0 := R0 + #2 ;TRY NEXT LOCATION
396 000600'
397 000600' 062767 000002 177316 LET RBUFVA := RBUFVA + #2 ;UPDATED STRT LOC
398 000606'
399 000606' 005367 177720 LET SVWDCI := SVWDCI - #1 ;DEC WORD COUNT
400 000612'
401 000612' 042700 000100 LET R0 := R0 CLR.BY #BIT06 ;WATCH FOR OVFLOW
402 000616'
403 000616' 000761 ENDDO
404 000620'
405
406 000620'
407 000620' 005067 177660 LET INTRP := #0 ;ZERO THE INTR COUNTER
408 000624'
409 000624' 016767 177164 177422 LET DV := DVID1
410 000632'
411 000632' 042767 177400 177414 LET DV := DV CLR.BY #177400
412
413
414
415 ;*****
416 ; IF A DEVICE IS DROPPED THE PROGRAM RE-ENTRY
417 ; POINT IS HERE. GET HERE FROM THE SUBROUTINE 'DMPDEV'
418 ;*****
419 000640'
420 000640'
421 000640' 016767 177410 177640 INLINE <REENTRY> ;RE-ENTRY HERE
422 000646'
423 000646' 016767 177402 177402 LET MASK := DV ;SHOULD ONLY BE 10 DEVICES
424 000654'
425 000654' 016700 177374 LET MORE := DV
426 000660'
427 000660' 005700
428 000662' 001005
429
430
431 000664' 104403 000000' 003774' .LIST MC
432 000672' 104410 000000' .LIST ME
433
434
435 000676'
  
```

```

436 000676'
437
438
439 000676'
440 000676' 000241
441 000700'
442 000700'
443 000700' 005700
444 000702' 001405
445 000704'
446 000704' 006200
447 000706'
448 000706' 103002
449 000710'
450 000710' 005267 177570
451 000714'
452 000714'
453 000714'
454 000714' 000771
455 000716'
456
457
458
459
460
461
462
463
464
465
466 000716'
467 000716' 012706 000252'
468
469
470
471
472
473
474
475
476
477 000722'
478 000722' 012700 000022
479 000726'
480 000726' 012701 000260'
481 000732'
482 000732'
483 000732' 005700
484 000734' 001403
485 000736'
486 000736' 005021
487 000740'
488 000740' 005300
489 000742'
490 000742' 000773
491 000744'
  
```

```

492
493 000744'
494 000744' 016767 177540 177352 LET DEVADR := FIRADR ;1 ST ADRS IN10 DEVADR
495 000752'
496 000752' 016767 177534 177346 LET DEVECT := FIRVEC ;FIRST VECTOR IN DEVECT
497 000760'
498 000760' 012704 000260' LET R4 := #Q22TB
499 000764'
500 000764' 012703 000302' LET R3 := #VECT ;SET UP TABLE POINTERS
501
502
503 *****
504
505 000770'
506 000770'
507 000770' 005767 177262
508 000774' 001427
509 000776'
510 000776' 000241
511 001000'
512 001000' 006267 177252
513 001004'
514 001004' 103003
515 001006'
516 001006' 004767 000716
517
518 001012'
519 001012' 000417
520 001014'
521 001014'
522 001014' 062767 000020 177302 LET DEVADR := DEVADR + #20 ;GET NEXT ADRS
523 001022'
524 001022' 062767 000004 177276 LET DEVECT := DEVECT + #4 ;NEXT VECTOR
525 001030'
526 001030' 026727 177270 170200 IF DEVADR EQ #170200 THEN
527 001036' 001005
528
529
530
531 001040' 104403 000000' 003770'
532 001046' 104410 000000'
533
534
535
536 001052'
537 001052'
538 001052'
539 001052'
540 001052'
541 001052' 000746
542 001054'
543
544
545
546
547
  
```

WHILE MORE NE #0 DO ;MORE HAS DEVICES
 TST MORE \$50010:
 BEQ \$50011
 INCLC <CLC> ;CLR THE C BIT
 CLC
 LET MORE := MORE SHIFT -1 ;IS THERE A DEVICE
 ASK MORE
 IF CND CS THEN ;IS C SET ?
 BCC \$50012
 CALL CLRREG ;YES, GO CLR REGISTERS AND
 JSR PC,CLRREG
 ;SET UP TABLES
 ;NO, A DEVICE IS NOT HERE
 BR \$50013
 \$50012:
 ADD #20,DEVADR
 ADD #4,DEVECT
 CMP DEVADR,#170200
 BNE \$50014
 .LIST MC
 .LIST ME
 MSGNS,BEGIN,TEXT1 ;ASCII MESSAGE CALL WITH COMMON HEADER
 ENDS,BEGIN
 .LIST MC
 .LIST ME
 ENDIF
 \$50014:
 ENDIF
 \$50013:
 ENDDU
 \$50011:

 ;THE PRECEDING CODE FOUND THE ADRS/VECTOR PAIRS
 ;AND ALSO PLACED THE DEVICE ADRS IN THE CORRECT
 ;TABLE.

```

548
549
550 001054'
551
552
553
554
555
556 001054' 104415 000000' 000124'
557
558
559 001062'
560 001062' 005067 177414
561
562 001066'
563 001066' 017767 177250 177440 LET RY := #LATCNT ;CLR LAT CNTR BY READING IT
564
565 001074'
566 001074' 005067 177424
567 001100'
568 001100' 012706 000252'
569 001104'
570 001104' 005067 177410
571
572
573 001110' 104414 000000'
574
575
576
577
578
579
580
581
582
583 001114'
584 001114' 016767 177140 177202 LET DEVADR := Q22TB ;FIRST ADRS IN THE TABLE
585 001122'
586 001122' 012704 000260' LET R4 := #Q22TB ;FIRST LOC OF TABLE
587 001126'
588 001126' 012703 000302' LET R3 := #VECT ;ADRS OF VECTOR TABLE
589
590 001132'
591 001132'
592 001132'
593 001132' 005767 177166
594 001136' 001002
595 001140'
596 001140' 000167 000352
597 001144'
598 001144'
599 001144'
600 001144' 011367 177156
601
602
603
  
```

RESTRI:

 .LIST MC
 .LIST ME
 GETPAS,BEGIN, RBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
 .LIST MC
 .LIST ME
 LET BLKMBT := #0 ;BLOCK MODE BIT TO ZERO
 CLR BLKMBT
 LET RY := #LATCNT ;CLR LAT CNTR BY READING IT
 MOV #LATCNT,RY
 LET INTCNT := #0 ;INTR COUNTER WORD TO 0
 CLR INTCNT
 LET R6 := #MODSP ;SIM GO BIT IN10 TO ZERO
 MOV #MODSP,R6
 LET SIMGBT := #0
 CLR SIMGBT
 .LIST MC
 .LIST ME
 GWRBUF, BEGIN ;GET WRITE BUFFER INFORMATION
 .LIST MC
 .LIST ME

 ;WE WILL NOW LOOK TO SEE IF THERE ARE ANY Q22'S IN
 ;THE APPROPRIATE TABLE.

 LET DEVADR := Q22TB ;FIRST ADRS IN THE TABLE
 MOV Q22TB,DEVADR
 LET R4 := #Q22TB ;FIRST LOC OF TABLE
 MOV #Q22TB,R4
 LET R3 := #VECT ;ADRS OF VECTOR TABLE
 MOV #VECT,R3
 INCLC <ESR13> ;LOCATION
 ESR13:
 IF DEVADR EQ #0 THEN ;IS CONTENTS OF LOC EQUAL
 TST DEVADR
 BNE \$50015
 INCLC <JMP SIMGO>
 JMP SIMGO
 ENDIF
 \$50015:
 LET DEVECT := (R3) ;FOUND THE RIGHT ONE
 MOV (R3),DEVECT

 ;WE NOW HAVE THE Q22 ADRS AND VECTOR


```

604
605
606
607 001150'
608 001150' 012767 000007 177074 LET SVINLV := #7          ;START W/INTR LEVEL SEVEN
609 001156' 004767 001122 CALL ADKSET          ;GO SET ADK FOR REGISTERS
610 001156' 004767 001122          ;AND PSW, GET READY TO DO
611          ;SOME DATA OUT XFRS
612
613
614
615
616
617 001162'
618 001162'
619 001162' 026727 177064 000003
620 001170' 001542
621
622 001172'
623 001172' 026727 177304 000001 IF BLKMBT EQ #1 THEN          ;8 XFRS/CYCLE
624 001200' 001004          ;BLK MODE XFRS DATA OUT
625 001202'
626 001202' 012777 000721 177120 LET @CSR1 := #721          ;BA MODE XFRS
627 001210'
628 001210' 000403          ;BA IS THE DATA SRCE
629 001212'
630 001212'
631 001212' 012777 000661 177110 LET @CSR1 := #661          ;NON BLK XFRS DATA OUT
632 001220'
633 001220'
634
635
636 001220'
637 001220' 005000
638 001222'
639 001222' 166700 177304 LET @WC := R0          ;ADJUST THE WC IF NECSY
640 001226'
641 001226' 010077 177104          ;CUMPLIMENT THIS
642
643
644 001232'
645 001232' 004767 001156          ;THE BA IS THE SOURCE
646 001236'
647 001236' 004767 000730
648 001242'
649 001242' 026727 177234 000001 IF BLKMBT EQ #1 THEN          ;22 HIT ADK AND INTR LEVEL
650 001250' 001003          ;JSR PC,BIT22
651 001252'
652 001252' 052767 001000 177246 LET IMPSK2 := IMPSK2 SET BY #BIT09
653 001260'
654 001260'
655
656
657 001260'
658 001260' 004767 001526 CALL GU          ;GO START THE XFR
659

```

```

660 001264'
661 001264' 004767 001404 CALL CHKDA          ;GU CHECK DATA
662
663
664
665
666
667
668
669
670
671 001270'
672 001270' 026727 177206 000001 IF BLKMBT EQ #1 THEN          ;16 XFRS/CYCLE
673 001276' 001004          ;CMP BLKMBT,#1
674 001300'
675 001300' 012777 000547 177022 LET @CSR1 := #547          ;BLK MODE XFRS
676 001306'
677 001306' 000403          ;MOV #547,@CSR1
678 001310'
679 001310'
680 001310' 012777 000407 177012 LET @CSR1 := #407          ;NON BLK , HUG MODE XFRS
681 001316'
682 001316'
683
684
685
686 001316'
687 001324' 001416
688 001326'
689 001326' 016700 176610 LET @WC := @BUFSZ          ;YES
690 001332'
691 001332' 020027 100000 IF @WC EQ #100000 THEN          ;MOV @BUFSZ,R0
692 001336' 001003
693 001340'
694 001340' 012700 077777 LET @WC := #77777          ;CMP R0,#100000
695 001344'
696 001344' 000401          ;MOV #77777,R0
697 001346'
698 001346'
699 001346' 005400
700 001350'
701 001350'
702 001350'
703 001350' 010077 176762
704 001354'
705 001354' 017700 176762 LET @WC := @LATCNT          ;LD WORD COUNTER
706 001360'
707 001360' 000403          ;CLR LAT CNT TO STOP ERR BIT
708 001362'
709 001362'
710 001362' 012777 177000 176746 LET @WC := #177000          ;MOV @LATCNT,R0
711 001370'
712 001370'
713
714
715 001370'

```

```

716 001370' 004767 001150
717 001374'
718 001374' 004767 000572      INLINE <JSR PC,ROUT1>      ;GET INTR LEVEL      JSR    PC,BIT23
719
720 001400'
721 001400' 026727 177076 000001      IF BLKMBT EQ #1 THEN      JSR    PC,ROUT1
722 001406' 001003
723 001410'
724 001410' 052767 001000 177110      LET TMPSTR2 := TMPSTR2 SET BY #BIT09      CMP    BLKMBT,#1
725 001416'
726 001416'
727 001416'
728 001416' 004767 001370      CALL    GO      ;GO START THE XFR      BNE    $50031
729
730 001422'
731 001422' 032767 000001 176366      IF #BIT00 SET IN SM1 THEN      JSR    PC,GO
732 001430' 001415
733 001432'
734 001432' 026727 177044 000001      IF BLKMBT NE #1 THEN      ;HAS OPERATOR SELECTED TO      MOV    #BIT00,SM1
735 001440' 001404      ;TRY BLOCK MODE XFRS      ;YES, SET MY BLOCK MD BIT      $50032
736
737 001442'
738 001442' 012767 000001 177032      LET BLKMBT := #1      ;TRY BLOCK MODE XFRS      MOV    #1,BLKMBT
739 001450'
740 001450' 000404      ELSE
741 001452'
742 001452'
743 001452' 005067 177024      LET BLKMBT := #0      ;MAKE SURE BLK MODE BIT      BR    $50034
744 001456'
745 001456' 005367 176570      LET SVINLV := SVINLV - #1      ;IS CLR. SUBT 1 FROM INTR LVL      $50033:
746 001462'
747 001462'
748 001462'
749 001462' 000404      ELSE
750 001464'
751 001464'
752 001464' 005067 177012      LET BLKMBT := #0      ;MAKE SURE BLK MODE BIT      BR    $50035
753 001470'
754 001470' 005367 176556      LET SVINLV := SVINLV - #1      ;IS CLR. SUBT 1 FROM INTR LVL      $50032:
755 001474'
756 001474'
757 001474'
758 001474' 000632      ENDDU      ;IS CLR. SUBT 1 FROM INTR LVL      CLR    BLKMBT
759 001476'
760
761 001476'
762 001476' 062704 000002      LET R4 := R4 + #2      ;NXT LOC IN Q22TB      DEC    SVINLV
763 001502'
764 001502' 011467 176616      LET DEVADR := (R4)      ;MAKE SURE BLK MODE BIT      CLC    BLKMBT
765 001506'
766 001506' 062703 000002      LET R3 := R3 + #2      ;NXT VECTOR IN TABLE      FROM INTR LVL
767 001512'
768 001512' 000167 177414      INLINE <JMP ESR13>      ;GO BACK FOR NXT ADRS      DEC    SVINLV
769
770
771

```

```

772 ; THIS SECTION OF THE PROGRAM WILL SET UP ALL
773 ; DEVICES TO ATTEMPT A SIMULTANEOUS TRANSFER
774 ; VIA THE SINGUA REGISTERS.
775 ;
776 ;*****
777
778 001516'
779 001516'
780 001516' 012767 000007 176526      SIMGO:      ;FIRST INTR LEVEL      MOV    #7,SVINLV
781 001524'
782 001524' 106427 000340      INLINE <MIPS #340>      ;RAISE CPU LVL TO 7      MIPS    #340
783 001530'
784 001530' 012767 000001 176762      LET SIMGBT := #1      ;FIRST INTR LEVEL      MOV    #1,SIMGBT
785
786 001536'
787 001536'
788
789 001536'
790 001536' 012703 000302'      LET R3 := #VECT      ;TABLE POINTERS. R5 IS USED TO ALTER      MOV    #VECT,R3
791 001542'
792 001542' 012704 000260'      LET R4 := #Q22TB      ;XFRS SO THAT 1 DEVICE WILL DO DATA INS      MOV    #Q22TB,R4
793 001546'
794 001546' 012705 000001      LET R5 := #1      ;AND THE NXT DEVICE WILL DO DATA OUTS      MOV    #1,R5
795
796 001552'
797 001552'
798 001552' 005713
799 001554' 001443      WHILE (R3) NE #0 DO      ;IF R3 IS 0 THEN VECTOR TABLE      $50036:
800
801 001556'
802 001556' 011367 176544      LET DEVECT := (R3)      ;FIRST VECTOR      MOV    (R3),DEVECT
803 001562'
804 001562' 011467 176536      LET DEVADR := (R4)      ;FIRST ADRS      MOV    (R4),DEVADR
805
806 001566'
807 001566' 004767 000512      CALL    ADRSET      ;GET REG ADRS, SET MASK      JSR    PC,ADRSET
808
809 001572'
810 001572' 005000      LET R0 := #0      ;ADJUST THE WC IF NDCSI      JSR    PC,ADRSET
811 001574'
812 001574' 166700 176732      LET R0 := R0 - SVWDCT      ;COMPLIMENT THIS      CLC    R0
813 001600'
814 001600' 010077 176532      LET #WC := R0      ;COMPLIMENT THIS      SUB    SVWDCT,R0
815 001604'
816 001604' 004767 000362      INLINE <JSR PC,ROUT1>      ;INTR LEVELS      MOV    R0,#WC
817 001610'
818 001610' 020527 000001      IF R5 EQ #1 THEN      ;INTR LEVELS      JSR    PC,ROUT1
819 001614' 001007
820 001616'
821 001616' 012777 000767 176504      LET @CSR1 := #767      ;DATA SRCE IS THE BA      CMP    R5,#1
822 001624'
823 001624' 004767 000564      INLINE <JSR PC,BIT22>      ;DATA OUT, 4 XFRS/CYCLE      BNE    $50040
824 001630'
825 001630' 005005      LET R5 := #0      ;DATA OUT, 4 XFRS/CYCLE      MOV    #767,@CSR1
826 001632'
827 001632' 000407      ELSE      ;DATA OUT, 4 XFRS/CYCLE      JSR    PC,BIT22
828
829
830

```

```
828 001634'
829 001634'
830 001634' 012777 000507 176466
831 001642'
832 001642' 004767 000676
833 001646'
834 001646' 012705 000001
835 001652'
836 001652'
837 001652'
838 001652' 062703 000002
839 001656'
840 001656' 062704 000002
841 001662'
842 001662' 000733
843 001664'
844
845
846
847
848 001664'
849 001664' 005067 176634
850
851 001670'
852 001670' 005067 176626
853 001674'
854 001674' 004767 001112
855
856 001700'
857 001700' 005367 176346
858 001704'
859 001704' 026727 176342 000003
860 001712' 001402
861 001714'
862 001714' 000167 177616
863 001720'
864 001720'
865
866
867
868
869 001720'
870
871
872
873 001720' 104413 000000'
874
875
876
877
878
879 001724'
880 001724' 000167 177124
881
882
```

```
      LET %CSR1 := #507          ;2 XFRS/CYCLE
      INLINE <JSR    PC,BIT23>
      LET R5 := #1              ;DATA IN
      ENDIF
      LET R3 := R3 + #2
      LET R4 := R4 + #2
      ENDDU
      BR $50037
      ;*****
      ;ALL DEVICES ARE NOW SET TO GO
      ;*****
      LET INTCNT := #0          ;SET INTR CNTR TO 0
      CLR INTCNT
      LET SVINT := #0          ;CLR SAVE INTERRUPTS
      CLR SVINT
      INLINE <JSR    PC,GO>
      JSR PC,GO
      LET SVINLV := SVINLV - #1 ;SUBT 1 FROM INTR LEVEL
      DEC SVINLV
      IF SVINLV NE #3 THEN
      CMP SVINLV,#3
      BEQ $50042
      INLINE <JMP ESR33>        ;RE ITERATE
      JMP ESR33
      ENDIF
      ;THIS SECTION
      $50042:
      ;*** *****
      ;WHERE TO GO FROM HERE
      ;*****
      PASS:
      .NLIST MC
      .LIST ME
      ENDITS,BEGIN
      ;SIGNAL END OF ITERATION.
      ;MONITOR SHALL TEST END OF PASS
      .LIST MC
      .NLIST ME
      INLINE <JMP    RESTRT>
      JMP RESTRT
```

```
883
884
885
886
887
888
889
890
891
892
893
894
895
896
897
898
899 001730'
900 001730'
901
902
903 001730'
904 001730' 016700 176370
905 001734'
906 001734' 010067 176370
907 001740'
908 001740' 010067 176366
909 001744' 062767 000002 176360
910 001752'
911 001752' 010067 176356
912 001756' 062767 000004 176350
913 001764'
914 001764' 010067 176346
915 001770' 062767 000006 176340
916 001776'
917 001776' 010067 176336
918 002002' 062767 000010 176330
919 002010'
920 002010' 010067 176326
921 002014' 062767 000012 176320
922 002022'
923 002022' 010067 176316
924 002026' 062767 000014 176310
925 002034'
926 002034' 010067 176306
927 002040' 062767 000016 176300
928
929 002046'
930 002046' 017700 176270
931 002052'
932 002052' 005077 176252
933 002056'
934 002056' 005077 176252
935 002062'
936 002062' 005077 176250
937 002066'
938 002066' 005077 176246
```

```
      .SBTIL SUB-ROUTINES CALLED OUT BY THE MAIN PROGRAM
      ;THIS WILL BE THE END SECTION OF THE DEC/111
      ;SOFTWARE MODULE. ALL NORMAL ROUTINES
      ;FOUND IN AN END SECTION OF A SOFTWARE MODULE
      ;WILL BE INCLUDED HERE.
      ;*****
      ; THIS ROUTINE WILL CHECK ALL DEVICES THAT ARE THERE
      ; AND WILL CLEAR THEIR APPROPRIATE REGISTERS.
      ; IN ADDITION THIS ROUTINE WILL SET UP THE CORRECT
      ; PSW AND THE APPROPRIATE ADRS FOR THE INTERRUPT
      ; SERVICE ROUTINE.
      ;*****
      ;R3-R4 HAVE BEEN SET UP ALREADY
      ROUTINE CLRREG
      CLRREG:
      LET R0 := DEVADR
      LET CSR1 := R0
      ;START WITH THIS ADRS
      MOV DEVADR,R0
      LET CSR2 := R0 + #2
      MOV R0,CSR2
      MOV R0,CSR2
      ADD #2,CSR2
      LET BA := R0 + #4
      MOV R0,BA
      ADD #4,BA
      LET WC := R0 + #6
      MOV R0,WC
      ADD #6,WC
      LET DATA := R0 + #10
      MOV R0,DATA
      ADD #10,DATA
      LET LATCNT := R0 + #12
      MOV R0,LATCNT
      ADD #12,LATCNT
      LET MVL CNT := R0 + #14
      MOV R0,MVL CNT
      ADD #14,MVL CNT
      LET SIMGOA := R0 + #16
      MOV R0,SIMGOA
      ADD #16,SIMGOA
      LET R0 := @LATCNT
      ;CLR LAT COUNTER BY
      MOV @LATCNT,R0
      LET %CSR1 := #0
      ;READING IT
      CLR %CSR1
      LET %BA := #0
      CLR %BA
      LET %WC := #0
      CLR %WC
      LET @DATA := #0
      CLR @DATA
```

```
934 002072' LET @CSR2 := #0
940 002072' 005077 176234 CLR @CSR2
941 002076' LET (R4)+ := DEVA DR MOV DEVA DR,(R4)+
942 002076' 016724 176222 MOV DEVECT,(R3)+
943
944 002102' LET (R3)+ := DEVECT MOV DEVECT,(R3)+
945 002102' 016723 176220
946
947 002106' LET R0 := FIRVEC MOV FIRVEC,R0
948 002106' 016700 176400 ; VECTOR OF FIRST POSSIBLE ONE INTO R1
949 002112' LET R1 := #VECT0 MOV #VECT0,R1
950 002112' 012701 000362' WHILE R0 NE DEVECT DO ; IF NOT THIS ONE
951 002116' ; $50045:
952 002116' ; CMP R0,DEVECT
953 002116' 020067 176204 ; BEQ $50046
954 002122' 001405 LET R0 := R0 + #4 ; THEN ADD 4 FOR THE
955 002124' ; ADD #4,R0
956 002124' 062700 000004 LET R1 := R1 + #12 ; NEXT ONE
957 002130' ; ADD #12,R1
958 002130' 062701 000012 ENDDO BR $50045
959 002134' ; $50046:
960 002134' 000770 LET (R0) := R1 ; PROPER ADRS
961 002136' MOV R1,(R0)
962 002136'
963 002136' 010110 LET DEVECT := DEVECT + #2 ; 2 MORE FOR THE PSW
964 ; ADD #2,DEVECT
965 002140' 062767 000002 176160 LET @DEVECT := #340 MOV #340,@DEVECT
966 002146' 012777 000340 176152 LET DEVA DR := DEVA DR + #20 ADD #20,DEVA DR
967 002154' 062767 000020 176142 LET DEVECT := DEVECT + #2 ; NEXT ADRS/VECTOR PAIR
968 002162' 062767 000002 176136 ADD #2,DEVECT
969
970 002170' ENDRTN
971
972 002170' $50043:
973 002170' $50044:
974 002170' 000207 RTS PC
975
976
977
978
979
980
981
982 ;*****
983 ;
984 ; ROUTINE TO MOVE INTERRUPT LEVEL,SVINLV, INTO CSR2
985 ;
986 ;*****
987
988
989 ROUT1:
990 SELECT SVINLV OF 7 BASE 3
991 002172' 016746 176054 MOV SVINLV, -(SP)
992 002176' 162716 000003 SUB #3, (SP)
993 002202' 002435 BLT $50055
994 002204' 026727 176042 000007 CMP SVINLV, #7
```

```
995 002212' 003031 HGT $50055
996 002214' 006316 ASL (SP)
997 002216' 062716 002224' ADD #50047, (SP)
998 002222' 013607 MOV @ (SP)+, PC
999 002224' $50047:
1000 002224' 002266' .WORD $50054
1001 002226' 002266' .WORD $50053
1002 002230' 002256' .WORD $50052
1003 002232' 002246' .WORD $50051
1004 002234' 002236' .WORD $50050
1005
1006 002236' CASE 7 $50050:
1007 002236' LET IMPSR2 := #32 MOV #32, IMPSR2
1008 002236' CASE 6 BR $50056
1009 002236' 012767 000032 176262 LET IMPSR2 := #12 MOV #12, IMPSR2
1010 002244' CASE 5 BR $50052
1011 002244' 000416 LET IMPSR2 := #6 MOV #6, IMPSR2
1012 002246' CASE 4 BR $50056
1013 002246' 012767 000012 176252 LET IMPSR2 := #6 MOV #6, IMPSR2
1014 002254' CASE 3 BR $50052
1015 002254' 000412 LET IMPSR2 := #6 MOV #6, IMPSR2
1016 002256' CASE 2 BR $50056
1017 002256' 012767 000006 176242 LET IMPSR2 := #6 MOV #6, IMPSR2
1018 002256' CASE 1 BR $50056
1019 002256' 012767 000006 176242 LET IMPSR2 := #6 MOV #6, IMPSR2
1020 002264' DEFAULT BR $50056
1021 002264' 000406 LET IMPSR2 := #2 MOV #2, IMPSR2
1022 002266' ENDSLECT
1023 002266'
1024 002266' 012767 000002 176232 LET IMPSR2 := #2 MOV #2, IMPSR2
1025 002266' 000402
1026 002274'
1027 002274' 000402
1028 002276' 062706 000002
1029 002276'
1030 002302'
1031 002302' 000207
1032 002302' 000207
1033
1034
1035 ;*****
1036 ;
1037 ; THIS ROUTINE WILL SET UP CORRECT REGISTER ADDRESSES
1038 ; FOR AN KNOWN DEVICE. IT WILL ALSO SET THE CORRECT
1039 ; BITS IN THE INTERRUPT MASK
1040 ;
1041 ;*****
1042
1043 ROUTINE ADRSET ADRSET:
1044 002304' LET MASK := #0 ; INITIALLY CLR MASK
1045 002304' CLR MASK
1046
1047 002304' IF SIMGBT EQ #0 THEN ; ONE DEV AT A TIME
```

```

1051 002310' 005767 176204      TST     SIMGBT
1052 002314' 001016      BNE     $50061
1053 002316'              LET R1 := FIRADR      ;FIRST POSSIBLE ADRES
1054 002316' 016701 176166      MOV     FIRADR,R1
1055 002322'              LET R0 := #B100        ;FIRST BIT
1056 002322' 012700 000001      MOV     #B100,R0
1057 002326'              WHILE DEVADR NE R1 DO
1058 002326'
1059 002326' 026701 175772      $50062:
1060 002342' 001404      CMP     DEVADR,R1
1061 002344'              BREQ    $50063
1062 002344' 006300      INLINE <ASL R0>      ;SHFT 1 TO THE LEFT
1063 002346'              ASL     R0
1064 002346' 062701 000020      LET R1 := R1 + #20      ;NXT ADRES
1065 002342'              ENDDO
1066 002342' 000771      BR      $50062
1067 002344'              $50063:
1068 002344'              LET MASK := MASK SET.BY R0
1069 002344' 050067 176136      BIS     R0,MASK
1070 002350'              ELSE
1071 002350' 000403      BR      $50064
1072 002352'              $50061:
1073 002352'              LET MASK := DV          ;ALL DEVICES AT ONCE
1074 002352' 016767 175676 176126      MOV     DV,MASK
1075 002360'              ENDIF
1076 002360'              $50064:
1077
1078              ;*****
1079              ;THE ABOVE CODE SETS UP THE INTR MASK
1080              ;*****
1081
1082 002360'              LET R0 := DEVADR          ;STARTING REG ADRES
1083 002360' 016700 175740      MOV     DEVADR,R0
1084 002364'              LET R1 := #10
1085 002364' 012701 000010      MOV     #10,R1
1086 002370'              LET R2 := #CSR1
1087 002370' 012702 000330'      MOV     #CSR1,R2
1088 002374'              WHILE R1 NE #0 DO
1089 002374'
1090 002374' 005701      $50065:
1091 002376' 001405      TST     R1
1092 002400'              BEQ     $50066
1093 002400' 010022      LET (R2)+ := R0
1094 002402'              MOV     R0,(R2)+
1095 002402' 005301      DEC     R1
1096 002404'              LET R0 := R0 + #2      ;NXT ADRES
1097 002404' 062700 000002      DEC     R1
1098 002410'              ADD     #2,R0
1099 002410' 000771      ENDDO
1100 002412'              BR      $50065
1101
1102              $50066:
1103 002412'              ENDRTN
1104 002412'
1105 002412'              $50057:
1106 002412' 000207      $50060:
                          RTS     PC
  
```

```

1107
1108
1109              ;*****
1110              ;
1111              ; ROUTINE TO SET UP A 22 BIT ADDRESS FOR USE IN THE
1112              ; MAIN PROGRAM FOR THE READ BUFFER INSTS.
1113              ;*****
1114
1115
1116 002414'              BIT22:
1117 002414'              IF #ADDR22 SETIN CONFIG THEN
1118 002414' 032767 001000 175434      BIT     #ADDR22,CONFIG
1119 002422' 001426      BEQ     $50067
1120 002424'              LET ADR := RBUFPA      ;LOWER 16 BITS INTO ADR
1121 002424' 016767 175476 175716      MOV     RBUFPA,ADR
1122 002432'              LET XMEM := RBUFEA     ;BITS 17 AND 18
1123 002432' 016767 175472 175712      MOV     RBUFEA,XMEM
1124
1125              .NLIST MC
1126 002440' 104416 000000' 000350'      MAP22S, BEGIN,ADR      ; GET 22-BIT ADDR FROM 18-BIT ADDR
1127              .LIST MC
1128              .NLIST ME
1129 002446'              LET #BA := PA22          ;LOAD 16 BITS INTO THE BA
1130 002446' 016777 175702 175660      MOV     PA22,#BA
1131              ;MOVE EA BITS FOR CSR1
1132 002454'              LET EA22 := EA22 SHIFT 2
1133 002454' 006367 175676      ASL     EA22
1134 002460' 006367 175672      ASL     EA22
1135 002464'              INLINE <SWAB EA22>      ;EA BIT 0 NOW IN BIT 10
1136 002464' 000367 175666      SWAB    EA22
1137 002470'              LET @CSR1 := @CSR1 SET.BY EA22
1138 002470' 056777 175662 175632      BIS     EA22,@CSR1
1139              ;SET EXTENDED ADRES BITS IN CSR1
1140 002476'              ELSE
1141 002476' 000421      BR      $50070
1142 002500'              $50067:
1143 002500'              LET #BA := RBUFPA      ;ONLY A 16 BIT ADRES
1144 002500' 016777 175422 175626      MOV     RBUFPA,#BA
1145 002506'              IF #BIT04 SETIN RBUFEA THEN
1146 002506' 032767 000020 175414      BIT     #BIT04,RBUFEA
1147 002514' 001403      BEQ     $50071
1148 002516'              LET @CSR1 := @CSR1 SET.BY #BIT10
1149 002516' 052777 002000 175604      BIS     #BIT10,@CSR1
1150 002524'              ENDIF
1151 002524'              $50071:
1152 002524'              IF #BIT05 SETIN RBUFEA THEN
1153 002524' 032767 000040 175376      BIT     #BIT05,RBUFEA
1154 002532' 001403      BEQ     $50072
1155 002534'              LET @CSR1 := @CSR1 SET.BY #BIT11
1156 002534' 052777 004000 175566      BIS     #BIT11,@CSR1
1157 002542'              ENDIF
1158 002542'              $50072:
1159              ;EA BITS INTO CSR1
1160 002542'              ENDRTN
1161 002542'              $50070:
1162
  
```

```

1163 002542'
1164 002542' 000207
1165
1166
1167
1168
1169
1170
1171
1172
1173 002544'
1174
1175 002544'
1176 002544' 032767 001000 175304
1177 002552' 001426
1178 002554'
1179 002554' 016767 175354 175566
1180 002562'
1181 002562' 016767 175350 175562
1182
1183
1184
1185 002570' 104416 000000' 000350'
1186
1187
1188
1189 002576'
1190 002576' 016777 175552 175530
1191 002604'
1192 002604' 006367 175546
1193 002610' 006367 175542
1194 002614'
1195 002614' 000367 175536
1196
1197 002620'
1198 002620' 056777 175532 175502
1199 002626'
1200 002626' 000421
1201 002630'
1202 002630'
1203 002630' 016777 175300 175476
1204 002636'
1205 002636' 032767 000020 175272
1206 002641' 001403
1207 002646'
1208 002646' 056777 002000 175454
1209 002654'
1210 002654'
1211 002654'
1212 002654' 032767 000040 175254
1213 002662' 001403
1214 002664'
1215 002664' 052777 004000 175436
1216 002672'
1217 002672'
1218

```

INLINE <RTS PC> ;GO BACK WHERE YOU CAME FROM
 RTS PC
 ;*****
 ;
 ; THIS ROUTINE WILL SET A 22 BIT ADDR FOR THE
 ; GET WRITE BUFFER INSTS.
 ;
 ;***
 BIT22:
 IF #ADDR22 SETIN CONFIG THEN
 LET ADP := #BUFPA ;LOWER 16 OF 18
 LET XMEM := #BUFEA
 MOV #BUFEA,XMEM
 .NLIST MC
 .LIST ME
 MAP22S, BEGIN,ADR ; GET 22-BIT ADDR FROM 18-BIT ADDR
 .LIST MC
 .NLIST ME
 LET #BA := PA22
 MOV PA22,#BA
 LET EA22 := EA22 SHIFT 2
 ASL EA22
 ASL EA22
 INLINE <SWAB EA22> ;SWAP BYTES FOR
 SWAB EA22
 LET #CSR1 := #CSR1 SET.BY EA22
 BIS EA22,#CSR1
 ELSE
 BR \$50074
 LET #BA := #BUFPA ;16 BIT ADRES
 MOV #BUFPA,#BA
 IF #BIT04 SETIN #BUFEA THEN
 BIT #BIT04,#BUFEA
 BEQ \$50075
 LET #CSR1 := #CSR1 SET.BY BIT10
 BIS BIT10,#CSR1
 ENDIF
 \$50075:
 IF #BIT05 SETIN #BUFEA THEN
 BIT #BIT05,#BUFEA
 BEQ \$50076
 LET #CSR1 := #CSR1 SET.BY #BIT11
 BIS #BIT11,#CSR1
 ENDIF
 \$50076:
 ;EA BITS INTO CSR1

```

1219 002672'
1220 002672'
1221
1222 002672'
1223 002672' 000207
1224
1225
1226
1227
1228
1229
1230
1231
1232
1233 002674'
1234 002674'
1235
1236 002674'
1237 002674' 016700 175224
1238
1239
1240
1241
1242
1243
1244 002700'
1245 002700' 016701 175626
1246 002704'
1247 002704' 032767 001000 175144
1248 002712' 001403
1249 002714'
1250 002714' 016702 175434
1251 002720'
1252 002720' 000402
1253 002722'
1254 002722'
1255 002722' 016702 175200
1256 002726'
1257 002726'
1258 002726'
1259 002726' 062702 000002
1260
1261 002732'
1262 002732'
1263 002732' 005701
1264 002734' 001425
1265 002736'
1266 002736' 021002
1267 002740' 001415
1268 002742'
1269 002742' 017767 175362 175132
1270 002750'
1271 002750' 010267 175126
1272 002754'
1273 002754' 005067 175124
1274 002760'

```

ENDIF
 \$50074:
 INLINE <RTS PC>
 RTS PC
 ;*****
 ;
 ; DATA CHECK ROUTINE
 ;
 ;*****
 ROUTINE CHKDA
 CHKDA:
 LET R0 := #BUFVA ;STARTING ADRES OF RD BUF
 MOV #BUFVA,R0
 ;THIS ADDRESS WAS ADJUSTED SO
 ;THAT STARTING ADDRESS WOULD
 ;START AT EITHER 0 OR 40. THIS
 ;IS NECESSARY FOR BLOCK MODE
 ;TRANSFERS TO COMMENCE AT THE
 ;CORRECT BOUNDARY.
 LET R1 := SV#UCT
 MOV SV#UCT,R1
 IF #ADDR22 SETIN CONFIG THEN ;22 BIT MONITOR
 BIT #ADDR22,CONFIG
 BEQ \$50101
 LET R2 := PA22 ;1 DATA WORD IS 2 ADRES
 MOV PA22,R2
 ELSE
 BR \$50102
 \$50101:
 LET R2 := #BUFPA ;18 BIT MONITOR
 MOV #BUFPA,R2
 ENDIF
 \$50102:
 LET R2 := R2 + #2
 ADD #2,R2
 \$50103:
 TST R1
 BEQ \$50104
 IF (R0) NE R2 THEN ;CONTENTS OF R0 = R2 ?
 CMP (R0),R2
 BEQ \$50105
 LET ACSR := #CSR1
 MOV #CSR1,ACSR
 LET SBADR := R2
 MOV R2,SBADR
 LET #ASADR := #0
 CLR #ASADR
 LET ASB := (R2)

```

1275 002760' 011267 175122
1276 002764'
1277 002764' 011067 175120
1278
1279
1280
1281
1282 002770' 104404 000000'
1283
1284
1285
1286
1287 002774'
1288 002774'
1289 002774'
1290 002774' 062700 000002
1291 003000'
1292 003000' 005301
1293 003002'
1294 003002' 062702 000002
1295 003006'
1296 003006' 000751
1297 003010'
1298
1299 003010'
1300 003010'
1301 003010'
1302 003010' 000207
1303
1304
1305
1306
1307
1308
1309
1310 003012'
1311 003012'
1312
1313 004012'
1314 003012' 012767 002000 175474
1315 003020'
1316 003020' 026727 175474 000001
1317 003026' 001114
1318
1319 003030'
1320 003030' 010067 175474
1321 003034'
1322 003034' 012700 000260'
1323 003040'
1324 003040' 011067 175470
1325 003044'
1326 003044' 016767 175464 175274
1327 003052' 062767 000016 175266
1328 003060'
1329 003060' 016767 175262 175244
1330 003066' 162767 000014 175236

      MOV      (R2),ASB
      MOV      (R0),AWAS

      .NLIST MC
      .LIST ME
      ;*****
      DATERS,BEGIN          :DATA ERROR!!!
      ;*****
      .LIST MC
      .NLIST ME

      ENDIF

      LET R0 := R0 + #2          ;INC TO NXT ADRS OF DATA
      ADD      #2,R0
      LET R1 := R1 - #1          ;DEC COUNTER
      DEC      R1
      LET R2 := R2 + #2          ;INC TO NXT DATA WORD
      ADD      #2,R2
      ENDDO

      BR      $50103
      $50104:

      $50077:
      $50100:
      RTS      PC

      ;*****
      ; THIS IS THE GO ROUTINE. IT WILL SET THE
      ; GO BIT, START THE LOOP COUNTER AND RETURN
      ; CONTROL TO THE MONITOR VIA THE BREAK
      ; INSTRUCTION.
      ;*****

      ROUTINE GO

      GO:

      LET LPCNTR := #2000          ;LOOP CNTR INIT VALUE
      MOV      MOV      #2000,LPCNTR
      IF SIMGHT EQ #1 THEN          ;CHK SIMULT GO BIT
      CMP      SIMGHT,#1
      BNE      $50110

      LET TMPRO := R0          ;TEMP SAVE R0
      MOV      RO,TMPRO
      LET RO := #Q22TB          ;ADRS OF TABLE
      MOV      #Q22TB,R0
      LET RY := (R0)          ;CONTENTS OF ADRS
      MOV      (R0),RY
      LET SIMGOA := RY + #16          ;LOWEST VALID VALUE IN SIMGOA
      MOV      RY,SIMGOA
      ADD      #16,SIMGOA
      LET CSR2 := SIMGOA - #14          ;LOWEST VALID CSR2
      MOV      SIMGOA,CSR2
      SUB      #14,CSR2
  
```

```

1331 003074'
1332 003074'
1333 003074'
1334 003074'
1335 003074' 016777 175426 175230
1336 003102'
1337 003102' 062767 000016 175222
1338 003110'
1339 003110' 062700 000002
1340 003114'
1341 003114'
1342 003114' 026710 175212
1343 003120' 001406
1344 003122' 005710
1345 003124' 001404
1346 003126'
1347 003126' 062767 000020 175176
1348 003134'
1349 003134' 000767
1350 003136'
1351 003136'
1352 003136' 062767 000002 175166
1353 003144'
1354 003144' 005710
1355 003146' 001352
1356
1357 003150'
1358 003150' 162767 000020 175154
1359
1360 003156'
1361 003156' 012777 000001 175162
1362 003164'
1363 003164' 106427 000140
1364 003170'
1365 003170'
1366 003170' 005767 175340
1367 003174' 001426
1368 003176'
1369 003176' 062767 000002 175330
1370 003204'
1371 003204'
1372 003204' 005767 175304
1373 003210' 001413
1374 003212' 032777 000200 175314
1375 003220' 001007
1376 003222'
1377 003222' 005367 175266
1378
1379
1380
1381 003226' 104407 000000'
1382 003232' 104407 000000'
1383
1384
1385 003236'
1386 003236' 000762

      REPEAT

      LET @CSR2 := IMPSK2          ;INTR BITS INTO CSR2
      MOV      TMPSTR,@CSR2
      LET CSR2 := CSR2 + #16          ;THIS WILL LD ALL CSR2'S
      ADD      #16,CSR2
      LET R0 := R0 + #2
      ADD      #2,R0
      WHILE CSR2 NE (R0) AND (R0) NE #0 DO
      $50111:
      CMP      CSR2,(R0)
      BEQ      $50113
      TST      (R0)
      BEQ      $50113
      LET CSR2 := CSR2 + #20          ;TRY NXT ADDRESS
      ADD      #20,CSR2
      ENDDO
      $50112:
      CMP      CSR2,(R0)
      BEQ      $50113
      TST      (R0)
      BEQ      $50113
      LET CSR2 := CSR2 + #2          ;CORRECT CSR2 ADRS
      ADD      #2,CSR2
      UNTIL (R0) EQ #0
      TST      (R0)
      BNE      $50111
      LET CSR2 := CSR2 - #20          ;RESTORE THIS TO REAL VALUE
      SUB      #20,CSR2
      LET @SIMGOA := #1          ;START ALL DEVICES
      MOV      #1,@SIMGOA
      INLINE <MIPS      #140>          ;DROP CPU INTR LVL
      MTPS      #140
      WHILE RY NE #0 DO          ;IF 0, ALL HAVE BEEN CHECKED
      $50114:
      TST      RY
      BEQ      $50115
      LET RY := RY + #2          ;GET ADRS OF CSR2
      ADD      #2,RY
      $50116:
      TST      LPCNTR
      BEQ      $50117
      BIT      #BIT07,@RY
      BNE      $50117
      LET LPCNTR := LPCNTR - #1
      DEC      LPCNTR
      .NLIST MC
      .LIST ME
      BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR....
      BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
      .LIST MC
      .NLIST ME
      ENDDO
      ;ALL DEVICES SHOULD BE DONE
      BR      $50116
  
```

```

1387 003240'
1388 003240'
1389 003240' 062700 000002
1390 003244'
1391 003244' 011067 175264
1392 003250'
1393 003250' 000747
1394 003252'
1395 003252'
1396 003252' 016700 175252
1397
1398 003256'
1399 003256' 000430
1400 003260'
1401 003260'
1402 003260' 052767 000001 175240
1403 003266'
1404 003266' 016777 175234 175036
1405
1406 003274'
1407 003274'
1408 003274' 005767 175214
1409 003300' 001417
1410 003302'
1411 003302' 032777 000209 175022
1412 003310' 001403
1413 003312'
1414 003312' 005067 175176
1415 003316'
1416 003316' 000403
1417 003320'
1418 003320'
1419 003320' 162767 000002 175166
1420 003326'
1421 003326'
1422
1423
1424
1425 003326' 104407 000000'
1426 003332' 104407 000000'
1427
1428
1429
1430 003336'
1431 003336' 000756
1432 003340'
1433 003340'
1434 003340'
1435
1436 003340'
1437 003340' 004767 000006
1438
1439 003344'
1440 003344' 004767 000150
1441
1442 003350'

      LET RO := RO + #2
      LET RY := (RO)
      ENDDO
      LET RO := TMPRO
      ELSE
      LET TMPSR2 := [MPSR2 SET.RY #BIT00
      LET [CSR2 := [MPSR2
      WHILE LPCNTR NE #0 DO
      IF #BIT07 SETIN [CSR2 THEN
      LET LPCNTR := #0
      ELSE
      LET LPCNTR := LPCNTR - #2
      ENDDO
      .NLIST PC
      .LIST ME
      BREAKS,BEGIN
      BREAKS,BEGIN
      .LIST MC
      .NLIST ME
      ENDDO
      ENDDIF
      CALL CHKINT
      CALL CHKERR
      ENDRIN

      $S0117:
      $BY NOW
      ADD #2,RO
      MOV (RO),RY
      BR $S0114
      $S0115:
      $STORE CONTENTS OF RO
      MOV TMPRO,RO
      BR $S0120
      $S0110:
      $GET GO BIT READY
      BIS #BIT00,[CSR2
      $GO, THIS IS FOR 1 DEVICE AT A TIME
      MOV [CSR2,[CSR2
      $S0121:
      $TST LPCNTR
      $BEQ $S0122
      $BIT #BIT07,[CSR2
      $BEQ $S0123
      $CLW LPCNTR
      $OTHERWISE COUNT DOWN
      BR $S0124
      $S0123:
      $SUB #2,LPCNTR
      $S0124:
  
```

```

1443 003350'
1444 003350'
1445 003350' 000207
1446
1447
1448
1449
1450
1451
1452
1453
1454 003352'
1455 003352'
1456
1457
1458
1459
1460
1461
1462
1463 003352'
1464 003352' 036767 175130 175142
1465 003360' 001002
1466 003362'
1467 003362' 004767 000046
1468 003366'
1469 003366'
1470
1471 003366'
1472 003366' 026727 175126 000001
1473 003374' 001007
1474 003376'
1475 003376' 026767 175102 175120
1476 003404' 001403
1477
1478
1479 003406' 104403 000000' 003760'
1480
1481
1482 003414'
1483 003414'
1484 003414'
1485 003414'
1486 003414'
1487 003414' 005067 175102
1488
1489 003420'
1490 003420'
1491 003420'
1492 003420' 000207
1493
1494
1495
1496
1497
1498

      $S0106:
      $S0107:
      RTS PC

      ;*****
      ;
      ; THIS ROUTINE WILL CHECK THAT THE PROPER
      ; INTERRUPTS HAVE OCCURRED.
      ;
      ;*****
      ROUTINE CHKINT
      CHKINT:
      $BIT/S IN MASK ARE DEVICES THAT
      $ARE EXPECTED TO INTERRUPT. SVINT IS
      $THE DEVICES THAT ACTUALLY DID AN
      $INTERRUPT. THEREFORE IF A BIT
      $IS SET IN MASK AND NOT IN SVINT, THAT
      $DEVICE DID NOT INTERRUPT.
      IF MASK NOTSETIN SVINT THEN
      $BIT MASK,SVINT
      $BNE $S0127
      $JSR PC,DRPDEV
      ENDDIF
      $DROP THIS DEVICE
      $S0127:
      IF SIMGRT EQ #1 THEN
      $ALL DEVICES WERE SET OFF
      $CMP SIMGRT,#1
      $BNE $S0130
      IF INTRP NE INTCNT THEN
      $DO INTR COUNTS AGREE
      $CMP INTRP,INTCNT
      $BEQ $S0131
      .NLIST MC
      .LIST ME
      MSGS,BEGIN,TEXT3
      .LIST MC
      .NLIST ME
      ENDDIF
      ENDDIF
      LET SVINT := #0
      $CLR SAVE INTR
      ENDRIN
      $S0131:
      $S0130:
      CLR SVINT
      $S0125:
      $S0126:
      RTS PC

      ;*****
      ;
      ; THIS IS THE INTERRUPT SERVICE ROUTINE
      ;
      ;*****
  
```


Address	Disassembly	Hex
1500	INTSRV:	003422
1501		003422
1502	LET INTCNT := INTCNT + #1	003422
1503		003422
1504		003426
1505	INLNE <NOP>	003426
1506		003426
1507	INLNE <NOP>	003430
1508		003430
1509	INLNE <INTI>	003432
1510		003432
1511	*****	
1512	THIS ROUTINE WILL DROP A DEVICE AND WILL THEN	
1513	BRANCH TO START AND CONTINUE.	
1514	*****	
1515		
1516		003434
1517	OPRDEV:	
1518		003434
1519	LET SVADR := FIRADR	003434
1520		003434
1521	LET MORE := #BIT0	003442
1522		003442
1523		003450
1524	WHILE MORE NOTSET IN MASK DO	
1525		003450
1526		003450
1527	INLNE <ASL MORE>	003456
1528		003460
1529	LET SVADR := SVADR + #20	003464
1530		003464
1531	ENDDO	003472
1532		003472
1533		003472
1534		003474
1535		
1536		
1537		
1538		003474
1539		104403
1540		000000
1541		003764
1542		
1543		003502
1544		003510
1545		003510
1546		003514
1547		003514
1548		
1549		
1550		
1551		
1552		
1553		
1554		

[illegible]

```

1611 .NLIST MC
1612 .LIST ME
1613 ;*****
1614 003b40' 104405 000009' 000330' HDRS,HEGIN,TAHLE1 ;DUMP REGISTER CONTENTS
1615 ;*****
1616 .LIST MC
1617 .NLIST ME
1618
1619 .ENDIF
1620 003b46'
1621 003b46'
1622 003b46'
1623 003b46'
1624 003b46' 010767 174656 174472 LET SIMGOA := TMPRO ;RESTORE ADKS FOR SIMGO
1625
1626 003b54'
1627 003b54'
1628 003b54'
1629 003b54' 000207
1630
1631 ;*****
1632 ; THIS ROUTINE WILL SETUP THE ERROR TABLE TO
1633 ; PRINT OUT THE CONTENTS OF THE DEVICE REGISTERS
1634 ;*****
1635
1636 003b5b'
1637
1638 003b56'
1639 003b56' 010067 174450
1640 003b62'
1641 003b62' 010067 174442
1642 003b66' 162767 000002 174434
1643 003b74'
1644 003b74' 010067 174434
1645 003700' 062767 000004 174426
1646 003706'
1647 003706' 010067 174424
1648 003712' 062767 000006 174416
1649 003720'
1650 003720' 010067 174414
1651 003724' 062767 000010 174406
1652 003732'
1653 003732' 010067 174404
1654 003736' 062767 000012 174376
1655 003744'
1656 003744' 010067 174374
1657 003750' 062767 000014 174366
1658
1659 003756'
1660 003756' 000207
1661
1662
1663 ;*****
1664 ;PUT SOME MESSAGES HERE
1665 ;
1666 003760' 004176'
1667
1668
1669
1670
1671
1672
1673
1674
1675
1676
1677
1678
1679
1680
1681
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1700
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1704
1705
1706
1707

```

```

1667 003762' 177777
1668 003764' 004102'
1669 003766' 177777
1670 003770' 004024'
1671 003772' 177777
1672 003774' 004000'
1673 003776' 177777
1674 004000' 047045 020117 042504
1675 004006' 044526 042503 020123
1676 004014' 047524 051040 047125
1677 004022' 000045
1678 004024' 042045 053105 041511
1679 004032' 020105 042101 051522
1680 004040' 044440 020123 047524
1681 004046' 020117 044510 044107
1682 004054' 046440 054101 053040
1683 004062' 046101 042525 044440
1684 004070' 020123 033461 030460
1685 004076' 030066 000045
1686 004102' 040445 042040 053105
1687 004110' 041511 026105 031121
1688 004116' 041062 026105 040510
1689 004124' 020123 042502 047105
1690 004132' 042040 047522 050120
1691 004140' 042105 042040 042525
1692 004146' 052040 020117 047101
1693 004154' 044440 052116 051105
1694 004162' 052522 052120 042440
1695 004170' 051122 051117 000045
1696 004176' 044445 041516 051117
1697 004204' 042522 052103 047040
1698 004212' 046525 042502 020122
1699 004220' 043117 044440 052115
1700 004226' 051105 052522 052120
1701 004234' 022523 000
1702
1703 004240'
1704
1705
1706 004240' 000256
1707 000001

```


\$STD = 000000	234#	516#	610#	658#	661#	728#	807#	1437#	1440#					
\$STOR = 000000	516#	610#	658#	661#	728#	807#	1437#	1440#						
\$STUTL= 000000	516#	517	610#	611	658#	659	661#	662	728#	729	807#	808	1437#	
	1438	1440#	1441											
\$SSIAG= 050000	234#													
\$50000 000562R	389#	403												
\$50001 000620R	391	393	404#											
\$50002 000676R	428	436#												
\$50003 000700R	442#	454												
\$50004 000716R	444	455#												
\$50005 000714R	448	452#												
\$50006 000732R	482#	490												
\$50007 000744R	484	491#												
\$50010 000770R	506#	541												
\$50011 001054R	508	542#												
\$50012 001014R	514	520#												
\$50013 001052R	519	539#												
\$50014 001052R	527	537#												
\$50015 001144R	594	598#												
\$50016 001162R	618#	758												
\$50017 001476R	620	759#												
\$50020 001212R	624	629#												
\$50021 001220R	628	633#												
\$50022 001260R	650	654#												
\$50023 001310R	673	678#												
\$50024 001316R	677	682#												
\$50025 001362R	687	708#												
\$50026 001346R	692	697#												
\$50027 001350R	696	701#												
\$50030 001370R	707	712#												
\$50031 001416R	722	726#												
\$50032 001464R	732	750#												
\$50033 001452R	735	741#												
\$50034 001462R	740	747#												
\$50035 001474R	749	756#												
\$50036 001552R	797#	842												
\$50037 001664R	799	843#												
\$50040 001634R	819	828#												
\$50041 001652R	827	836#												
\$50042 001720R	860	864#												
\$50043 002170R	975#													
\$50044 002170R	976#													
\$50045 002116R	952#	960												
\$50046 002136R	954	961#												
\$50047 002224R	997	999#												
\$50050 002236R	1004	1007#												
\$50051 002246R	1003	1012#												
\$50052 002256R	1002	1017#												
\$50053 002266R	1001	1023#												
\$50054 002266R	1000	1022#												
\$50055 002276R	993	995	1028#											
\$50056 002302R	1011	1016	1021	1027	1030#									
\$50057 002412R	1104#													
\$50060 002412R	1105#													
\$50061 002352R	1052	1072#												
\$50062 002326R	1058#	1066												

\$50063 002344R	1060	1067#												
\$50064 002360R	1071	1076#												
\$50065 002374R	1089#	1099												
\$50066 002412R	1091	1100#												
\$50067 002500R	1119	1142#												
\$50070 002542R	1141	1161#												
\$50071 002524R	1147	1151#												
\$50072 002542R	1154	1158#												
\$50073 002630R	1177	1201#												
\$50074 002672R	1200	1220#												
\$50075 002654R	1206	1210#												
\$50076 002672R	1213	1217#												
\$50077 003010R	1300#													
\$50100 003010R	1301#													
\$50101 002722R	1248	1253#												
\$50102 002726R	1252	1257#												
\$50103 002732R	1262#	1296												
\$50104 003010R	1264	1297#												
\$50105 002774R	1267	1288#												
\$50106 003350R	1443#													
\$50107 003350R	1444#													
\$50110 003260R	1317	1400#												
\$50111 003074R	1333#	1355												
\$50112 003114R	1341#	1349												
\$50113 003136R	1343	1345	1350#											
\$50114 003170R	1365#	1393												
\$50115 003252R	1367	1394#												
\$50116 003204R	1371#	1386												
\$50117 003240R	1373	1375	1387#											
\$50120 003340R	1399	1434#												
\$50121 003274R	1407#	1431												
\$50122 003340R	1409	1432#												
\$50123 003320R	1412	1417#												
\$50124 003326R	1416	1421#												
\$50125 003420R	1490#													
\$50126 003420R	1491#													
\$50127 003366R	1465	1469#												
\$50130 003414R	1473	1485#												
\$50131 003414R	1476	1483#												
\$50132 003450R	1525#	1533												
\$50133 003474R	1527	1534#												
\$50134 003554R	1627#													
\$50135 003654R	1628#													
\$50136 003630R	1572	1606#												
\$50137 003566R	1576#	1602												
\$50140 003626R	1578	1603#												
\$50141 003620R	1585	1598#												
\$50142 003646R	1605	1622#												
\$50143 003646R	1609	1620#												
\$50144 004774R	255#	256#	391	392	393	394	428	429	444	445	448	449	484	
	485	508	509	514	515	527	528	594	595	620	621	624	625	
	650	651	673	674	687	688	692	693	722	723	732	733	735	
	736	799	800	819	820	860	861	954	955	993	994	995	996	
	1052	1053	1060	1061	1091	1092	1119	1120	1147	1148	1154	1155	1177	
	1178	1206	1207	1213	1214	1248	1249	1264	1265	1267	1268	1317	1318	
	1343	1344	1345	1346	1355	1356	1367	1368	1373	1374	1375	1376	1409	

1410	1412	1413	1465	1466	1473	1474	1476	1477	1527	1528	1572	1573
1578	1579	1585	1586	1609	1610	1703	1706					

BLOC	234#																
BEGIN	234#																
BKMOD	1#																
BREAK	1#	1381	1425														
BTDD	1#																
CALL	234#	515	609	657	660	727	806	1436	1439								
CASE	234#	1006	1010	1015													
CKDATA	1#																
DATACK	1#																
DATERP	1#	1261															
DEALLO	234#	975#	1104#	1300#	1443#	1490#	1627#										
DECLAR	234#																
DECRA	234#																
DCRAB	234#																
DECRU	234#																
DECRUB	234#																
DEFaul	234#	1020															
DFSEVN	1#	22H															
DSEVNT	1#	22H															
ELSE	234#	516	627	676	695	706	739	748	826	1070	1140	1199	1251	1398	1415		
END	1#	234#															
ENDDec	234#																
ENDOo	234#	402	453	489	540	757	841	959	1065	1098	1295	1348	1385	1392	1430		
ENDIF	153Z 234# 863	1601 435 1075	451 536 1150	538 597 1157	538 597 1150	597 632 1209	632 653 1216	653 681 1219	681 700 1256	700 711 1287	711 725 1420	725 746 1433	746 755 1468	755 835 1482	835 835 1484		
ENDINC	234#																
ENDIT	1#	673															
ENDLoo	234#																
ENDMUD	1#	432	532														
ENDROO	234#																
ENDRTI	234#																
ENDRTN	234#	974	1103	1299	1442	1489	1626										
ENDSEL	234#	1026															
EQUATS	1#	22H															
EXIF	234#																
EXIFA	234#																
EXIT	1#																
GETPA	1#	556															
GWBUFF	1#	573															
HDRBR	1#	1541	1613														
IP	234#	426	525	592	622	648	671	685	690	720	730	733	817	858	1050		
	1117	1145	1152	1175	1204	1211	1246	1265	1315	1410	1463	1471	1474	15			

1024#	1046#	1051#	1053#	1058#	1059#	1061#	1066#	1073#	1089#	1090#	1092#	1099#	1118#	1120#
1143#	1146#	1148#	1153#	1155#	1176#	1178#	1202#	1205#	1207#	1212#	1214#	1235#	1247#	1249#
1254#	1262#	1263#	1265#	1266#	1268#	1296#	1312#	1316#	1318#	1333#	1334#	1341#	1342#	1346#
1349#	1365#	1366#	1368#	1371#	1372#	1376#	1386#	1393#	1401#	1407#	1408#	1410#	1411#	1413#
1418#	1431#	1456#	1464#	1466#	1472#	1474#	1475#	1477#	1525#	1526#	1528#	1533#	1558#	1571#
1573#	1576#	1577#	1579#	1584#	1586#	1602#	1607#	1608#	1610#					
SSSETT	991#	1007#	1008#	1011#	1013#	1016#	1018#	1022#	1023#	1024#	1027#			

. ARS. 000000 000.
004774 001

ERRORS DETECTED: 0
% DEFAULT GLOBALS GENERATED: 1

CXBTCA,CXBTCA.PRT/SUL/CRF=SPMACJ/ML,DDXCOM,P11,CXBTCA.P11,CXBTCA.END
RUN-TIME: 48 41 4 SECONDS
RUN-TIME RATIO: 132/94=1.4
CORE USED: 24K (48 PAGES)

.ENABL LC

.REM 8

IDENTIFICATION

PRODUCT CODE: AC-S914C-MC
 PRODUCT NAME: CXDUBCO UDA DEC/X MOD
 PRODUCT DATE: 01-JUL-82
 MAINTAINER: DIAGNOSTIC ENGINEERING
 AUTHOR: MATT TEDONE

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1.0 ABSTRACT

THE EXERCISER WILL BE SIMILAR TO THAT OF OTHER DISK SUBSYSTEM EXERCISERS. WRITES WILL BE PERFORMED TO THE DISKS FOLLOWED BY READ AND COMPARE OF THE DATA READ. THE UDA WILL DO ALL ERROR RETRYING. ERRORS WILL BE REPORTED ON THE CONSOLE TTY.

ALL DESIRED DISK DRIVES ON THE UDA WILL BE EXERCISED SIMULTANEOUSLY. IF DISK ACCESSING IS NOT REQUIRED, THEN DATA WRITTEN WILL GO ONLY AS FAR AS THE CONTROLLER'S RAM MEMORY.

THIS DEC/X11 MODULE WILL BE RELEASED UNDER THE NAME CXDUBBO BEFORE FCS OF THE UDA-50 AND THE K&B0. A DEC/X11-PLUS MODULE UNDER THE NAME CSDUBBO WILL BE RELEASED AFTER BEING MODIFIED BY THE DIAGNOSTIC SYSTEMS ENGINEERING GROUP TO RUN UNDER THE DEC/X11-PLUS MONITOR.

IF THE RESULTS OF THE EXERCISER REQUIRES MORE INFORMATION, TWO OTHER PDP-11 DIAGNOSTIC PROGRAMS ARE AVAILABLE. THEY ARE:

CZUDCA0 - UDA AND DISK DIAGNOSTIC
 CZUDEA0 - UDA DISK FORMATTER.

2.0 REQUIREMENTS

HARDWARE FOR ALL CASES:
 ONE DEC/X11 MODULE CONFIGURED FOR ONE UDA CONTROLLER.

HARDWARE FOR DISK ACCESSING:
 ONE UDA CONTROLLER WITH AT LEAST ONE DRIVE IS THE MINIMUM AMOUNT. OR ONE UDA CONTROLLER WITH FOUR DRIVES IS THE MAXIMUM AMOUNT.

HARDWARE FOR NO DISK ACCESSING:
 ONE UDA CONTROLLER IS THE ONLY REQUIREMENT.

STORAGE: DUBB REQUIRES
 DECIMAL WORDS -- 4096 MAX

3.0 START-UP

ON THE INITIAL START, THE PROGRAM WILL CLEAR BIT1 OF 'SR1' AND TYPE THE FOLLOWING MESSAGES.

```
DUBBU PA:0060162      APC: 000674      PASS #00000
'IF YOU WISH TO DESTROY CUSTOMER DATA, SET BIT1 (NOT BIT0)
IN SWITCH REGISTER 1(SR1) OF DUBB? EQUAL TO 1.'
```

```
DUBBU PA:0060210      APC: 000722      PASS #00000
'! OPERATING WITH NO DISK ACCESSING !'
```

THIS WILL OCCUR REGARDLESS OF THE CONDITION OF SR1 (BIT1) AT CONFIGURE TIME.

IF THE OPERATOR WISHES TO EXERCISE THE UNIT, SR1 (BIT1) MUST BE MODIFIED AT LOCATION 16 OF CADUBBU MODULE (SEE SECTION 9). THIS CAN BE ACCOMPLISHED BY USING THE 'MOD' COMMAND SUPPLIED BY THE DEC/11 RUN TIME SYSTEM. UNLESS THE PROGRAM IS RELOADED OR THE OPERATOR MODIFIES THE LOCATION AGAIN, THE CONTENTS OF SR1 WILL REMAIN THE SAME ON ALL SUBSEQUENT STARTS.

ON ALL SUBSEQUENT STARTS, THE CONDITION OF SR1 (BIT1) WILL TYPE TO TERMINAL IN THE FOLLOWING MANNER.

IF BIT1 OF SR1 IS EQUAL TO 0 (ZERO), THE FOLLOWING WARNING WILL BE TYPED.

```
DUBBU PA:0060210      APC: 000722      PASS #00000
'! OPERATING WITH NO DISK ACCESSING !'
```

IF BIT1 OF SR1 IS EQUAL TO 1 (ONE), THE FOLLOWING WARNING WILL BE TYPED.

```
DUBBU PA:0060210      APC: 000722      PASS #00000
'! CUSTOMER DATA WILL BE DESTROYED !'
```

4.0 PASS DEFINITION

WHEN THIS DEC/X11 MODULE RUNS IN DISKLESS MODE, ITS DATA RATE EXCEEDS ALL OTHER DEVICES. THIS MAY CAUSE ERRONEOUS DATA LATES FROM OTHER DEVICES.

4.0 PASS DEFINITION

ONE PASS OF THE DUBB MODULE CONSISTS OF 512 ITERATIONS OF THE BASIC TEST SEQUENCE (WRITE, READ, DATA-CHECK). THE TEST SEQUENCE WRITES A USER DEFINED NUMBER OF WORDS (DEFAULT IS 256) WORDS, READS 256 WORDS, AND DATA-COMPARE SAME.

5.0 EXECUTION TIME

THE DEFAULT EXECUTION TIME OF ONE PASS OF DUBB RUNNING ALONE ON A PDP-11/44 UNDER SEQUENTIAL DISK ACCESSING MODE WILL BE APPROXIMATELY 20 SECONDS. UNDER RANDOM ACCESSING MODE, THE TIME IS 40 SECONDS. FOR NO DISK ACCESSING, THE TIME IS FIVE SECONDS.

6.0 CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

```
DEVADR: 172150, VECIOR: 154, BR1: 5, DEVCNT: 1, SR1: 0, SR2: 0
```

REQUIRED PARAMETERS:

ADDITIONAL UDA MODULE(S) CONFIGURED MUST HAVE A DIFFERENT UNIBUS ADDRESS(ES) AND VECTOR(S).

7.0 DEVICE/OPTION SETUP

FOR DISK MODE, MAKE CERTAIN THAT ALL UNITS ARE POWERED UP, WRITE ENABLED, CONNECTED TO A UDA VIA THE S01 AND READY.

FOR DISKLESS MODE, MAKE CERTAIN THE UDA IS POWERED UP.

8.0 MODULE OPERATION

TEST SEQUENCE DISK MODE:

A. SETUP DEVICE REGISTER ADDRESSES AND MODULE VARIABLES.
B. SET CONTROLLER CHARACTERISTIC.
C. RESET ALL UNITS ON-LINE AND DROP ALL THAT ARE NOT.
D. GET A UNIT ADDRESS.
E. GET A DISK ADDRESS AND A FRESH BLOCK OF DATA.
F. DO A WRITE -- IF ERRORS, REPORT.
G. DO A READ -- IF ERRORS, REPORT.
H. DO A DATA-CHECK -- IF ERRORS, REPORT AND CONTINUE.
I. MAKE UNIT AVAILABLE.
J. WAIT FOR AVAILABLE ATTENTION MESSAGE.
K. IF END OF PASS, REPORT AND GO TO D.
IF END OF TESTING UNIT, GO TO C; ELSE GO TO D.

BLOCKS DETERMINED DEFECTIVE WON'T BE REPLACED BY THE EXERCISER DURING THIS SEQUENCE. THE EXERCISER MAKES FULL USE OF THE UDA WHICH DOES REVECTURING ON ITS OWN.

TEST SEQUENCE DISKLESS MODE:

A. GET A FRESH BLOCK OF DATA.
B. DO A WRITE TO UDA RAM BUFFER -- IF ERRORS, REPORT.
C. DO A READ FROM UDA RAM BUFFER -- IF ERRORS, REPORT.
D. DO A DATA-CHECK -- IF ERRORS, REPORT AND CONTINUE.
E. IF END OF PASS, REPORT.
F. GO TO A.

9.0 OPERATION OPTIONS

ONE OR MORE SOFTWARE SWITCH REGISTERS CAN BE USED BY THE MODULE PROGRAM GENERAL PURPOSE SWITCHES. THESE WORDS ARE USED TO DEFINE OR SPECIFY A UNIQUE DEVICE OPTION OR TO POINT TO A SPECIFIC ROUTINE IN THE MODULE. ANY OPTION MUST BE SPECIFIED BY THE OPERATOR BEFORE THE MODULE IS RUN. SWITCH REGISTER 1 HAS THE FOLLOWING CHARACTERISTICS.

SRI BIT 1 SET (1): ALLOW DISK TRANSFERS.
<<< NOTE >>> IF SET, CUSTOMER DATA WILL BE DESTROYED!
RESET (0): NO DISK TRANSFERS.

SRI BIT 2 SET (1): DO NOT REPORT ERRORS AS THEY OCCUR.
RESET (0): REPORT ERRORS AS THEY OCCUR.

SRI BIT 3 SET (1): DO NOT PRINT ERROR SUMMARY AT END OF PASS.
RESET (0): PRINT ERROR SUMMARY AT END OF PASS.

SRI BIT 9 SET (1): RUN DUAL PORT MODE (ONLY VALID IF SRI BIT 1 IS SET)
RESET (0): DO NOT RUN DUAL PORT MODE

SRI BIT 10 SET (1): SELECT RANDOM BLOCK ADDRESSING.(ONLY VALID IF SRI BIT 1 IS SET)

RESET (0): SELECT SEQUENTIAL BLOCK ADDRESSING.

SRI BIT 11 SET (1): BYPASS DATA COMPARE.
RESET (0): DO DATA COMPARE.

SWITCH REGISTER 2 HAS THE FOLLOWING CHARACTERISTICS.

SR2 BITS 0 TO 5: BURST RATE.

A BURST RATE TO SPEED UP NPK TRANSFERS BY THE UDA CAN BE USED. THIS VALUE IS 0 BITS MAXIMUM AND SET UP IN SR2 AT CONFIGURE TIME.

<<< NOTE >>>
THE DVID1 MASK REFLECTS THE NUMBER OF UNITS CHOSEN FOR TESTING AND WHICH UNITS ON THE SYSTEM ARE TO BE TESTED. EXAMPLE: IF DVID1 CONTAINS A 1, ONLY THE FIRST UNIT FOUND ON THE SYSTEM WILL BE TESTED. A UNIT'S ORDER ON THE SYSTEM IS JUDGED BY ITS UNIT NUMBER. THE LOWEST UNIT NUMBER ZERO (0). UNIT 0 WOULD BE THE FIRST TESTED ON THE SYSTEM.

IF DVID1 CONTAINS A 10, THE FOURTH UNIT ON THE SYSTEM WILL BE TESTED. IF THE FIRST TWO UNITS ARE CHOSEN, DVID1 IS 3. FOUR CONSECUTIVE UNITS MEANS DVID1 IS 17. SIX UNITS, DVID1 IS 77.

IF THERE IS NOT A UNIT CORRESPONDING TO THE DVID1 BIT SETTING, THE BIT SET IN DVID1 GETS CLEARED. THE EXERCISER WILL READJUST THE MASK AND DROP THE NONEXISTENT UNITS IF MORE UNITS ARE CHOSEN THAN ACTUALLY ARE PRESENT. THE MODULE IS DROPPED IF ALL DVID1 BITS ARE CLEARED.

IF THE NUMBER OF UNITS CHOSEN IS LESS THEN THE ACTUAL NUMBER OF UNITS PRESENT, ONLY THE DESIRED UNITS WILL BE USED DURING THE EXERCISE.

<<< ANOTHER NOTE >>>
MAKE SURE ALL SUBUNITEL DRIVES ARE ACCOUNTED FOR. DESTROYING CUSTOMER DATA IS NOT DESIRABLE.

<<< ONE MORE NOTE >>>
IF SRI BIT 3 IS RESET, A SUMMARY STATUS IS PRINTED EVERY 15 PASSES. THIS STATUS IS FORMATTED AS FOLLOWS:

DUBB0 PA: 00060470 ACP: 001210 PASS #00000
SOFT ERROR COUNT #00000 *** HARD ERRO COUNT #00000
CHECK DATA ERROR COUNT #00000

10.0 PRINTOUTS

A. MJSI PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN THE DEC/X11 DOCUMENT.

B. NON-STANDARD PRINTOUTS INCLUDE ERROR MESSAGES WHICH DUMP THE FOLLOWING:

- 1) SUMMARY STATUS
- 2) FLAGS AND ENDCODE
- 3) UNIT NUMBER
- 4) BYTE COUNT
- 5) HI 16-BIT LBN VALUE
- 6) LO 16-BIT LBN VALUE
- 7) EXTENDED ADDRESS
- 8) PHYSICAL ADDRESS

ALL VALUES EXCEPT FOR PASS, RUNTIME AND ERRCNT ARE PRINTED IN OCTAL. PASS, RUNTIME AND ERRCNT ARE PRINTED IN DECIMAL. EXAMPLE:

DUBB0 PA: 0006411b APC: 004b30 PASS: 00000 ERRCNT: 00001
CSMA: 172150 CSRC: 000000 ASTAT: 00000b ERRTP: 00000b
RUNTIME: 000:00:22

DUBB0 PA: 00064052 APC: 0045b4 PASS: 00000

STATUS ENDCOD UNITNO BYTECO HI LBN LO LBN EXTADR PHYADR
000006 000242 000005 000000 000003 11b321 000001 062100

STATUS - RESPONSE OF THE COMMAND SENT TO THE UDA. THIS IS CONTAINED IN THE LAST FIVE BITS OF THE WORD. HERE IS A LIST OF STATUS CODES.

- 0 - SUCCESS
- 1 - INVALID COMMAND
- 2 - COMMAND ABORTED
- 3 - UNIT OFFLINE
- 4 - UNIT AVAILABLE
- 5 - MEDIA ERROR
- 6 - WRITE PROTECTED
- 7 - COMPARE ERROR
- 10 - DATA ERROR
- 11 - HOST BUFFER ACCESS ERROR
- 12 - CONTROLLER ERROR
- 13 - DRIVE ERROR

ENDCOD - ENDING CODE OF THE COMMAND SENT. THIS SHOWS WHAT COMMAND WAS SENT TO THE UDA. HERE IS A LIST OF ALL POSSIBLE ENDCODES THIS MODULE USES.

- 100 - AVAILABLE ATTENTION MESSAGE (NOT A COMMAND BUT A MESSAGE SENT TO THE HOST FROM THE UDA)
- 200 - INVALID COMMAND
- 203 - GET UNIT STATUS
- 204 - SET CONTROLLER CHARACTERISTICS
- 210 - AVAILABLE

- 211 - ONLINE
- 230 - MAINTENANCE READ
- 231 - MAINTENANCE WRITE
- 241 - READ
- 242 - WRITE

UNITNO - UNIT NUMBER OF THE DRIVE THAT IS BEING ACCESSED. THIS IS NOT RELEVANT IF THE USER IS RUNNING DISKLESS MODE.

BYTECO - SIZE OF THE BUFFER IN BYTES.

HI LBN - HIGH LOGICAL BLOCK NUMBER (UPPER 16 BITS) WHICH TELLS THE USER WHERE ON THE DISK THE DATA IS GOING. THIS IS ONLY VALID FOR DISK MODE.

LO LBN - LOW LOGICAL BLOCK NUMBER (LOWER 16 BITS).

EXTADR - EXTENDED ADDRESS OF THE READ/WRITE BUFFER.

PHYADR - PHYSICAL ADDRESS OF THE READ/WRITE BUFFER.

C. IF THE UDA FAILED TO PASS ITS INTERNAL DIAGNOSTIC, ONE OF THE FOLLOWING MESSAGES WILL BE PRINTED.

IF THE DIAGNOSTIC FOUND A FAULT:

DUBB0 PA: 000620b2 APC: 0025b4 PASS: 00000
UDA INIT ERROR, FOUND BY DIAGNOSTIC
UDASA = XXXXXX IN STEP YYYY
ADDR = ZZZZZZ

IF A STEP BIT WAS NOT SET AS EXPECTED DURING THE INITIALIZATION SEQUENCE OF THE UDA:

DUBB0 PA: 00062152 APC: 0026b4 PASS: 00000
UDA INIT ERROR, STEP NOT SET
UDASA = XXXXXX IN STEP YYYY
ADDR = ZZZZZZ

IF DATA PASSED BACK FROM THE UDA WAS NOT EQUAL TO THE EXPECTED VALUE:

DUBB0 PA: 00062252 APC: 0027b4 PASS: 00000
UDA INIT ERROR, EXPECTED DATA WAS INCORRECT
UDASA = XXXXXX IN STEP YYYY
ADDR = ZZZZZZ

WHERE XXXXXX CAN HAVE ANY OF THE FOLLOWING VALUES AND MEANINGS:

- 104000 - FATAL SEQUENCER ERROR
- 104040 - D PROCESSOR ALU ERROR
- 104041 - D PROC ROM PARITY ERROR
- 105102 - D PROC WITH NO BOARD #2 OR RAM PARITY ERROR
- 105105 - D PROC RAM BUFFER ERROR
- 105152 - D PROC SDI ERROR
- 105153 - D PROC WRITE MODE WRAP SERDES ERROR

105154 - D PROC READ MODE SERVES, RSGEN, AND ECC ERROR
106040 - U PROC RDU ERROR
106041 - U PROC CONTROL REGISTER ERROR
106042 - U PROC DFAIL/RUN PARITY ERROR/BOARD #1 TEST COUNT IS WRONG
106047 - U PROC CONSTANT ROM ERROR WITH D PROC RUNNING SDI TEST
106055 - UNEXPECTED/TRY AGAIN FOUND, ABORTED DIAGNOSTIC
106071 - U PROC ROM ERROR
106072 - U PROC ROM PARITY ERROR
106200 - STEP 1 DATA ERROR (MSB NOT SET)
107103 - U PROC RAM PARITY ERROR
107107 - U PROC RAM BUFFER ERROR
107115 - BOARD #2 TEST COUNT WAS WRONG
112300 - STEP 2 ERROR
122240 - NVR ERROR
122300 - STEP 3 ERROR
142300 - STEP 4 ERROR

WHERE YYYYY IS THE STEP IN WHICH THE ERROR WAS FOUND.

WHERE ZZZZZZ IS THE ADDRESS OF THE UDA.

IF THE MAXIMUM NUMBER OF RETRIES HAS BEEN EXCEEDED, THE FOLLOWING MESSAGE WILL BE PRINTED.

DUBBO PA: 00061414 APC:002126 PASS #00000

RETRY COUNT EXCEEDED, ABORT

THIS MEANS THE UDA DID NOT SUCCESSFULLY COMPLETE THE INITIALIZATION IN FOUR PASSES. THE MODULE IS THEN DROPPED.

D. IF THE UDA DID NOT SUCCESSFULLY CLEAR THE RING BUFFER IN THE HOST AREA, THE FOLLOWING MESSAGE WILL BE PRINTED.

DUBBO PA: 00061414 APC:002126 PASS #00000

RING AREA NOT CLEARED

THIS IS A FATAL ERROR. IT MEANS THAT THE UDA DID NOT ACCESS HOST MEMORY THAT THE UDA WOULD USE TO COMMUNICATE WITH THE HOST. THE MODULE IS THEN DROPPED.

E. IF THE UDASA DISPLAYS A NON-ZERO VALUE AFTER THE INITIALIZATION SEQUENCE IS DONE, THE FOLLOWING MESSAGE WILL BE PRINTED.

DUBBO PA: 00064252 APC: 004764 PASS: 00000

UDASA IS NOT ZERO, = XXXXXX

UDA IS GOING THROUGH INITIALIZATION

WHERE XXXXXX CAN HAVE THE FOLLOWING VALUES AND MEANINGS.

004400 - UDA HAS BEEN INITI BY EITHER A BUS INIT OR BY WRITING INTO THE UDALP.

100001 - UNIBUS ENVELOPE/PACKET READ ERROR (PARITY OR TIMEOUT)

100002 - UNIBUS ENVELOPE/PACKET WRITE ERROR (PARITY OR TIMEOUT)
100003 - UDA ROM AND RAM PARITY ERROR
100004 - UDA RAM PARITY ERROR
100005 - UDA ROM PARITY ERROR
100006 - UNIBUS RING READ ERROR
100007 - UNIBUS RING WRITE ERROR
100010 - UNIBUS INTERRUPT MASK FAILURE
100011 - HOST ACCESS TIMEOUT ERROR
100012 - HOST EXCEEDED CREDIT LIMIT
100013 - UDA SDI HARDWARE FATAL ERROR
100014 - DM AFC FATAL ERROR
100015 - HARDWARE TIMEOUT OF INSTRUCTION LOOP
100016 - INVALID VIRTUAL CIRCUIT IDENTIFIER
100017 - INTERRUPT WRITE ERROR ON UNIBUS

E. IF A DRIVE IS DROPPED BY THE EXERCISER, ONE OF THE FOLLOWING MESSAGES WILL BE PRINTED.

IF THE DRIVE HAD AN ERROR IT COULD NOT HANDLE PROPERLY AFTER AN ITERATION, THE FOLLOWING MESSAGE WILL BE PRINTED:

DUBBO PA: 00063012 APC: 003524 PASS #00000

DRIVE 00000 DROPPED.

DEVICE ID BIT = 000001

ERRORS CAUSED DRIVE TO BE DROPPED

IF THE DRIVE WAS NOT FOUND BY THE EXERCISER, THE FOLLOWING MESSAGE WILL BE PRINTED:

DUBBO PA: 00063012 APC: 003524 PASS #00000

DRIVE 00000 DROPPED.

DEVICE ID BIT = 000001

UNIT WAS NOT FOUND BY THE EXERCISER

IF THERE WERE MORE DEVICE COUNT BITS SET THAN THE ACTUAL NUMBER OF DRIVES FOUND, THE FOLLOWING MESSAGE WILL BE PRINTED:

DUBBO PA: 00063012 APC: 003524 PASS #00000

DRIVE 00000 DROPPED.

DEVICE ID BIT = 000001

DVID1 BIT SET HIGHER THAN ACTUAL # OF DRIVES FOUND

SOLUTION: TRY A LESSER NUMBER OF UNITS IN DVID1 (LOC 14)

11.0 DUAL PORT OPERATION

TO RUN A DUAL PORT OPERATION, SET BITS OF SRI. THE EXERCISER WILL CHECK THE UNIT TO SEE IF IT IS OFFLINE OR AVAILABLE.

THE UDA WILL RETAIN CONTROL OF A UNIT UNTIL THE MSCP AVAILABLE COMMAND IS ENTERED BY THE HOST. DURING THIS TIME, THE OTHER CONTROLLER IS NOT ALLOWED ACCESS TO THE UNIT THROUGH THE OTHER PORT BETWEEN THE WRITE AND READ. THE OTHER CONTROLLER SENSES WHEN THE UNIT BECOMES AVAILABLE AND TAKES IT. THE MSCP AVAILABLE COMMAND IS ONLY EXECUTED IF SKI BIT 9 AND SKI BIT 1 ARE SET. THIS ALLOWS DUAL PORTING AND DISK ACCESSING RESPECTIVELY.

DEC/X11 WILL ONLY DUAL PORT A DRIVE WITH ANOTHER DEC/X11 EXERCISER.

12.0 GLOSSARY

DUBB FOLLOWS THE MODULE NAME FORMAT DESCRIBED IN THE DEC/X11 PROGRAMMER'S GUIDE.

- DU-- IDENTIFIES THE HARDWARE AND THUS THE MODULE.
- B- DISTINGUISHES BETWEEN TWO OR MORE DIFFERENT MODULES FOR THE SAME GENERIC DEVICE. THE SEQUENCE A, B, C, ETC. MUST BE USED FOR EACH ADDITIONAL EXAMPLE.
- B SPECIFIES THE MODULE REVISION.

IO400X IS A TYPE OF MODULE IN AN EXTENDED INPUT/OUTPUT MODE. THESE MODULES ARE INTERRUPT DRIVEN AND ARE CAPABLE OF INPUT/OUTPUT OPERATION. SOME ADDED CAPABILITIES PROVIDED INCLUDE:

- # USE OF MONITOR SUPPLIED WRITE BUFFERS,
- # ABILITY TO CHANGE THE SIZE OF THE WRITE BUFFERS,
- # ACCESS TO THE MONITOR'S CHECK DATA UTILITY,
- # CONVERSION ROUTINES TO GET 18 BIT ADDRESSES FROM 16 BIT ADDRESSES.

13.0 BIBLIOGRAPHY

- CX00A0 'DEC/X11 USER'S MANUAL' SEPT 1978
- CX0AF0 'DEC/X11 PROGRAMMER'S GUIDE' SEPT 1978

6

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1
2
3 000000 .SHTTL MODULE HEADER BLOCK
   000000 IUMUDX <DUBC> 172150,154,5,0,0,1000,104,RRUF,256,,256.
   000000 MODULE 150000,DUBC 172150,154,5,0,0,1000,104,RRUF,256,,256.
   000000 TITLE DUBC DEC/X11 SYSTEM EXERCISER MODULE
   000000 DDACUM VERSION 6 23-MAY-78
   000000 LIST BIN
*****
000000 REGIN:
000000 MODNAM: .ASCII /DUBC / ;MODULE NAME.
000003 104 125 102 XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF RRUF USAGE
000005 103 040 ADDR: 172150+0 ;1ST DEVICE ADDR.
000006 172150 VECTOR: 154+0 ;1ST DEVICE VECTOR.
000010 000154 BR1: .BYTE PRY5+0 ;1ST RR LEVEL.
000012 240 BR2: .BYTE PRY0+0 ;2ND RR LEVEL.
000013 000 RVIO1: 0+1 ;DEVICE INDICATOR 1.
000014 000001 SR1: OPEN ;SWITCH REGISTER 1.
000016 000000 SR2: OPEN ;SWITCH REGISTER 2.
000020 000000 SR3: OPEN ;SWITCH REGISTER 3.
000022 000000 SR4: OPEN ;SWITCH REGISTER 4.
000024 000000 *****
000026 150000 STAT: 150000 ;STATUS WORD.
000030 000660 INIT: START ;MODULE START ADDR.
000032 000252 SPUINT: MODSP ;MODULE STACK POINTER.
000034 000000 PASCNT: 0 ;PASS COUNTER.
000036 001000 ICUNI: 1000 ;# OF ITERATIONS PER PASS=1000
000040 000000 ILCUNT: 0 ;LOC TO COUNT ITERATIONS
000042 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000044 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000046 000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000050 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000052 000000 SYSCNT: 0 ;# OF SIS ERRORS ACCUMULATED
000054 000000 RANUM: 0 ;HOLD RANUM # WHEN RANU MACRO IS CALLED
000056 000000 CONFIG:
000058 000000 RES1: 0 ;RESERVED FOR MONITOR USE
000060 000000 RES2: 0 ;RESERVED FOR MONITOR USE
000062 000000 SVR0: OPEN ;LOC TO SAVE R0.
000064 000000 SVR1: OPEN ;LOC TO SAVE R1.
000066 000000 SVR2: OPEN ;LOC TO SAVE R2.
000070 000000 SVR3: OPEN ;LOC TO SAVE R3.
000072 000000 SVR4: OPEN ;LOC TO SAVE R4.
000074 000000 SVR5: OPEN ;LOC TO SAVE R5.
000076 000000 SVR6: OPEN ;LOC TO SAVE R6.
000100 000000 CSRAD: OPEN ;ADDR OF CURRENT CSR.
000102 000000 ACSK: OPEN ;ADDR OF GOOD DATA, OR
000104 000000 WASADR: ;CONTENTS OF CSR.
000106 000000 ASADR: OPEN ;ADDR OF BAD DATA, OR
000108 000000 ASKTY: OPEN ;STATUS REG CONTENTS.
000110 000000 ASB: OPEN ;TYPE OF ERROR.
000112 000000 AWAS: OPEN ;EXPECTED DATA.
000114 000000 RSTRT: RESIPT ;ACTUAL DATA.
000116 000000 RWBUF: 0 ;RESTART ADDRESS AFTER END OF PASS
000118 000000 RWBUF: 0 ;WORDS TO REPORT PER ITERATION
000120 000000 RWBUF: 0 ;WORDS FROM REPORT PER ITERATION
000122 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
000124 000104 IUMUM: 104 ;MODULE IDENTIFICATION NUMBER=104

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MODULE HEADER BLOCK

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000124 007076' RBUFVA: RBUF ;READ BUFFER VIRTUAL ADDRESS
000126 000000 RBUFP: OPEN ;READ BUFFER PHYSICAL ADDRESS
000130 000000 RBUFEA: OPEN ;READ BUFFER EA BITS
000132 000400 RBUFSZ: 256 ;SIZE OF THE READ BUFFER
000134 000000 WRBUP: OPEN ;WRITE BUFFER PHYSICAL ADDRESS
000136 000000 WRBUEA: OPEN ;WRITE BUFFER EA BITS
000140 000000 WRBUPH: 256 ;WRITE BUFFER SIZE REQUESTED
000142 000000 WRBFSZ: 0 ;WRITE BUFFER SIZE AVAILABLE
000144 000000 CDECT: OPEN ;C/DATA/DATCH ERROR COUNT
000146 000000 CDWDCI: OPEN ;C/DATA/DATCH WORD COUNT
000150 000040 FREE: .PEP1 SPSIZ ;RESERVED FOR FUTURE USE
   .MLIST ;MODULE STACK STARTS HERE.
   .WORD 0
   .LIST
   .ENDR

000252 MODSP:
*****
4 .SHTTL MODULE STORAGE AREA
5 ;
6 ; VERSION 1.0 FOR RELEASE
7 ; NO LONGER TEST AFTER STEP 4
8 ; NO LONGER WAIT FOR INTERRUPT AFTER SENDING MSCP AVAILABLE COMMAND
9 ; USE BIT 9 IN SR1 FOR DUAL PORTING. (DON'T SEND MSCP AVAILABLE
10 ; COMMAND IF WE WANT JUST SEQUENTIAL OR RANDOM ACCESS MODE --
11 ; IN OTHER WORDS, ONLY SEND ONLINE COMMAND ONCE DURING PASS UNLESS
12 ; DUAL PORT MODE)
13 ;
14 000002 SR.XFR = BIT01 ;NO DISK TRANSFER 0 = NO DISK TRANSFER, 1 = DO DISK TRANSFER
15 000004 SR.REP = BIT02 ;REPORT ERROR AS THEY OCCUR 0 = REPORT, 1 = DON'T REPORT
16 000010 SR.SUM = BIT03 ;REPORT ERRORS ON END OF PASS 0 = REPORT, 1 = DON'T REPORT
17 000014 SR.DUA = BIT09 ;DUAL PORT 0 = NO DUAL PORT, 1 = DUAL PORT
18 000020 SR.SEQ = BIT10 ;RANDOM (NOT SEQUENTIAL) DISK ADDRESSING 0 = SEQUENTIAL, 1 = RANDOM
19 000000 SR.CMP = BIT11 ;NO DATA COMPARE 0 = DO DATA COMPARE, 1 = DON'T DO DATA COMPARE
20 ;
21 ;
22 000252 000000 ;UDA POLLING REG
23 UDASA: .WORD 0 ;UDA STATUS REG
24 000254 000000 CINTR: .WORD 0 ;COMMAND INTERRUPT INDICATOR
25 000256 000000 RINTR: .WORD 0 ;RESPONSE INTERRUPT INDICATOR
26 ;
27 000260 RSPONC: .BLKW 2 ;MESSAGE RING
28 000264 .BLKW 2 ;COMMAND RING
29 ;
30 000270 000000 CMUREF: .WORD 0 ;COMMAND REFERENCE NUMBER
31 ;
32 000272 000000 RSPLEN: .WORD 0 ;RESPONSE PACKET LENGTH
33 000274 000000 RSPVIR: .WORD 0 ;RESPONSE PACKET VIRTUAL CIRCUIT
34 000276 .BLKW 24 ;RESPONSE PACKET
35 ;
36 000356 000000 CMPLen: .WORD 0 ;COMMAND PACKET LENGTH
37 000360 000000 CMPLVIR: .WORD 0 ;COMMAND PACKET VIRTUAL CIRCUIT
38 000362 .BLKW 24 ;COMMAND PACKET
39 ;
40 000442 000264' VA: .WORD 0 ;GENERIC VIRTUAL ADDRESS FOR GETPA
41 000444 000000 PA: .WORD 0 ;GENERIC PHYSICAL ADDRESS
42 000446 000000 EA: .WORD 0 ;GENERIC EXTENDED ADDRESS

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43
44 000450 000000      RBFFEA: .WORD 0      ;READ BUFFER EXTENDED ADDRESS SAVE AREA
45 000452 000000      WBFFEA: .WORD 0      ;WRITE BUFFER EXTENDED ADDRESS SAVE AREA
46
47 000454 000000      NUM: .WORD 0      ;ADDRESS USED IN UJUA
48 000456 000000      OLDDPA: .WORD 0      ;THE OLD PHYSICAL ADDRESS
49 000460 000000      OLDEA: .WORD 0      ;THE OLD EXTENDED ADDRESS TO CHECK IF
50                                     ; UJA WILL BE REINITED
51
52 000462 000017      PRNMSG: .WORD 0      ;PRINT MESSAGE EVERY 15TH TIME
53 000017 000017      PRNUM = 15.      ;PRINT WORD SAVES THE VALUE TO CHECK FOR WHEN
54                                     ; THE NEXT TIME AN END OF PASS MESSAGE IS WRITTEN
55 002260      TIMER = 1200.      ;TIMER VALUE TO WAIT 2-3 SECONDS AFTER DAP COMMAND
56
57 000464 177777      EXPADV: .WORD 177777      ;EXPECTING AN AVAILABLE ATTENTION MESSAGE = 0
58                                     ;NOT EXPECTING AN AVAILABLE ATTENTION MESSAGE = 177777
59
60 000466      ADR1: .BLKB 0
61 000474      ADR2: .BLKB 0
62 000475      ADR3: .BLKB 0
63 000503      ADR4: .BLKB 0
64 000504      ADR5: .BLKB 0
65 000512      ADR6: .BLKB 0
66 000513      ADR7: .BLKB 0
67 000521      ADR8: .BLKB 0
68 000522      ADR9: .BLKB 0
69 000530      ADR10: .BLKB 0
70 000531      ADR11: .BLKB 0
71 000537      ADR12: .BLKB 0
72 000540      ADR13: .BLKB 0
73 000546      ADR14: .BLKB 0
74 000547      ADR15: .BLKB 0
75 000555      ADR16: .BLKB 0
76

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1
2      ;STILL MORE MODULE STORAGE
3
4      ;DO NOT CHANGE THE ORDER OF THE NEXT 4 LOCATIONS
5      ;NEEDED FOR MAP 22 ROUTINE
6 000556 000000      PA1B: .WORD 0
7 000560 000000      XMEM: .WORD 0
8 000562 000000      PA22: .WORD 0
9 000564 000000      EA22: .WORD 0
10
11 000566 000000      SECL: .WORD 0      ;CURRENT SECTOR LO ORDER ADDRESS
12 000570 000000      SECH: .WORD 0      ;CURRENT SECTOR HI ORDER ADDRESS
13
14 000572 000000      UNSZL: .WORD 0      ;UNII SIZE LO ORDER LIMIT FROM ONLINE CMND
15 000574 000000      UNSZH: .WORD 0      ;UNII SIZE HI ORDER LIMIT
16
17 000576 003300      LIMIT: .WORD 3300      ;4K - 1200 = MUST WORDS WAITW CAN TAKE
18
19 000600 000001      DEVICE: .WORD 1      ;DEVICE TO TEST
20 000602 000000      UNITNO: .WORD 0      ;UNIT NUMBER
21 000604 000000      TRY: .WORD 0      ;NUMBER OF TRIES
22 000606 000001      PORTID: .WORD 1      ;BIT POSITION SELECTS THE PORT
23 000610 000000      UNIFL: .WORD 0      ;SAVE UNIT FLAG
24 000612 000000      WORK: .WORD 0      ;TEMPORARY WORK AREA
25
26 000670      TIMEOUT = 3000.      ;TIME OUT GADGE
27 000004      RLIM = 4      ;RETRY LIMIT
28
29 000614 000000 000001      TABLE: .WORD 0,1      ;TABLE ENTRY UNITNO,PORTID
30 000620 177777 177777      .WORD -1,-1      ;CURRENT LAST TABLE ENTRY
31 000624      .BLKB 12.      ;REST OF TABLE
32 000654 177777 177777      TEND: .WORD -1,-1      ;END MARKER
33
34      ;S: .WORD 0,0,0,0,0,0,0      ;FOR HARD AND SOFT ERRORS
35      ; .WORD 177777
36
37      ;TABLE: .WORD S
38      ; .WORD S+2      ;EACH ENTRY OF THE TABLE POINTS TO
39      ; .WORD S+4      ;THE CORRESPONDING ENTRY OF S.
40      ; .WORD S+6      ;THIS IS USED IN ORDER & SUPER
41      ; .WORD S+10
42      ; .WORD S+12
43      ; .WORD S+14
44      ; .WORD S+16
45      ; .WORD 177777

```



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1      .SBITL  MODULE PRIVATE DATA
2
3      000001      BIT00 = 1
4      000002      BIT01 = 2
5      000004      BIT02 = 4
6      000010      BIT03 = 10
7      000020      BIT04 = 20
8      000040      BIT05 = 40
9      000100      BIT06 = 100
10     000200      BIT07 = 200
11     000400      BIT08 = 400
12     001000      BIT09 = 1000
13     002000      BIT10 = 2000
14     004000      BIT11 = 4000
15     010000      BIT12 = 10000
16     020000      BIT13 = 20000
17     040000      BIT14 = 40000
18     100000      BIT15 = 100000
19
20     ;
21     ;      ERROR BITS
22     ;
23     000000      ERR_0 = 0      ;NOT DEFINED
24     000001      ERR_1 = 1      ;DATA ERROR
25     000003      ERR_3 = 3      ;CONTROLLER NOT READY
26     000006      ERR_6 = 6      ;DRIVE NOT READY, OFF LINE OR NON EXISTENT
27     000032      ERR_32 = 32    ;NPR ERROR
28

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1      .SBITL  UDA BIT DEFINITIONS
2
3      ;UDASA REGISTER UNIVERSAL READ BITS
4
5      004000      SA.S1= 004000      ;STEP 1 STATUS BIT
6      010000      SA.S2= 010000      ;STEP 2 STATUS BIT
7      020000      SA.S3= 020000      ;STEP 3 STATUS BIT
8      040000      SA.S4= 040000      ;STEP 4 STATUS BIT
9      100000      SA.ERR= 100000     ;ERROR INDICATOR
10
11     ;UDASA REGISTER ERROR STATUS BITS
12
13     003777      SA.ERC= 003777      ;ERROR CODE
14
15     ;UDASA REGISTER STEP ONE READ BITS
16
17     003400      SA.CTP= 003400      ;CONTROLLER TYPE
18     000400      SA.DIA= 000400      ;DIAG BIT IN UDASA
19
20     ;UDASA REGISTER STEP ONE WRITE BITS
21
22     000177      SA.VEC= 000177      ;INTERRUPT VECTOR (DIVIDED BY 4)
23     000200      SA.INT= 000200      ;INTERRUPT ENABLE DURING INITIALIZATION
24     007400      SA.RSP= 007400      ;RESPONSE RING LENGTH
25     170000      SA.CMD= 170000      ;COMMAND RING LENGTH
26
27     ;UDASA REGISTER STEP TWO READ BITS
28
29     000177      SA.VCE= 000177      ;INTERRUPT VECTOR ECHO
30     000200      SA.INE= 000200      ;INTERRUPT ENABLE ECHO
31
32     ;UDASA REGISTER STEP TWO WRITE BITS
33
34
35     000001      SA.PRG= 000001      ;LOW ORDER MESSAGE RING BYTE ADDRESS
36     ;ENABLE VAX UNIBUS ADAPTER PURGE INTERRUPT
37
38     ;UDASA REGISTER STEP THREE READ BITS
39
40     000017      SA.RSE= 000017      ;RESPONSE RING LENGTH ECHO
41     000360      SA.CME= 000360      ;COMMAND RING LENGTH ECHO
42
43     ;UDASA REGISTER STEP THREE WRITE BITS
44
45     040000      SA.LFC= 040000      ;HIGH ORDER MESSAGE RING BYTE ADDRESS
46     ;LAST FAILURE CODE REQUEST
47
48     ;UDASA REGISTER STEP FOUR READ BITS
49
50     000377      SA.MCV= 000377      ;UDA MICROCODE VERSION
51
52     ;UDASA REGISTER STEP FOUR WRITE BITS
53
54     000001      SA.GU= BIT0        ;GU BIT TO START UDA FIRMWARE

```

1		.SBTTL COMMAND/MESSAGE DESCRIPTOR BIT DEFINITIONS
2		
3	100000	PG.OWN= BIT15 ;SET WHEN UDA OWNS RING
4	040000	PG.FLG= BIT14 ;FLAG BIT
5		
6		;/OFFSETS INTO HOST COMMUNICATIONS AREA WITH ONE DESCRIPTOR TO EACH RING
7		
8	000010	HC.SIZE= 8. ;SIZE OF HOST COMM AREA IN BYTES
9	000060	PKTSIZ= 48. ;SIZE OF PACKETS IN BYTES
10		
11	000000	HC.RESP= 0. ;RESPONSE RING START
12	000002	HC.RCT= 2. ;RESPONSE RING CONTROL WORD
13	000004	HC.CMD= 4. ;COMMAND RING START
14	000006	HC.CCI= 6. ;CONTROL RING CONTROL WORD
15	000276	HC.RPK= RSPACK ;START OF RESPONSE PACKET BUFFER
16	000356	HC.CPK= HC.RPK+PKTSIZ ;START OF COMMAND PACKET BUFFER

1		.SBTTL COMMAND PACKET UPCODES
2		
3	000001	UP.ABD= 01 ;ABORT COMMAND
4	000020	UP.ACC= 20 ;ACCESS COMMAND
5	000010	UP.AVL= 10 ;AVAILABLE COMMAND
6	000021	UP.CCD= 21 ;COMPARE CONTROLLER DATA COMMAND
7	000040	UP.CMP= 40 ;COMPARE HOST DATA COMMAND
8	000013	UP.DAP= 13 ;DETERMINE ACCESS PATHS COMMAND
9	000022	UP.EMS= 22 ;ERASE COMMAND
10	000023	UP.FLU= 23 ;FLUSH COMMAND
11	000002	UP.GCS= 02 ;GET COMMAND STATUS COMMAND
12	000003	UP.GUS= 03 ;GET UNIT STATUS COMMAND
13	000011	UP.OWL= 11 ;ONLINE COMMAND
14	000041	UP.RD= 41 ;READ COMMAND
15	000024	UP.RPL= 24 ;REPLACE COMMAND
16	000004	UP.SCC= 04 ;SET CONTROLLER CHARACTERISTICS COMMAND
17	000012	UP.SUC= 12 ;SET UNIT CHARACTERISTICS COMMAND
18	000042	UP.WK= 42 ;WRITE COMMAND
19	000030	UP.MRD= 30 ;MAINTENANCE READ COMMAND
20	000031	UP.MWK= 31 ;MAINTENANCE WRITE COMMAND
21	000200	UP.END= 200 ;END PACKET FLAG
22	000100	UP.AVA= 100 ;AVAILABLE ATTENTION MESSAGE
23	000101	UP.EPL= 101 ;ERROR LOG ATTENTION MESSAGE
24	000102	UP.SHC= 102 ;SHADOW COPY COMPLETE ATTENTION MESSAGE
25	000102	UP.ACP= 102 ;ACCESS PATH ATTENTION MESSAGE
26		
27		;/NOTE: END PACKET UPCODES (ALSO CALLED ENCODES) ARE FORMED BY ADDING THE END
28		;/PACKET FLAG TO THE COMMAND UPCODE. THE UNKNOWN COMMAND END PACKET CONTAINS
29		;/JUST THE END PACKET FLAG IN ITS UPCODE FIELD.

1		.SBTTL COMMAND MODIFIERS	
2			
3	040000	MD.CMP= 040000	:COMPARE
4	100000	MD.EXP= 100000	:EXPRESS REQUEST
5	010000	MD.ERR= 010000	:FORCE ERROR
6	004000	MD.SCH= 004000	:SUPPRESS CACHING (HIGH SPEED)
7	002000	MD.SCL= 002000	:SUPPRESS CACHING (LOW SPEED)
8	001000	MD.SEC= 001000	:SUPPRESS ERROR CORRECTION
9	000400	MD.SER= 000400	:SUPPRESS ERROR RECOVERY
10	000200	MD.SSH= 000200	:SUPPRESS SHADOWING
11	000100	MD.WBN= 000100	:WRITE-BACK (NON-VOLATILE)
12	000040	MD.WBV= 000040	:WRITE BACK (VOLATILE)
13	000001	MD.SP0= 000001	:SPIN-DOWN
14	000001	MD.FEUS= 000001	:FLUSH ENTIRE UNIT
15	000002	MD.VUL= 000002	:VOLATILE ONLY
16	000001	MD.NXU= 000001	:NEXT UNIT
17			
18		.SBTTL END PACKET FLAGS	
19			
20	000200	EF.BBR= 000200	:BAD BLOCK REPORTED
21	000100	EF.BBU= 000100	:BAD BLOCK UNREPORTED
22	000040	EF.LOG= 000040	:ERROR LOG GENERATED
23	000020	EF.SEX= 000020	:SERIOUS EXCEPTION
24			
25		.SBTTL UNIT FLAGS	
26			
27			
28	000001	UF.CMK= 000001	:COMPARE READS
29	000002	UF.CMA= 000002	:COMPARE WRITES
30	010000	UF.RPL= 010000	:HOST INITIATED BAD BLOCK REPLACEMENT
31	040000	UF.LNA= 040000	:INACTIVE SHADOW SET UNIT
32	000200	UF.RMV= 000200	:REMOVABLE MEDIA
33	004000	UF.SCH= 004000	:SUPPRESS CACHING (HIGH SPEED)
34	002000	UF.SCL= 002000	:SUPPRESS CACHING (LOW SPEED)
35	000040	UF.WBN= 000040	:WRITE-BACK (NON-VOLATILE)
36	020000	UF.WPV= 020000	:WRITE PROTECT(SOFTWARE)
37	010000	UF.WPS= 010000	:WRITE PROTECT(SOFTWARE OR VOLUME)
38	000040	UF.S7b= 000040	:7b BYTE SECTORS

1		.SBTTL CONTROLLER FLAGS	
2			
3	000200	CF.AVL= 000200	:ENABLE AVAILABLE ATTENTION MESSAGES
4	000100	CF.MSC= 000100	:ENABLE MISCELLANEOUS ERROR LOG MESSAGES
5	000040	CF.OTH= 000040	:ENABLE OTHER HOST'S ERROR LOG MESSAGES
6	000020	CF.IHS= 000020	:ENABLE THIS HOST'S ERROR LOG MESSAGES
7	000002	CF.SHD= 000002	:SHADOWING
8	000001	CF.S7b= 000001	:7b BYTE SECTORS
9			
10		.SBTTL COMMAND PACKET OFFSETS	
11			
12			
13	000000	: P.CMT= 0.	GENERIC COMMAND PACKET OFFSETS:
14	000004	: P.UNIT= 4.	:COMMAND REFERENCE NUMBER
15	000010	: P.UPCD= 8.	:UNIT NUMBER
16	000012	: P.MUD= 10.	:OPCODE
17	000014	: P.MCNT= 12.	:MODIFIERS
18	000020	: P.BUFF= 16.	:BYTE COUNT
19	000020	: P.ADPA= 16.	:BUFFER DESCRIPTION
20	000022	: P.ADEA= 18.	:BUFFER'S PHYSICAL ADDRESS (P.BUFF)
21	000034	: P.LBN= 48.	:BUFFER'S EXTENDED ADDRESS (P.BUFF+2)
22	000040	: P.SFTW= 32.	:LOGICAL BLOCK NUMBER
23			:SOFTWARE WORDS
24			
25	000014	: P.UTRF= 12.	ABORT AND GET COMMAND STATUS COMMAND PACKET OFFSETS:
26			:OUTSTANDING REFERENCE NUMBER
27			
28	000016	: P.UNFL= 14.	UNLINE AND SET UNIT CHARACTERISTICS COMMAND PACKET OFFSETS:
29	000020	: P.HSTI= 10.	:UNIT FLAGS
30	000024	: P.UNIT= 20.	:HOST IDENTIFIER
31	000034	: P.ELGF= 28.	:UNIT IDENTIFIER
32	000040	: P.SHUM= 32.	:ERROR LOG FLAGS
33	000042	: P.CSP= 34.	:SHADOW UNIT
34			:COPY SPEED
35			
36	000014	: P.RBN= 12.	REPLACE COMMAND PACKET OFFSETS:
37			:REPLACEMENT BLOCK NUMBER
38			
39	000014	: P.VRSN= 12.	SET CONTROLLER CHARACTERISTICS COMMAND PACKET OFFSETS:
40	000016	: P.CMTF= 14.	:MSCP VERSION
41	000020	: P.HTMD= 16.	:CONTROLLER FLAGS
42	000022	: P.USEF= 18.	:HOST TIMEOUT
43	000024	: P.TIME= 20.	:USE FRACTION
44			:LOAD-WORD TIME AND DATE
45			
46	000034	: P.RGID= 28.	MAINTENANCE READ AND MAINTENANCE WRITE COMMAND PACKET OFFSETS:
47	000040	: P.RGOF= 32.	:REGION ID
			:REGION OFFSET

```

1      .SBTTL END PACKET OFFSETS
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```

GENERIC END PACKET OFFSETS:

COMMAND REFERENCE NUMBER
UNIT NUMBER
UPCODE (ALSO CALLED ENDCODE)
END PACKET FLAGS
MULTIPLIERS
BYTE COUNT
FIRST BAD BLOCK
SOFTWARE WORDS

GET COMMAND STATUS END PACKET OFFSETS:

OUTSTANDING REFERENCE NUMBER
COMMAND STATUS

GET UNIT STATUS END PACKET OFFSETS:

MULTI-UNIT CODE
UNIT FLAGS
HOST IDENTIFIER
UNIT IDENTIFIER
SHADOW UNIT
SHADOW STATUS
TRACK SIZE
GROUP SIZE
CYLINDER SIZE
PCT TABLE SIZE
PBN / TRACK
PCT COPIES

ONLINE AND SET UNIT CHARACTERISTICS

MULTI-UNIT CODE
UNIT FLAGS
HOST IDENTIFIER
UNIT IDENTIFIER
SHADOW UNIT
UNIT SIZE
VOLUME SERIAL NUMBER

SET CONTROLLER CHARACTERISTICS END PACKET OFFSETS:

FSCP VERSION
CONTROLLER FLAGS
CONTROLLER TIMEOUT
CONTROLLER COMMAND LIMIT
CONTROLLER ID
MEDIA TYPE
SHADOW STATUS

ERROR LOG ATTENTION MESSAGE PACKET OFFSETS

COMMAND REFERENCE NUMBER
UNIT NUMBER
UPCODE
UPCODE
ERROR LOG FLAGS
SIZE OR OFFSET
START OF ERROR LOG DATA

```

1      .SBTTL ERROR LOG FLAGS
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3      ;
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```

ERROR LOG MESSAGE OFFSETS

EVENT CODE
EVENT NUMBER
CONTROLLER IDENTIFIER
CONTROLLER SOFTWARE REVISION
CONTROLLER HARDWARE REVISION
UNIT IDENTIFIER
UNIT SOFTWARE REVISION
UNIT HARDWARE REVISION
ERROR LOCATION
CYLINDER
GROUP
TRACK
SECTION
VOLUME SERIAL NUMBER
EVENT DEPENDENT DATA

STATUS AND EVENT CODE DEFINITIONS

STATUS / EVENT CODE MASK
SUB-CODE MULTIPLIER
SUCCESS
INVALID COMMAND
COMMAND ABORTED
UNIT-OFFLINE
UNIT-AVAILABLE
MEDIA ERROR
WRITE PROTECTED
COMPARE ERROR
DATA ERROR
HOST BUFFER ACCESS ERROR
CONTROLLER ERROR
DRIVE ERROR
MESSAGE FROM AN INTERNAL DIAGNOSTIC

SUBCODES FOR ST.OFL

NO VOLUME MOUNTED
OR DRIVE DISABLED VIA RUN/STOP SWITCH
UNIT INOPERATIVE
UNIT DISABLED BY FIELD SERVICE
OR INTERNAL DIAGNOSTIC
DUPLICATE UNIT NUMBER

SUBCODES FOR ST.DRV

SDI RESPONSE TIME OUT
INVALID SDI RESPONSE

DUBC DEC/X11 SYSTEM EXERCISER M MACRO V04.00 29-JUN-82 15:37:54 PAGE 16-1
MODULE CODE

SEQ 0027

```

58 000730 005067 177210 CLW CUENCL          ;CLEAR DATA CHECK ERROR COUNT
59 000734 012767 177777 MOV    #177777,EXPVAL    ;DO EXPECTING AN INTERRUPT
60 000742 012767 000017 MOV    #PRNUM,PRNMSG    ;INITIALIZE PRINT WORD
61 000750 016767 177049 MOV    UDI01,VICE      ;VICE HAS DESIRED BITS SET
62 000756 000756 177132 MOV    #TABLE,PC      ;SET TABLE FOR UNIT 0
63 000759 012767 000001 MOV    #TABLE+2,PC      ;SET TABLE FOR PORTED FOR UNIT 0
64 000765 005067 177274 CLR    CINDEX      ;CINDEX AND REF # = 0
65 000774 104417 000000 RANDS,BTGLN      ;
66 000780 016767 177050 MOV    #RANUM,SECL    ;
67 001006 005067 177560 MOV    SECL,SECL      ;
68 001012 016767 176770 CLR    ADDR,UDASA      ;
69 001020 062767 000002 ADD    #2,UDASA      ;
70 001026 005067 177424 CLR    UDOPA      ;
71 001032 005067 177422 CLR    UDOPA      ;
72                                     ;
                                     ; FOR RESTARTING. THIS WILL FORCE A UDA REINIT TO TAKE PLACE

```

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13 001036
14 001036 012767 000260' 177376
15 001044 104415 000000' 000442'
16 001052 026767 177366 177376
17 001060 001004
18 001062 026767 177360 177370
19 001070 001412
20 001072 016767 177346 177356
21 001100 016767 177342 177352
22 001106 004767 000332
23 001112 005067 177466
24 001116
25 001116 032767 000010 176672
26 001124 001034
27 001126 026767 177330 176700
28 001134 001030
29 001136 026767 000017 177316
30
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42
001144 104421 000000' 000042'
001152 000475'
001154 105067 177322
001160 104421 000000' 000044'
001166 000504'
001170 105067 177315
001174 104421 000000' 000144'
001202 000466'
001204 105067 177263
001210 104403 000000' 005316'
001216 012777 004336' 176564 15:
001224 104415 000000' 000124'
001232 016700 176672
001236 004767 000566
001242 010067 177202

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43 001246 005067 177330
44 001252 032767 000002 176536
45 001260 001454
46
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51 001262 004767 001026
52 001266 005767 177306
53 001272 001002
54
55 001274 104410 000000'
56
57 001300
58 001300 104414 000000'
59 001304 016700 176626
60 001310 004767 000514
61 001314 010067 177132
62 001320 012704 000614'
63 001324 012703 000001'
64 001330 030367 177244
65 001334 001412
66 001336 016467 000002 177242
67 001343 011467 177232
68
69 001350 004767 001244
70 001354 103002
71 001356 004767 002022
72 001362
73 001362 062704 000004
74 001366 006303
75 001370 022704 000654'
76 001374 001403
77 001376 020367 177176
78 001402 003752
79 001404
80 001404 104413 000000'
81
82 001410 000733
83
84
85
86
87
88 001412
89 001412 104414 000000'
90 001416 016700 176514
91 001422 004767 000402
92 001426 010067 177020
93 001432 004767 001460
94 001436 104413 000000'
95
96
97
98
99
100 001442 000763

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22 001444 012767 000260' 176770 INITUD: MOV #RSPUNC,VA ;VA -> RSPUNC
23 001452 104415 000000' 000442' GETPAS,BEGIN,VA ;GET PHYSICAL ADDRESS FROM 16-BIT VA
24 001460 005004 ;#4 IS USED IF AN ERROR IS DETECTED
25 001462 012702 000001 CLR #R2 ;R2 = STEP INDICATOR REG FOR MSG'S
26 001466 005077 176314 CLF #ADDH ;WRITE TO UDAIP TO INIT UDA
27 001472 012701 002260 MOV #TIMEK,R1 ;SET TIME OUT LIMIT
28 001476 017700 176550 MOV #UASA,R0 ;R0 HAS UDASA DATA
29 001502 032700 100000 BIT #<SA.ERN>,R0 ;CHECK FOR ERROR
30 001506 001007 BNE ZS ;IF FOUND, GET OUT OF LOOP
31 001510 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
32 001520 005301 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
33 001522 001365 DEC R1 ;TIME OUT?
34 001524 000404 BNE ZS ;IF NOT, LOOP
35 001526 012703 004000 BR 4S ;IF DONE, CONTINUE
36 001532 000167 000400 JMP ERRORD1 ;R3 = STEP 1 HIT
37 001536 022700 004400 CMP #<SA.S1+SA.DIA>,R0 ;IF HERE, ERROR
38 001542 001402 BEQ 5S ;DID DATA COMPARE PROPERLY?
39 001544 000167 000370 JMP ERRORD3 ;IF SO, CONTINUE
40 ;REPORT ERROR
41 001550 016705 176234 MOV VECTOR,R5 ;VECTOR GIVEN
42 001554 006205 ASK R5 ;SET TO APPROPRIATE VALUE
43 001556 006205 ASK R5 ;= VECTOR/4
44 001560 052705 100200 BIS #<SA.INT+BIT15>,R5 ;ACTIVATE INTERRUPTS & SET MSB FOR STEP 1
45 ;LEN'S ARE 0
46 001564 010500 MOV R5,R0 ;STORE R5 IN R0 FOR SUBROUTINE
47 001566 012703 004000 MOV #SA.S1,R3 ;R3 HAS STEP HIT FOR SUBROUTINE
48 001572 004767 000244 JSE PC,SNDSTP ;SEND STEP DATA
49 001576 042705 100000 BIC #BIT15,R5 ;CLEAR MSB FOR COMPARE DATA
50 001602 042700 000200 BIC #BIT07,R0 ;HAS HIT07 ONLY HIT SET?, SHOULD BE
51 001606 001404 BEQ 6S ;SET R0 TO REPORT THE ERROR
52 001610 052700 010200 BIS #<SA.S2+BIT07>,R0 ;REPORT ERROR
53 001614 000167 000320 JMP ERRORD3 ;R0 GETS PHYSICAL ADDRESS
54 001620 016700 176620 MOV PA,R0 ;SEND STEP DATA
55 001624 004767 000212 JSR PC,SNDSTP ;HIGH BYTE CLEARED
56 001630 042705 177400 BIC #177400,R5

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57 001634 020500 CMP R5,R0 ;CHECK ECHO DATA
58 001636 001402 BEQ 7S ;IF OK, SKIP
59 001640 000167 000274 JMP ERRORD3 ;IF NOT, REPORT ERROR
60 001644
61
62 001644 016700 176576 MOV STEP 3 EA,R0 ;ADJUST THE EXTENDED ADDRESS BITS
63 001650 004767 000154 JSR PC,ASR04 ;SHIFT EXTENDED ADDRESS BITS FOR UDA
64 001654 004767 000162 JSR PC,SNDSTP ;SEND STEP DATA
65 001660 012700 000254' MOV #RSPUNC-4,R0 ;R0 -> RING ENVELOPE
66
67 001664 005720 BNE ZS ;IS THE RING ENTRY = 0?
68 001666 001117 BNE ERRORD5 ;IF NOT, ERROR
69 001670 022700 000270' CMP #CHDREF,R0 ;IS NO POINT PAST THE RINGS?
70 001674 001373 BNE ZS ;IF NOT, LOOP
71 001676 016700 176116 MOV SM2,R0 ;R0 = BURST VALUE
72 001702 000241 CLC ;CLEAR CARRY
73 001704 006300 ASL R0 ;ALIGN BURST FOR STEP 4
74 001706 006300 ASL R0
75 001710 052700 000001 BIS #SA.GO,R0 ;SET GO BIT
76 001714 010077 176332 MOV #UASA,R0 ;SEND DATA TO UDA/DONE WITH THE INIT
77 001720 012767 000362' MOV #CMPACK,VA ;GET COMMAND PACKET PA AND EA
78 001726 104415 000000' 000442' GETPAS,BEGIN,VA ;GET PHYSICAL ADDRESS FROM 16-BIT VA
79 001734 016767 176504 MOV PA,CUMMND ;STORE ADDRESS IN THE RING
80 001742 016700 176500 MOV EA,R0 ;SAVE IN R0
81 001746 004767 000056 JSR PC,ASR04 ;SHIFT EXTENDED ADDRESS BITS FOR UDA
82 001752 010067 176310 MOV #C,CUMMND+2 ;MOVE ADJUSTED EA INTO RING
83
84 001756 012767 000276' 176456 MOV #RSPACK,VA ;GET RESPONSE PACKET PA AND EA
85 001764 104415 000000' 000442' GETPAS,BEGIN,VA ;GET PHYSICAL ADDRESS FROM 16-BIT VA
86 001772 016767 176446 176260 MOV PA,RSPUNC ;STORE ADDRESS IN THE RING
87 002000 016700 176442 MOV EA,R0 ;SAVE IN R0
88 002004 004767 000020 JSR PC,ASR04 ;SHIFT EXTENDED ADDRESS BITS FOR UDA
89 002010 010067 176246 MOV #RSPUNC+2 ;MOVE ADJUSTED EA INTO RING
90 002014 012777 004336' 175766 MOV #INTERRUPT,VECTOR ;STORE INTERRUPT ADDRESS IN VECTOR
91 002022 005067 176556 CLR TRY ;CLEAR TRY SO DRIVE WILL
92 ;GO BACK ONLINE IF NECESSARY
93
94 002026 000207 RIS PC
95
96
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108 002030 ASR04: ASK R0 ;SHIFT 10
109 002030 006200 ASK R0 ;SHIFT 4
110 002032 006200 ASK R0 ;SHIFT 2
111 002034 006200 ASK R0 ;SHIFT 1
112 002036 006200 RTS PC ;RETURN
113 002040 000207

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126 002042 016701 175742
127 002046 012721 002066
128 002052 116711 175734
129 002056 010077 176170
130
131 002062 104400 000000
132
133 002066
134 002066 000004 000000 002074
135 002074
136 002074 017700 176152
137 002100 032700 100000
138 002104 001017
139 002106 005202
140 002110 006303
141 002112 030300
142 002114 001002
143 002116 000167 000020
144 002122 040300
145 002124 000207

*****
SEND STEP DATA
INPUT: R0 HAS DATA TO BE SENT TO UDA FOR STEP
R3 HAS PREVIOUS STEP FLAG SET
OUTPUT: R0 HAS DATA SENT FROM UDA TO HOST FOR ECHO AND NEXT STEP
R3 HAS CURRENT STEP FLAG SET
*****
SNDSTP: MOV VECTOR,R1
MOV #INTA,(R1)+
MOV BHI,(R1)
MOV R0,UDASA
EXIT$ BEGIN
*****
;SET UP INTERRUPT HANDLER ADDRESS
;SET PRIORITY LEVEL
;SEND STEP1 WRITE FORMMATED DATA
;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
INTA:
-----
PIRGS,BEGIN,$S ; QUEUE UP TO CONTINUE AT 3$ AND RTI
-----
3$:
MOV UDASA,R0
BIF #SA,ERR,R0
BNE ERROR1
INC R2
ASL R3
BIT R3,R0
BNE AS
JMP ERROR2
BIC R3,R0
RTS PC
*****
;GET STEP N FORMMATED DATA
;TEST FOR ERROR
;IF NOT OK, REPORT
;SET STEP REGISTER
;R3 HAS STEP HIT PROPERLY SET
;WAS STEP N SET?
;IF SO, CONTINUE
;IF NOT CORRECT STEP, ERROR
;CLEAR THE STEP BIT, FOR COMPARE
;RETURN

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25 002126
26 002126 104403 000000 005374
27 002134 104410 000000
28 002140 005204
29 002142 005204
30 002144 005204
31 002146 010267 176302
32
33 002152 104420 000000 000454
34 002160 000475
35 002162 017767 176064 176264
36 002170 104420 000000 000454
37 002176 000466
38 002200 005304
39 002202 001003
40 002204 104403 000000 005204
41 002212 005304
42 002214 001003
43 002216 104403 000000 005230
44 002224 005304
45 002226 001003
46 002230 104403 000000 005236

*****
ERROR 1
PRINT AN ERROR REPORTED BY THE UDA DIAGNOSTICS
*****
ERROR2
PRINT A THE VALUE OF THE UDASA WHEN THE STEP HIT WAS NOT SET
*****
ERROR3
PRINT A THE VALUE OF THE UDASA WHEN THE ECHO WAS NOT SET
CORRECTLY
*****
INPUT R0 -> UDASA
R2 = STEP COUNT
OUTPUT THE RETRY COUNT IS INCREMENTED
IF THE RETRY COUNT > RETRY LIMIT, END MODULE
*****
ERRORS
RING AASH'T ALL ZERO -> ERROR
DROP UDASA
*****
ERRORS: MSGNS,BEGIN,ZERO
ENDS,BEGIN
*****
;ASCII MESSAGE CALL WITH COMMON HEADER
;
ERROR3: INC R4
ERROR2: INC R4
ERROR1: INC R4
MOV R2,NUM
*****
;R4 = 3 FOR ERROR3
;R4 = 2 FOR ERROR2
;R4 = 1 FOR ERROR1
;STORE STEP REG IN A NUMBER FOR CONVRT
;CONVERT NUM TO ASCII AND
;STORE AT ADDR2
OIOAS,BEGIN,NUM,ADR2
*****
;STORE VALUE IN A NUMBER
;CONVERT NUM TO ASCII AND
;STORE AT ADM1
MOV UDASA,NUM
*****
OIOAS,BEGIN,NUM,ADM1
*****
DEC R4
BNE 1$
MSGNS,BEGIN,INITE1
DEC R4
BNE 2$
MSGNS,BEGIN,INITE2
DEC R4
BNE 3$
MSGNS,BEGIN,INITE3
*****
;ERROR 1?
;IF NOT, CHECK IF IT IS THE NEXT ERROR
;ASCII MESSAGE CALL WITH COMMON HEADER
;ERROR 2?
;IF NOT, CHECK IF IT IS THE NEXT ERROR
;ASCII MESSAGE CALL WITH COMMON HEADER
;ERROR 3?
;IF NOT, CHECK IF IT IS THE NEXT ERROR
;ASCII MESSAGE CALL WITH COMMON HEADER

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58 002514 016714 176062      17S:  MOV     UNITNO,(R4)          ;ELSE, CORRECT THE UNIT NUMBER IN TABLE
59 002520                                :NEXT PARTID SET
60 002520 006303                                :DUNE?
61 002522 026703 176052      ASL      R3                      ;IF R3 > DVICE, ALL DESIRED DRIVES ARE FOUND.
62 002526 100407                                :NEXT UNITNO SET
63 002530 005267 176046      BMT     UNITNO                    ;POINT TO NEXT ENTRY TO TEST DRIVE
64 002534 062704 000004      ADD      #4,R4                    ;POINT TO END? IF SO, TABLE FULL
65 002540 022704 000654      CMP      REND,R4                    ;IF R4 NOT REACHED END, GO TEST
66 002544 101276                                :
67 002546                                :
68 002546 000207      19S:   RTS      PC
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76 002550 022700 000003      TSTUFL: CMP  #ST,UFL,R0          ;WAS THE DRIVE FOUND OFFLINE?
77 002554 001403      BEQ      10S                      ;CHECK WHAT KIND OF OFFLINE
78 002556 022700 000013      CMP      #ST,DRV,R0          ;WAS IT A DRIVE ERROR? -> SDI?
79 002562 001012      BNE      13S                      ;IF IT WAS NOT, ERROR (DROP DRIVE)
80 002564 032767 000740 10S:  BIT      #SC,NVL+SC.DIS+SC.DDP+SC.10P,P.STS+RSPACK ;WERE ANY OF THESE BITS SET?
81
82
83 002572 001004      BNE      12S                      ; = NO VOLUME MOUNTED, UNIT DISABLED BY FIELD SERVICE
84 002574 032767 177000 175506  BIT      #SC,NVL+SC.DIS+SC.DDP+SC.10P,P.STS+RSPACK ;OR DUPLICATE UNIT NUMBER OR UNIT INOPERATIVE
85 002602 001002      BNE      13S                      ;IF SO, EXIT
86 002604 000241      CLC      CARRY                      ;IF SO, DROP
87 002606 000207      RTS      PC                          ;CLEAR CARRY
88 002610 000261      SET      CARRY, DRIVE WAS FOUND TO BE OFFLINE
89
90 002612 004767 002132      JSR      PC,ERRRURH          ;RETURN
91 002616 000207      RTS      PC                          ;REPORT ERROR

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18 002620                                :
19 002620 032767 001009 175170  CYCLED:  BIT      #SR,DUA,SM1          ;DUAL PORT?
20 002626 001004      BNE      2S                        ;IF NOT, CONTINUE
21
22 002630 005767 175750      ; *** CHECK IF WE DO OFFLINE FOR THE FIRST TIME.
23 002634 100443      TST      TRY                        ;IF TRY HAS SET MSH, DON'T DO OFFLINE
24 002636 000422      FMT      10S                      ;DON'T DO OFFLINE
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29 002640 012701 000010      ; *** DO GET STATUS COMMANDS TO ASSURE THE DRIVE IS AVAILABLE TO THE UDA
30 002644 004767 001262      ; *** FOR DUAL PORTING.
31 002650 103013      2S:   MOV      #10,R1                    ;R1 = # OF GET STATUS TO DO
32 002652 004767 177672      4S:   JSR      PC,G1STAT          ;IS THE DRIVE OFFLINE?
33 002656 103507      JCC      PC,TSTOFL                    ;IF ALL OK, DO THE CYCLE
34
35 002660 005067 175600      ; *** HANDLE OFF LINE DRIVE, WAIT FOR AVAILABLE ATTENTION MESSAGE
36 002664 052767 140000 175370  CLR      EXPAV          ;EXPECT AN AVAILABLE ATTENTION MESSAGE
37 002672 004767 001434      BIS      #RG,0WH+RG.FLG>,RSPUNC+2 ;SET FLAG FOR ATTN MESSAGE
38
39 002676 000402      JSR      PC,INTERR                    ;WAIT FOR MESSAGE
40
41 002700 005301      BNE      4S                        ; 2ND ATTENTION MESSAGE
42 002702 001360      DNE      4S                        ;DUNE?
43 002704 004767 001316      6S:   DNE      4S                        ;IF NOT DUNE, TRY AGAIN
44 002710 103753      10S:  JSR      PC,ONLINE              ;DO AND OFFLINE COMMAND
45 002712 016767 175426 175654  BCS      2S                        ;IF CARRY WAS SET, TRY AGAIN
46 002720 016767 175416 175644  14S:  MOV      P,UNSZ+2+RSPACK,UNSZH    ;IS THE UNIT SIZE HI ADDRESS
47 002726 001006      MOV      P,UNSZ+RSPACK,UNSZL          ;GET UNIT SIZE/IS IT = 0?
48 002730 005767 175640      BNE      10S                      ;IF NOT ZERO, CONTINUE WITH ITERATION
49 002734 001731      TST      UNSZH                      ;IS UNSZH ALSO 0?
50 002736 012767 100000 175640  BEQ      CYCLED              ;IF 0, TRY TO BRING ONLINE AGAIN
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58 002744      ;*****
59 002744      16S:
60 002744      MOV    WBUF,RO
61 002750      CLR    R1
62 002752      INC    R1
63 002754      SUB    #400,R0
64 002760      BHL    R0
65 002762      SUM    R1,UNSZL
66          ; ***
67 002766      NDA    PICK WHICH BLOCK TO WRITE TO
68 002772      JSR    PC,PICKBK
69 002776      JSR    PC,GTSTAI
70 003000      BCS    ZS
71 003004      CMP    #ST.AVL,R0
72          ; ***
73 003006      BEQ    ZS
74 003012      WRITE TO THE BLOCK SELECTED
75 003014      JSR    PC,WRITE
76 003022      BCC    19S
77 003024      BIT    #SR.DUA,SR1
78 003030      BNE    ZS
79          ; ***
80 003032      READ IT BACK
81 003036      JSR    PC,READ
82 003040      BCS    ZS
83 003046      BIT    #SR.CMP,SR1
84          ; ***
85 003050      HNE    ZOS
86 003056      COMPARE DATA
87 003060      COPIAS,BEGIN,RBUFA
88 003066      *+2
89 003070      BIT    #SR.DUA,SR1
90 003074      BEQ    ZS
91          ; ***
92 003076      MAKE THE DRIVE AVAILABLE
93          JSR    PC,AVAILM
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114 003134      001004      ; *****
115 003136      004412      000000* 000126*      ; IF NOT, SKIP THE COMPARE
116 003144      003146*      ; REQUEST FOR MONITOR TO CHECK DATA
117 003146      000207      21S:      ; IF ERROR, CONTINUE
118          RTS    PC

```

MODULE CODE

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11 003150
12 003150 032767 002000 174640
13 003156 001467
14 003160
15 003160 104417 000000
16 003164 016746 174664
17 003170 104417 000000
18 003174 016746 174654
19
20
21
22 003200 000241
23 003202 042716 100000
24 003206 012667 175356
25 003212 005767 175356
26 003216 001430
27
28 003220 016700 175350
29 003224 005100
30 003226 012701 100000
31 003232 030100
32 003234 001403
33 003236 000241
34 003240 006001
35 003242 000773
36 003244 040100
37 003246 000241
38 003250 006001
39 003252 001374
40 003254 040067 175310
41 003260 026767 175304 175306
42 003266 002420
43 003270 001405
44 003272 006267 175272
45 003276 000414
46
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48
49 003300 005067 175264
50 003304 005767 175262
51 003310 001406
52 003312 166716 175254
53 003316 103375
54 003320 066716 175246
55 003324 000401
56 003326 005016
57 003330 012667 175232

*****
;
; PICK A BLOCK TO WRITE TO.
;
; EITHER PICK THE NEXT SEQUENTIAL BLOCK (DEFAULT) OR TAKE ONE AT
; RANDOM.
;
; OUTPUT: FILL SECH & SECL (CURRENT SECTOR ADDR)
;
*****
PICKBK: BIT #SR,SEQ,SP; ;CHECK SRI FOR RANDOM ACCESS MODE
;SEQACC ;OR IF SEQUENTIAL ACCESS
;
;
; RANDS,BEGIN
; MOV RANNUM,-(SP) ;GENERATE THE SECTOR ADDRESS
; RANDS,BEGIN
; MOV RANNUM,-(SP) ;GENERATE THE SECTOR ADDRESS
;
; ADJUST HI ADDRESS FIRST
;
;
; CLC ;CLEAR CARRY FOR ROTATE
; BIC #100000,(SP) ;CLEAR UPPER BIT MAKES SURE VALUE'S +
; MOV (SP)+,SFCH ;STORE IN SECTOR HI ADDRESS
; TST UNSZH ;IS THE MAX SIZE 0?
; BEQ 38 ;IF 0, GET LOW SECTOR ADDRESS
;
; *** UNSZH > 0 IF CODE FALLS THROUGH HERE
; MOV UNSZH,R0 ;R0 = MAX VALUE
; CUM R0 ;R0 COMPLEMENT, NOW FIND MS ZERO
; MOV #100000,R1 ;R1 IS INDEX INTO MAX VALUE
; BIT R1,R0 ;HAVE 0 YET?
; BEQ 28 ;IF 1ST 0 REACHED, CLEAR REST OF THE BITS
; CLC ;CLEAR CARRY FOR ROR
; ROR R1 ;POINT TO NEXT BIT
; BPL 18 ;BRANCH TO TEST AGAIN
; BIC R1,R0 ;CLEAR REST OF THE BITS
; CLC ;CLEAR CARRY FOR ROR
; ROR R1 ;IF R1 ROTATES INTO CARRY, R1 = 0
; BNE 28 ;IF R1 NOT 0, MORE BITS TO CLEAR
; BIC R0,SECH ;CLEAR UPPER BITS OF HIGH SECTOR VALUE
; CMP SECH,UNSH ;IF THE HIGH SECTOR VALUE > MAX VALUE?
; BEQ 38 ;IF =, TEST LOW ORDER VALUE
; ASR SECH ;IF =, TEST LOW ORDER VALUE
; BK 78 ;SECH = SECH/2 - CAN'T BE > MAX NOW
; EXIT
;
; GET LOW SECTOR ADDRESS
;
;
; CLR SECH ;CLEAR HI SECTOR SIZE
; UNSZL ;IS THE HIGHEST POSSIBLE = 0?
; BEQ 48 ;IF TRUE, DON'T DO LOOP
; SUB UNSZL,(SP) ;ELSE, SECL = SECL - UNSZL (ADJUST)
; BCC 58 ;IF UNSZL > SECL, LOOP
; ADD UNSZL,(SP) ;ELSE SUBTRACTED ONCE TOO OFTEN
; BR 78 ;AND EXIT
;
; CLR (SP) ;CLEAR LO SECTOR ADDRESS (IF HIGHEST POSSIBLE = 0)
; MOV (SP)+,SECL ;SAVE LO SECTOR ADDRESS

```

MODULE CODE

```

58 003334 000207 HIS PC ; RETURN
59
60
61
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63 003336
64 003336 005267 175224
65 003342 001405
66 003344 026767 175216 175220
67 003352 103413
68 003354 000402
69 003356
70 003356 005267 175206
71 003362
72 003362 026767 175202 175204
73 003370 103404
74 003372 005067 175170
75 003376 005067 175166
76
77 003402
78 003402 000207

;GENERATE DISK ADDRESS BY SEQUENTIAL ADDRESSING
;
;SEQACC: INC SECL ;INCREMENT THE SECTOR ADDRESS
;TST 168 ;OR IF ZERO
;CMP SECL,UNSZL ;OVER LIMIT?
;BLT 168 ;OR IF LOWER
;BR 178 ;SKIP THE INCREMENT
;
;168: INC SECH ;INCREMENT SECTOR HIGH ADDRESS
;
;178: CMP SECH,UNSH ;OVER LIMIT?
;BLT 168 ;OR IF LOWER
;CLR SECL ;RESET THE STARTING SECTOR ADDRESS
;CLR SECH
;
;188: HIS PC

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16 003404
17 003404 012767 000001 175042
18 003412 000407
19 003414
20 003414 012767 000002 175032
21 003422 000403
22 003424
23 003424 012767 000003 175022
24 003432 010767 175150 175140
25 003440 001445
26 003442 022767 177777 175136
27
28 003450 001441
29
30 003452 046767 175130 175120
31
    003460 104421 000000' 000602'
    003466 000475'
32 003470 105067 175006
33
    003474 104420 000000' 000606'
    003502 000466'
34 003504 012764 177777 000002
35 003512 005367 174736
36 003516 001004
37 003520 104403 000000' 005244'
38 003526 000412
39 003530 005367 174720
40 003534 001004
41 003536 104403 000000' 005262'
42 003544 000403
43 003546
44 003546 104403 000000' 005300'
45 003554 000207
46

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*****
;
; DROP A DRIVE
;
; A DRIVE WOULDN'T RESPOND, DROP IT. SET THIS UP IN DEVICE.
;
; INPUT UNITNO = UNIT NUMBER OF DRIVE TO DROP
;        PORTID = BIT SET TO DROP DRIVE
;
; OUTPUT DEVICE HAS A BIT CLEARED. THE BIT POSITION
;        REPRESENTS THE DRIVE
;
*****
DROP1: MOV #1,NUM
      BR DROP4
DROP2: MOV #2,NUM
      BR DROP4
DROP3: MOV #3,NUM
      BR DROP4
DROP4: MOV PORTID,DEVICE ;HAS THE DRIVE BEEN DROPPED, DON'T DROP AGAIN
      BEQ 10$ ;IF DRIVE HAS BEEN DROPPED, DON'T DROP AGAIN
      CMP #177777,PORTID ; (WILL ZERO DEVICE PREMATURE)
      BEQ 10$ ;IF =, DRIVE HAS BEEN DROPPED -> EXIT ROUTINE
      BIC PORTID,DEVICE ;DROP THE DRIVE
      *****
      ;CONVERT UNITNO TO ASCII AND
      ;STORE AT ADDR2
      BTODS,BEGIN,UNITNO,ADR2
      *****
      CLRH ADR2+5
      *****
      ;CONVERT PORTID TO ASCII AND
      ;STORE AT ADDR1
      UTOAS,BEGIN,PORTID,ADR1
      *****
      MOV #177777,2(R4) ;DESELECT DRIVE SO IT WON'T BE USED AGAIN.
      DEC NUM ;DROPPED FOR WHICH ERROR?
      BNE 1$ ;IF NOT FOR ERRORS, CONTINUE
      MSGNS,BEGIN,DRP1 ;ASCII MESSAGE CALL WITH COMMON HEADER
      BR 10$
      DEC NUM
      BNE 2$ ;WAS UNIT NOT FOUND?(NON EXISTENT UNIT)
      MSGNS,BEGIN,DRP2 ;ASCII MESSAGE CALL WITH COMMON HEADER
      BR 10$
      MSGNS,BEGIN,DRP3 ;ASCII MESSAGE CALL WITH COMMON HEADER
      ; ACTUAL UNITS FOUND
      RTS PC

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13 003556 004767 001074
14 003562 012767 000030 174602
15 003570 016767 174654 174606
16 003576 016767 174324 174576
17 003604 016700 174322
18 003610 000424
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34 003612 004767 001040
35 003616 012767 000031 174546
36 003624 016767 174622 174552
37 003632 016767 174276 174542
38 003640 026767 174276 174730
39 003646 100403
40 003650 016700 174722
41 003654 000402
42 003656 016700 174260
43 003662 006300
44 003664 010067 174506
45 003670 012767 000020 174374
46 003676 012767 000044 174452
47 003704 012767 000001 174504
48 003712 012767 177777 174440
49 003720 012767 177777 174346
50 003726 000167 000330

*****
MAINTENANCE READ
*****
SET UP A PACKET WITH:
  OPCODE & MODIFIER
  REGION ID & REGION OFFSET
  READ BUFFER DESCRIPTOR
  BYTE COUNT
THEN SEND THE PACKET
*****
MAITH: JSR PC,CLRPAN ;CLEAR THE PACKETS
        MOV #UP,MRD,P,OPCD+CMPPACK ;SET THE OPCODE
        MVB #HFFEA,P,ADPA+CMPPACK ;SET THE BUFFER DESCRIPTOR
        MVB #HUFFSZ,R0 ;STORE THE BUFFER SIZE IN WORDS
        BR MAITP ;SET UP THE REST OF THE PACKET
*****
MAINTENANCE WRITE
*****
SET UP A PACKET WITH:
  OPCODE & MODIFIER
  REGION ID & REGION OFFSET
  WRITE BUFFER DESCRIPTOR
  BYTE COUNT (EITHER #HUFFSZ OR LIMIT IF #HUFFSZ > LIMIT)
THEN SEND THE PACKET
*****
MAITH: JSR PC,CLRPAN ;CLEAR THE PACKETS
        MOV #UP,MWR,P,OPCD+CMPPACK ;SET THE OPCODE
        MVB #HFFEA,P,ADPA+CMPPACK ;SET THE BUFFER DESCRIPTOR
        CMP #HUFFSZ,LIMIT ;IS THE BUFFER SIZE > LIMIT?
        BNC MAITP ;IF NOT, #HUFFSZ IS OK
        MOV #HUFFSZ,R0 ;STORE THE BUFFER SIZE IN WORDS
        BR MAITP ;AND SKIP
        MAITP: MVB #HUFFSZ,R0 ;STORE THE BUFFER SIZE IN WORDS
        MOV #HUFFSZ,R0 ;MAKE IT NUMBER OF BYTES
        MVB #HUFFSZ,R0 ;SET WRITE BUFFER SIZE
        MVB #16,,RSPLEN ;SET RESPONSE PACKET LENGTH
        MVB #36,,CMPLN ;SET COMMAND PACKET LENGTH
        MVB #1,P,RCID+CMPPACK ;SET REGION ID = 1
        MVB #177777,CMPLN ;SET COMMAND VIRTUAL CIRCUIT (-1 FOR DM)
        MVB #177777,RSPVIR ;SET COMMAND VIRTUAL CIRCUIT
        JMP SEND ;SEND THE PACKET
*****
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12 003732 004767 000720
13 003736 012767 000042 174426
14 003744 016700 174172
15 003750 016767 174160 174424
16 003756 016767 174470 174420
17 003764 000415
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30 003766 004767 000664
31 003772 012767 000041 174372
32 004000 016700 174126
33 004004 016767 174440 174372
34 004012 016767 174110 174362
35 004020 012767 000040 174244
36 004026 012767 000040 174322
37 004034 006300
38 004036 010067 174334
39 004042 016767 174520 174346
40 004050 016767 174514 174342
41 004056 000501

*****
WRITE
*****
SET UP OPCODE, MODIFIERS, BUFFER SIZE (BYTE COUNT),
  BUFFER DESCRIPTOR (PHYSICAL AND EXTENDED ADDRESS),
  DISK ADDRESS AND CYLINDER ID (LOGICAL BLOCK NUMBER),
  THEN SEND THE PACKET.
*****
WRITE: JSR PC,CLRPAN ;CLEAR PACKETS
        MOV #UP,WR,P,OPCD+CMPPACK ;SET THE OPCODE
        MVB #HUFFSZ,R0 ;STORE THE BUFFER SIZE IN WORDS
        MVB #HUFFEA,P,ADPA+CMPPACK ;SET THE BUFFER DESCRIPTOR(PA)
        MVB #HFFEA,P,ADPA+CMPPACK ;SET THE BUFFER DESCRIPTOR(LEA)
        BR READA
*****
READ
*****
SET UP OPCODE, MODIFIERS, BUFFER SIZE (BYTE COUNT),
  BUFFER DESCRIPTOR (PHYSICAL AND EXTENDED ADDRESS),
  DISK ADDRESS AND CYLINDER ID (LOGICAL BLOCK NUMBER),
  THEN SEND THE PACKET.
*****
READ: JSR PC,CLRPAN ;CLEAR PACKETS
        MOV #UP,RD,P,OPCD+CMPPACK ;SET THE OPCODE
        MVB #HUFFSZ,R0 ;STORE THE BUFFER SIZE IN WORDS
        MVB #HUFFEA,P,ADPA+CMPPACK ;SET THE BUFFER DESCRIPTOR
        MVB #HUFFEA,P,ADPA+CMPPACK ;SET THE BUFFER DESCRIPTOR
        MVB #12,,RSPLEN ;SET RESPONSE PACKET LENGTH
        MVB #32,,CMPLN ;SET COMMAND PACKET LENGTH
        MVB #HUFFSZ,R0 ;MAKE IT NUMBER OF BYTES
        MVB #1,P,RCID+CMPPACK ;SET REGION ID = 1
        MVB #177777,CMPLN ;SET COMMAND VIRTUAL CIRCUIT (-1 FOR DM)
        MVB #177777,RSPVIR ;SET COMMAND VIRTUAL CIRCUIT
        JMP SEND ;SEND THE PACKET
*****
```

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8 004060 004767 000572
9 004064 012767 000013 174300
10 004072 012767 000074 174172
11 004100 012767 000074 174250
12 004106 000465
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21 004110 004767 000542
22 004114 012767 000010 174250
23 004122 012767 000014 174142
24 004130 000413
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34 004132 004767 000520
35 004136 012767 000003 174226
36 004144 012767 000001 174222
37 004152 012767 000060 174112
38 004160 012767 000014 174170
39 004166 000435

*****
DETERMINE ACCESS PATHS
SET UP CODE, GO SEND PACKET
*****
DAP: JSR PC,CLRPAK ;CLEAR PACKETS
      MOV #UP,DAP,P,UPCD+CMACK ;SET UP CODE
      MOV #60,,RSPLEN ;SET LENGTHS
      MOV #60,,CMPLN ;SEND THE PACKET
      BR SEND
*****
AVAILABLE PACKET
SET UP CODE AND MODIFIERS THEN SEND THE PACKET
*****
AVAILM: JSR PC,CLRPAK ;CLEAR PACKETS
         MOV #UP,AVL,P,UPCD+CMACK ;SET THE UP CODE
         MOV #12,,RSPLEN ;SET RESPONSE PACKET LENGTH
         BR GTSTAA ;SEND THE PACKET
*****
GET UNIT STATUS
SET OP CODE AND MODIFIER (FOR THEN NEXT UNIT
THEN SEND THE PACKET
*****
GTSTAT: JSR PC,CLRPAK ;CLEAR PACKETS
         MOV #UP,GUS,P,UPCD+CMACK ;SET THE OP CODE
         MOV #MD,NXD,P,MOD+CMACK ;CLEAR MODIFIERS
         MOV #48,,RSPLEN ;SET RESPONSE PACKET LENGTH
         MOV #12,,CMPLN ;SET COMMAND PACKET LENGTH
         BR GTSTAA ;SEND THE PACKET
*****

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11 004170
12 004174 004767 000462
13 004174 012767 000034 174154
14 004202 012767 000034 174062
15 004210 012767 000004 174154
16 004216 012767 000200 174154
17
18 004224 000416
19
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29 004226 004767 000424
30 004232 012767 000040 174032
31 004240 012767 000044 174110
32 004246 012767 000011 174116
33 004254 016767 174322 174140
34

*****
SET CONTROLLER CHARACTERISTICS
SET UP CODE AND CONTROLLER FLAG (ENABLE ATTENTION MSGS)
CLEAR MSCP VERSION, HOST TIMEOUT, USE FRACTION,
AND ALL OF QUAD WORD TIME AND DATE.
THEN SEND PACKET
*****
SCC: JSR PC,CLRPAK ;GO CLEAR THE COMMAND PACKET
      MOV #28,,CMPLN ;SET UP COMMAND PACKET LENGTH
      MOV #28,,RSPLEN ;SET UP RESPONSE PACKET LENGTH
      MOV #UP,SCC,P,UPCD+CMACK ;SET THE UP CODE
      MOV #CF,AVL,P,CNTF+CMACK ;SET THE CONTROLLER FLAGS
      BR SEND ;TO ENABLE ATTENTION MSGS
      ;SEND THE PACKET
*****
ONLINE
SET UP CODE, MODIFIERS, UNIT ID, HOST ID
SHADOW UNIT, ERROR FLAGS
THEN SEND PACKET
*****
ONLINE: JSR PC,CLRPAK ;CLEAR PACKETS
         MOV #32,,RSPLEN ;SET RESPONSE PACKET LENGTH
         MOV #36,,CMPLN ;SET COMMAND PACKET LENGTH
         MOV #UP,ONL,P,UPCD+CMACK ;SET THE UP CODE
         MOV #UNITID,P,SHDW+CMACK ;SHADOW UNIT = UNITNO.
         ;SEND THE PACKET
*****

```


113 004646 052767 140000 173406 B1S *KRG,OWN+RG,PLG>,RSPUNC+2
114 004654 000626 BR INTERP ;WAIT FOR RESPONSE OF LAST PACKET SENT
115

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12 004656 017767 173370 173570
13 004656 001421
14 004664 001421
15
16 004666 104420 000000 000454
17 004674 000466
18 004676 104403 000000 005336
19 004704 010346
20 004706 010446
21 004710 004767 174530
22 004714 012603
23 004716 012604
24 004720 004767 177244
25 004724 005267 173114
26 004730 012702 000064 5S: MOV #52,R2
27 004734 012705 000272 6S: MOV #KSPLEN,R5
28 004740 005025 CLR (R5)+
29 004742 005302 DEC R2
30 004744 001375 BNE 6S
31 004746 000207 RTS PC
32
33
34
35
36
37 004750
38 004750 032767 000004 173040
39 004756 001403
40 004760 005267 173060
41
42 004764 000407
43 004766 004767 000056 7S: JSR PC,SETTAB
44 004772 104405 000000 000000 HDRS,BEGIN,MULL
45 005000 004767 000070 8S: JSR PC,PRINIE
46 005004 000261 SEC
47 005006 000207 RTS PC
48
49
50

```

CLEAR PACKETS
ASSUMPTION: 1) RESPONSE BUFFER PRECEDES THE COMMAND BUFFER
2) TWO WORDS BEFORE EACH BUFFER IS FOR LENGTH
OF PACKET AND VIRTUAL CIRCUIT
OUTPUT: R2 = 0 WHEN DONE
R5 = END OF COMMAND PACKET WHEN DONE

CLRPAC: MOV #UDASA,NUM ;IS UDASA NOT ZERO/STORE IN NUM IF TRUE
BEQ 5S ;IF UDASA IS ZERO, CLEAR PACKETS

;CONVERT NUM TO ASCII AND
;STORE AT ADDR1
UDAS,BEGIN,NUM,ADDR1

MSGNS,BEGIN,SANUTO ;ASCII MESSAGE CALL WITH COMMON HEADER
MOV R3,=(SP) ;SAVE R3
MOV R4,=(SP) ;SAVE R4
JSR PC,INITUD ;RE INIT UDASA
MOV (SP),R3 ;RESTORE R3
MOV (SP),R4 ;RESTORE R4
JSR PC,SCC ;SET CONTROLLER CHARS AGAIN
INC HRCNT ;INCREMENT HARD ERROR COUNT
;DOING THIS WILL CAUSE ANOTHER CALL TO CLRPAC
R2 = # OF WORDS TO CLEAR
R5 -> KSPLEN, 1ST WORD TO CLEAR
CLR (R5)+ ;CLEAR WORD
DEC R2 ;R2 = ZERO? (DONE CONDITION)
BNE 6S ;IF NOT ZERO, LOOP
RTS PC ;RETURN

HARD ERROR CARRY WILL BE SET

ERRORH: BIT #SR.REP,SR1 ;DO WE REPORT THE ERROR?
BEQ 7S ;IF SO, REPORT
INC HRCNT ;ELSE, INCREMENT THE HARD ERROR
;COUNT IF NOT REPORTED
BR 8S ;SKIP REPORT
7S: JSR PC,SETTAB ;SET UP TABLE

HDRS,BEGIN,MULL ;

8S: JSR PC,PRINIE
SEC
RTS PC ;RETURN TO CYCLED

```

51      ;
52      ;
53      ;
54      005010      ;
55      005010      032767      000004      173000      ;
56      005016      001403      ;
57      005020      005267      173016      ;
58      ;
59      005024      000407      ;
60      005026      004767      000016      ;
61      005032      104406      000000      000000      ;
62      005040      004767      ;
63      005044      000261      000030      ;
64      005046      000207      ;
65      ;
66      ;
67      ;
68      ;
69      ;
70      ;
71      ;
72      ;
73      005050      ;
74      005050      016767      172732      173022      ;
75      005056      016767      173226      173020      ;
76      005064      017767      173162      173010      ;
77      005072      000207      ;
78      ;
79      ;
80      ;
81      ;
82      ;
83      ;
84      ;
85      005074      ;
86      ;
87      ;
88      ;
89      ;
90      ;
91      ;
92      ;
93      ;
94      ;
95      ;
96      ;

```

SOFT ERROR CARRY WILL BE SET

ERRORS:

BIT #SH.REP,SK1 ;DO WE REPORT THE ERROR?

BEQ 9S ;IF SO, REPORT

INC SUFCNT ;ELSE, INCREMENT THE HARD ERROR

9S: JSH 10S ;COUNT IF NOT REPORTED

JSH PC,SETTAB ;SKIP REPORT

SUFCNT,BEGIN,NULL ;SET UP TABLE

JSH PC,PRINTF ;SET CARRY

SEC ;RETURN TO CYCLE

RTS PC

SETTAB

SET UP A TABLE OF VALUES FOR A SOFT OR HARD ERROR

SETTAB:

MOV ADDR,CSHA ;SET UP CONTROL STATUS REG REPORT

MOV P.STS+RSPACK,ASTAT ;SET UP STATUS

MOV MODASA,ACSR ;REPORT WHAT IS STATUS REG

RTS PC

PRINT EXTENDED ERROR MESSAGE

PRINT STATUS, UPCODE, UNIT NUMBER, BYTE COUNT, LBN AND ADDRESS

PRINT:

CONVERT P.STS+RSPACK TO ASCII AND

STORE AT ADR1

UTDAS,BEGIN,P.STS+RSPACK,ADR1

CONVERT P.OPCD+RSPACK TO ASCII AND

STORE AT ADR2

UTDAS,BEGIN,P.OPCD+RSPACK,ADR2

CONVERT P.UNIT+RSPACK TO ASCII AND

STORE AT ADR3

UTDAS,BEGIN,P.UNIT+RSPACK,ADR3

CONVERT P.BCNT+RSPACK TO ASCII AND

```

005124      104420      000000      000312      ;
005132      000513      ;
90      ;
91      ;
92      ;
93      ;
94      ;
95      ;
96      ;

```

STORE AT ADR4

UTDAS,BEGIN,P.BCNT+RSPACK,ADR4

CONVERT P.LBN+2+CMPPACK TO ASCII AND

STORE AT ADR5

UTDAS,BEGIN,P.LBN+2+CMPPACK,ADR5

CONVERT P.LBN+CMPPACK TO ASCII AND

STORE AT ADR6

UTDAS,BEGIN,P.LBN+CMPPACK,ADR6

CONVERT P.ADEA+CMPPACK TO ASCII AND

STORE AT ADR7

UTDAS,BEGIN,P.ADEA+CMPPACK,ADR7

CONVERT P.ADPA+CMPPACK TO ASCII AND

STORE AT ADR8

UTDAS,BEGIN,P.ADPA+CMPPACK,ADR8

MSGNS,BEGIN,BANNER ;ASCII MESSAGE CALL WITH COMMON HEADER

RTS PC

```

1 005204 005446' .SHITL: MODULE MESSAGES
2 005206 005502' INITE1: MSG2
3 005210 177777 MSG4
4 177777
5
6 005212 005470' INITEP: MSG3
7 005214 000466' ADK1
8 005216 005662' MSG10
9 005220 000475' ADK2
10 005222 000010' MSG14
11 005224 000504' ADK3
12 005226 177777 177777
13
14 005230 005446' INITE2: MSG2
15 005232 005527' MSG5
16 005234 177777 177777
17
18 005236 005446' INITE3: MSG2
19 005240 005545' MSG6
20 005242 177777 177777
21
22 005244 005637' DRP1: MSG8
23 005246 000475' ADK2
24 005250 005647' MSG9
25 005252 006215' MSG20
26 005254 000466' ADK1
27 005256 006702' MSGD1
28 005260 177777 177777
29
30 005262 005637' DRP2: MSG8
31 005264 000475' ADK2
32 005266 005647' MSG9
33 005270 006215' MSG20
34 005272 000466' ADK1
35 005274 006746' MSGD2
36 005276 177777 177777
37
38 005300 005637' DRP3: MSG8
39 005302 000475' ADK2
40 005304 005647' MSG9
41 005306 006215' MSG20
42 005310 000466' ADK1
43 005312 007010' MSGD3
44 005314 177777 177777
45
46 005316 005674' ERRPAS: MSG11
47 005320 000475' ADK2
48 005322 005720' MSG12
49 005324 000504' ADK3
50 005326 005756' MSG13
51 005330 000466' ADK1
52 005332 005444' MSG1
53 005334 177777 177777
54
55 005336 006072' SANUT0: MSG17
56 005340 000466' ADK1
57 005342 006121' MSG18

```

```

58 005344 177777 177777
59
60 005346 006021' UNIOFF: MSG16
61 005350 000466' ADK1
62 005352 177777 177777
63
64 005354 006526' WARN1: MSG40
65 005356 177777 177777
66
67 005360 006403' WARN2: MSG37
68 005362 177777 177777
69
70 005364 006334' WARN3: MSG36
71 005366 177777 177777
72
73 005370 005602' ABORT: MSG7
74 005372 177777 177777
75
76 005374 006165' ZERO: MSG19
77 005376 177777 177777
78
79 005400 006240' RANNER: MSG21
80 005402 000466' ADK1
81 005404 006332' MSG23
82 005406 000475' ADK2
83 005410 006332' MSG23
84 005412 000504' ADK3
85 005414 006332' MSG23
86 005416 000513' ADK4
87 005420 006332' MSG23
88 005422 000522' ADK5
89 005424 006332' MSG23
90 005426 000531' ADK6
91 005430 006332' MSG23
92 005432 000540' ADK7
93 005434 006332' MSG23
94 005436 000547' ADK8
95 005440 005444' MSG1
96 005442 177777 177777

```

MORE MODULE MESSAGES

```

1      .SBTIL  MORE MODULE MESSAGES
2      .NLST   HEX
3
4 005444 045 000 MSG1: .ASCIZ 'A'
5 005446 045 125 MSG2: .ASCIZ 'A00A INIT ERROR, '
6 005470 045 125 MSG3: .ASCIZ 'A00A = '
7 005502 106 117 MSG4: .ASCIZ 'FOUND BY DIAGNOSTIC '
8 005527 123 124 MSG5: .ASCIZ 'STEP NOT SET, '
9 005545 105 120 MSG6: .ASCIZ 'EXPECTED DATA WAS INCORRECT '
10 005602 045 122 MSG7: .ASCIZ 'RETRY COUNT EXCEEDED, ABORT '
11 005637 045 104 MSG8: .ASCIZ 'ABOVE '
12 005647 040 122 MSG9: .ASCIZ 'DROPPED, '
13 005662 040 111 MSG10: .ASCIZ 'IN STEP '
14 005674 045 123 MSG11: .ASCIZ 'SOFT ERROR COUNT #'
15 005720 040 040 MSG12: .ASCIZ 'HARD ERROR COUNT #'
16 005756 045 103 MSG13: .ASCIZ 'CHECK DATA ERROR COUNT #'
17 006010 045 101 MSG14: .ASCIZ 'ADDR = '
18 006021 045 125 MSG15: .ASCIZ 'UNIT WAS FOUND OFFLINE. UNIT NUMBER = '
19 006072 045 125 MSG16: .ASCIZ 'A00A IS NOT ZERO, = '
20 006121 045 125 MSG17: .ASCIZ 'A00A IS GOING THROUGH INITIALIZATION '
21 006165 045 122 MSG18: .ASCIZ 'RING AREA NOT CLEARED '
22 006215 045 104 MSG19: .ASCIZ 'DEVICE ID BIT = '
23 006240 045 124 MSG20: .ASCIZ 'STATUS ENCOD UNITNO BYTECO HI LBN LO LBN EXTADR PHYADR#'
24 006332 040 000 MSG21: .ASCIZ ' '
25 006334 040 041 MSG22: .ASCIZ ' ! OPERATING WITH NO DISK ACCESSING ! '
26 006403 007 007 MSG23: .ASCIZ '<0><07> ! CUSTOMER DATA WILL BE OVERWRITTEN ! '
27 006424 045 055 MSG24: .ASCIZ '-----<07><07>'
28 006526 040 106 MSG40: .ASCIZ ' ! IF YOU WISH TO DESTROY CUSTOMER DATA, SET BIT (NOT BIT0) '
29 006621 040 111 MSG25: .ASCIZ ' ! IN SWITCH REGISTER (SK1) OF DUBA? EQUAL TO 1. '
30 006702 045 105 MSG26: .ASCIZ 'ERRORS CAUSED DRIVE TO BE DROPPED '
31 006746 045 125 MSG27: .ASCIZ 'UNIT WAS NOT FOUND BY EXERCISER '
32 007010 045 104 MSG28: .ASCIZ 'UNID1 BIT SET HIGHER THAN ACTUAL # OF DRIVES FOUND '
33
34 007076 000001 FBUF: .BLKW 256. ;THE READ BUFFER
35

```

DURC DEC/111 SYSTEM EXERCISER M MACRO V04.00 29-JUN-82 15:37:54 PAGE 31-1

SEQ 0057

SYMBOL TABLE

```

A00MT 005370P CF.57b= 000001 HC.C4= 000004 M.L.HRV= 000040 UP.ONLE= 000011
ACSH 000102H CINTR 000254H HC.CPK= 000356H MODNAM 000000H UP.RD= 000041
ADDR 000006H CLRPAK 004656H HC.KCTE= 000002 MUDSP 000252H UP.RPL= 000024
ADDR22= 001000 CMDREF 000270H HC.KES= 000000 MSG01 006702H UP.SC= 000004
ADK1 000466H CMAPACK 000362H HC.KPK= 000276H MSG02 006746H UP.SHC= 000102
ADK2 000475H CMAPLEN 000356H HC.KSZ= 000010 MSG03 007010H UP.SUC= 000012
ADK3 000504H CMAPVIR 000360H HMOCKR= 000044H MSGNS = 104403 UP.WK= 000042
ADK4 000513P CMAND 000264H HNDERS= 104405 MSGNS = 104402 OTAS = 104420
ADK5 000522H CONF1G 000056H HNDERS= 000050H MSGS = 104401 PA = 000444H
ADK6 000531R CSKA 000100H ICOUNT 000036H MSG1 005444H PASCNT 00034H
ADK7 000540R CYCLE1 002620P ICOUNT 000040H MSG10 005662H PAIR 000556H
ADK8 000547R CYCLE2 003116H IDUM = 000122H MSG11 005674H PAZ2 000562H
AS= 000106P DAP 000406H IWDIA= 000000 MSG12 005720H PICKHK 003150H
ASH04 002030H DATCK5= 104411 INIT 000030P MSG13 005756H PIPWS = 000004
ASTAT 000104H DAPERS= 104404 INITER 005212H MSG14 006010P PKTSIZ= 000060
AVAILB 004110H DDINTR 003100H INITF1 005204H MSG16 006021P POPSP = 005726
AAS 000110H DRUP1 003404H INITF2 005240H MSG17 006072P POPSP= 022626
BANNER 005400H DRUP2 003414H INITF3 005236H MSG18 006121H POKI1D 000606H
BEGIN 000000P DRUP3 003424H INITUD 001444H MSG19 006165H PRINTL 000674H
BIT0 = 000001 DRUP4 003432H INTA 002066H MSG2 005446H PRNMSG 000462H
BIT00 = 000001 DHP1 005244H INTERP 004332H MSG20 006215H PRTMUM= 000017
BIT01 = 000002 DRP2 005262H INTR 000120H MSG21 006240H PRIY = 000000
BIT02 = 000004 DRP3 005300H LIMIT 000576H MSG23 006332H PRTO = 000000
BIT03 = 000010 DVICE 000600H LOUP1 001300H MSG3 005470H PRY1 = 000040
BIT04 = 000020 DVID1 000014H LOUP2 001336H MSG36 006344H PRY2 = 000100
BIT05 = 000040 EA 000446H L.CHVR= 000015 MSG37 006403H PRY3 = 000140
BIT06 = 000100 FA22 000564H L.CNTI= 000014 MSG4 005502H PRY4 = 000200
BIT07 = 000200 EF.HHR= 000260 L.CYL = 000034 MSG40 006526H PRY5 = 000240
BIT08 = 000400 EF.HRU= 000100 L.DATA= 000050 MSG5 005527H PRY6 = 000300
BIT09 = 001000 EF.FRS= 000200 L.EKLC= 000030 MSG6 005545H PRY7 = 000340
BIT1 = 000002 EF.LOC= 000040 L.FVMT= 000000 MSG7 005602H PS = 177776
BIT10 = 002000 EF.LST= 000180 L.GRP = 000040 MSG8 005637H PSH = 177776
BIT11 = 004000 EF.MS= 000001 L.SCTR= 000042 MSG9 005647H PSH = 005746
BIT12 = 010000 EF.SEX= 000020 L.SLUT= 000002 NTRUPT 004335H PSH2 = 024646
BIT13 = 020000 ENDT5= 104413 L.TKCN= 000041 NULL = 000000 P.WFLG= 000002
BIT14 = 040000 ENDS = 104410 L.UHVR= 000027 NUM 000454H P.ADEA= 000022
BIT15 = 100000 EKHORH 004750H L.UH1= 000016 OLDEA 000460H P.ADEA= 000020
BIT2 = 000004 ERRORS 003010P L.UHVR= 000026 OLDPA 000456H P.BCNT= 000014
BIT3 = 000010 ERRORI 002144H L.VSRK= 000044 UNF1L= 000001 F.BUFF= 000020
BIT4 = 000020 EKHOR2 002142H MAITP 003662H ONLINE 004226H P.CMSI= 000020
BIT5 = 000040 EKHOR3 002140H MAITR 003556H UP.EY = 000000 P.CMCL= 000022
BIT6 = 000100 ERRORS 002126H MAITA 003612H UP.ABU= 000001 F.CNT = 000006
BIT7 = 000200 ERRPAS 005316H MAP22S= 104416 UP.ACC= 000020 F.CWIF= 000016
BIT8 = 000400 EKRTYP 000106P MAIONC 001412H UP.ACP= 000102 F.CNTI= 000024
BIT9 = 001000 ERK.O = 000000 MD.CMP= 000000 UP.AVA= 000100 F.CPSP= 000046
BREKAS= 104407 ERK.1 = 000001 MD.EXP= 010000 UP.AVL= 000001 F.CMP = 000006
BR1 000012H ERK.3 = 000003 MD.FEU= 000000 UP.CCD= 000021 F.CTAD= 000020
BR2 000013H ERK.32= 000032 MD.FEU= 000001 UP.CMP= 000040 F.CYL = 000050
BIT09S = 104421 ERK.6 = 000006 MD.GAU= 000001 UP.DAT= 000013 F.ELGF= 000034
CDATA5 = 104412 EXITS = 104400 MD.SCH= 000000 UP.DND= 000200 F.FBCK= 000034
CODECT 000144H EXPAV 000464H MD.SCL= 002000 UP.EKL= 000101 F.FLGS= 000011
CODEUCT 000146H FREE 000150H MD.SEC= 001000 UP.EKS= 000022 F.GRP = 000046
CF.AVL= 000200 GETPAS= 104415 MD.SER= 000400 UP.FLU= 000323 F.HSII= 000020
CF.AVC= 000100 GSTAA 004160H MD.SPD= 000001 UP.FLS= 000002 F.HIMU= 000020
CF.OTH= 000040 GSTAT 004132H MD.SSH= 000200 UP.GUS= 000003 F.LBN = 000034
CF.SHD= 000002 GWRUFS= 104414 MD.VUL= 000002 UP.MPD= 000030 P.LGDI= 000014
CF.THS= 000020 HC.CCI= 000006 MD.WRN= 000100 UP.MAR= 000031 P.MEDI= 000034

```

```

. ABS. 000000      000
        010076      001
ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 12800 WORDS ( 50 PAGES)
DYNAMIC MEMORY AVAILABLE FOR 71 PAGES
.AX:XDUBCO.LST/C=DDXCDM.XDUBCO.DUC.XDUBCO

```

SEQ 0059

[illegible]

SEW 0061

SEW 0061

ERRITYP	6-3#	28-46*	28-59*	28-64*	28-69*	28-95*
EXISTS	6-3#	18-131	28-29			
EXPAND	6-15#	16-59*	21-35*	21-98*	28-108	28-110*
FREE	6-3#					
GCTPAS	6-3#	17-15	17-39	18-23	18-78	18-85
GISTAA	26-24	26-38*				
GISTAT	24-30	21-30				
GWBUFF	6-3#	17-57	21-68	26-34*		
HC-CCT	10-14#					
HC-CMD	10-13#					
HC-CPK	10-16#					
HC-RCT	10-12#					
HC-RES	10-11#					
HC-RPK	10-15#	10-16				
HC-SIZ	10-8#					
HRDCNT	6-3#	17-32	29-23*	29-40*		
HRDRS	6-3#	19-49	29-44*			
HKDPAS	6-3#					
ICOUNT	6-3#					
ICOUNT	6-3#					
IDNUM	6-3#					
IMODX.	6-3#	17-57	17-88			
INIT	6-3#					
INITE1	19-38	30-2#				
INITE2	19-42	30-4#				
INITE3	19-46	30-18#				
INITER	19-50	30-6#				
INITUD	17-22	18-22#	29-19			
INTA	18-127	18-133#				
INTEPP	21-17	21-100	28-28#	28-114		
INTR	6-3#					
L-CHVR	15-13#					
L-CNTI	15-11#	15-12#				
L-CYL	15-18#					
L-DATA	15-23#					
L-ERLC	15-17#					
L-EVNT	15-9#					
L-GXP	15-19#					
L-SECT	15-21#					
L-SLOT	15-10#					
L-TRCK	15-20#					
L-UHVR	15-16#					
L-UNIT	15-14#					
L-USVR	15-15#					
L-VSER	15-22#					
L-WITT	7-16#					
LOOP1	17-53	24-37	24-39			
LOOP2	17-63#	17-57#	17-81			
MAIONC	17-45	17-78				
MAITP	24-18	17-88#	17-94			
MAITR	24-18	24-40	24-42#			
MAITw	21-112	24-13#				
MAITw	21-111	24-33#				
MAP22s	6-3#					
MD-CMD	12-3#					
MD-ERR	12-5#					
MD-EXP	12-4#					

DOI: 10.1002/for

[illegible]

SEQ 0065

SEQ 0065

[illegible]

DIHC DEC/X11 SYSTEM EXERCISER M MACRO V04.00 29-JUN-82 15:37:54 PAGE 5-9
CROSS REFERENCE TABLE (CREF V04.00)

SEQ 0067

[illegible]

[illegible]

```

1      .REM      *
2
3
4
5
6      IDENTIFICATION
7
8      PRODUCT CODE:  AC-S871A-MC
9
10     PRODUCT NAME:  CXCIAA0 DEC/X11 CISP MODULE
11
12     DATE:          JANUARY, 1982
13
14     MAINTAINER:    DIAGNOSTIC GROUP
15
16
17     THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT
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32      1.  ABSTRACT
33
34      THE CISP IS A DEC/X11 BACKGROUND MODULE THAT EXERCISES
35      THE COMMERCIAL INSTRUCTION SET PROCESSOR.  THE CHARACTER
36      STRINGS INSTRUCTIONS ARE TESTED BY MOVING STRINGS IN
37      MEMORY AND COMPARING THE RESULTING DESTINATION WITH THE
38      SOURCE.  THE DECIMAL ARITHMETIC INSTRUCTIONS ARE TESTED
39      BY CALCULATING ALGEBRAIC IDENTITIES INVOLVING ALL
40      OPERATIONS AND COMPARING THE ANSWERS.  NUMBERS FOR
41      CALCULATIONS ARE GENERATED RANDOMLY.
42
43      2.  REQUIREMENTS
44
45      HARDWARE:  A CISP COMMERCIAL INSTRUCTION SET PROCESSOR
46                CONFIGURED ON A PDP-11 PROCESSOR
47
48      STORAGE:  CISP REQUIRES 1715 OCTAL OR 963 DECIMAL WORDS OF STORAGE.
49
50      3.  PASS DEFINITION
51
52      ONE PASS OF THE CISP MODULE CONSISTS OF 200 (OCTAL)
53      CYCLES OF THE TEST SEQUENCE.
54
55      4.  EXECUTION TIME
56
57      ONE PASS OF THE CISP MODULE RUNNING ALONE ON PDP-11/44
58      TAKES APPROXIMATELY 20 SECONDS.
59
60      5.  CONFIGURATION REQUIREMENTS
61
62      DEFAULT PARAMETERS:  NONE
63
64      VECTOR:  NONE
65
66      REQUIRED PARAMETERS:  NONE
67
68      6.  DEVICE/OPTION SETUP
69
70      MAKE SURE A COMMERCIAL INSTRUCTION SET PROCESSOR IS INSTALLED
71
72
73
74
75
76      7.  MODULE OPERATION
77
78      THE MODULE XCIAA0 CONSISTS OF 16 TESTS INVOLVING A SEQUENCE OF
79      ONE OR MORE CIS INSTRUCTIONS.  TESTS USUALLY END WITH A COMPARE
80      STRING INSTRUCTION FOR ERROR CHECKING.  ERRORS ARE REPORTED AFTER
81      EACH SEQUENCE AND CONSIST OF STARTING ADDRESS OF SOURCE AND
82      DESTINATION STRINGS PLUS THE FIRST WORD OF EACH STRING.  MODULE
83      OPERATION IS DEPENDENT ON THE SUCCESS OF THE COMPARE INSTRUCTIONS,
84      THEREFORE THEY ARE TESTED INDIVIDUALLY BEFORE EACH SECTION OF THE
85      MODULE.  THE FAILURE OF A COMPARE INSTRUCTION IS FATAL AND RESULTS
86      THE MODULE BEING DROPPED.
87      THE CISP USE THE R6 STACK AS WORK SPACE FOR BOTH THE EXECUTION
88      AND INTERRUPTION OF THE CIS INSTRUCTIONS.  THIS MODULE PROVIDES ITS
  
```

```

89      OWN H6 STACK TO INSURE ENOUGH WORK SPACE FOR THE CISP. THE
90      SUSPENSION OF A CIS INSTRUCTION IS INDICATED BY PROCESSOR STATUS
91      BIT 8 BEING SET. THIS MODULE RELIES ON THE MONITOR STACK TO
92      PROVIDE SPACE FOR AN INTERRUPTED CIS INSTRUCTION.
93
94      TEST SEQUENCE:
95
96      1) COMPARE CHARACTER STRING
97
98      2) MOVE COMPARE
99
100     3) SCAN MOVE COMPARE
101
102     4) SPAN MOVE COMPARE
103
104     5) MATCH MOVE COMPARE
105
106     6) MOVE REVERSE MOVE REVERSE COMPARE
107
108     7) MOVE TRANSLATE MOVE TRANSLATE COMPARE
109
110     8) LOCATE MOVE COMPARE
111
112     9) SKIP MOVE COMPARE
113
114     10) COMPARE NUMERIC
115
116     11) CALCULATE 10[(A+B-C)]=(10*A-10*C+10*B) COMPARE NUMERIC
117
118     12) CONVERT LONG -> NUMERIC -> LONG
119
120     13) CONVERT NUMERIC -> PACKED -> NUMERIC COMPARE NUMERIC
121
122     14) COMPARE PACKED
123
124     15) CALCULATE 10[H+C]=10[(H**2+C**2)/(H-C)] COMPARE PACKED
125
126     16) CONVERT LONG -> PACKED -> LONG
127
128
129     8. OPERATION OPTIONS
130
131     NONE
132
133     9. NON-STANDARD PRINTOUTS
134
135     NONE
  
```

```

136      000000      R0      =%0
137      000001      R1      =%1
138      000002      R2      =%2
139      000003      R3      =%3
140      000004      R4      =%4
141      000005      R5      =%5
142      000006      R6      =%6
143      000007      R7      =%7
144      000007      PC      =R7
145      000006      SP      =R6
146      076010      MFPT    =076010
147      076020      L2D0    =076020
148      076021      L2D1    =076021
149      076022      L2D2    =076022
150      076023      L2D3    =076023
151      076024      L2D4    =076024
152      076025      L2D5    =076025
153      076026      L2D6    =076026
154      076027      L2D7    =076027
155      076030      MOVCC   =076030
156      076031      MOVVC   =076031
157      076032      MOVTC   =076032
158      076040      LOCC     =076040
159      076041      SKPC     =076041
160      076042      SCANC    =076042
161      076043      SPANC    =076043
162      076044      CMPC     =076044
163      076045      MATC     =076045
164      076050      ADDN     =076050
165      076051      SUBN     =076051
166      076052      CMPN     =076052
167      076053      CVTNL    =076053
168      076054      CVTPN    =076054
169      076055      CVTNP    =076055
170      076056      ASHN     =076056
171      076057      CVTLN    =076057
172      076060      L3D0     =076060
173      076061      L3D1     =076061
174      076062      L3D2     =076062
175      076063      L3D3     =076063
176      076064      L3D4     =076064
177      076065      L3D5     =076065
178      076066      L3D6     =076066
179      076067      L3D7     =076067
180      076070      ADDP     =076070
181      076071      SUBP     =076071
182      076072      CMPP     =076072
183      076073      CVTPL     =076073
184      076074      MULP     =076074
185      076075      DIVP     =076075
186      076076      ASHP     =076076
187      076077      CVTLP     =076077
188      076130      MOVCI     =076130
189      076131      MOVRCI    =076131
190      076132      MOVTCI    =076132
191      076140      LOCCI     =076140
192      076141      SKPCI     =076141
  
```

193	076142	SCANDI	=076142
194	076143	SPANDI	=076143
195	076144	CMPCI	=076144
196	076145	MATCI	=076145
197	076150	ADDNI	=076150
198	076151	SUBNI	=076151
199	076152	CMPNI	=076152
200	076153	CVTNLI	=076153
201	076154	CVTPNI	=076154
202	076155	CVINPI	=076155
203	076156	ASHNI	=076156
204	076157	CVFLNI	=076157
205	076170	ADDP1	=076170
206	076171	SUBP1	=076171
207	076172	CMPP1	=076172
208	076173	CVTPL1	=076173
209	076174	MULP1	=076174
210	076175	DIVP1	=076175
211	076176	ASHP1	=076176
212	076177	CVTLP1	=076177
213	076600	MED6X	=076600
214	076601	MED74C	=076601
215	177776	PSW	=177776

```

240 ;*****
241 BEGIN:
242 000000 103 111 101 MODNAM: .ASCII /CIAA / ;MODULE NAME.
243 000005 000 XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WRUFF USAGE
244 000006 000000 ADDR: 0 ;1ST DEVICE ADDR.
245 000010 000000 VECTOR: 0 ;1ST DEVICE VECTOR.
246 000012 000 BR1: .BYTE PRTY0 ;1ST BR LEVEL.
247 000013 000 BR2: .BYTE PRTY0 ;2ND BR LEVEL.
248 000014 000002 DVID: 2 ;DEVICE INDICATOR 1.
249 000016 000000 SR1: OPEN ;SWITCH REGISTER 1
250 000020 000000 SR2: OPEN ;SWITCH REGISTER 2
251 000022 000000 SR3: OPEN ;SWITCH REGISTER 3
252 000024 000000 SR4: OPEN ;SWITCH REGISTER 4
253 ;*****
254 000026 040020 STAT: 40020 ;STATUS WORD.
255 000030 000234' INIT: START ;MODULE START ADDR.
256 000032 000234' SPOINT: MODDSP ;MODULE STACK POINTER.
257 000034 000000 PASCNT: 0 ;PASS COUNTER.
258 000036 000200 ICOUNT: 200 ;# OF ITERATIONS PER PASS=200
259 000040 000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
260 000042 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
261 000044 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
262 000046 000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
263 000050 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
264 000052 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
265 000054 000000 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
266 000056 000000 CONFIG: 0 ;RESERVED FOR MONITOR USE
267 000058 000000 RES1: 0 ;RESERVED FOR MONITOR USE
268 000060 000000 RES2: 0 ;RESERVED FOR MONITOR USE
269 000062 000000 SVR0: OPEN ;LOC TO SAVE R0.
270 000064 000000 SVR1: OPEN ;LOC TO SAVE R1.
271 000066 000000 SVR2: OPEN ;LOC TO SAVE R2.
272 000070 000000 SVR3: OPEN ;LOC TO SAVE R3.
273 000072 000000 SVR4: OPEN ;LOC TO SAVE R4.
274 000074 000000 SVR5: OPEN ;LOC TO SAVE R5.
275 000076 000000 SVR6: OPEN ;LOC TO SAVE R6.
276 000100 000000 CSRA: OPEN ;ADDR OF CURRENT CSW.
277 000102 000000 SBADR: ;ADDR OF GOOD DATA, OR
278 000102 000000 ACSR: OPEN ;CONTENTS OF CSW.
279 000104 000000 WASADR: ;ADDR OF BAD DATA, OR
280 000104 000000 ASTAT: OPEN ;STATUS REG CONTENTS.
281 000106 000000 ERRTYP: ;TYPE OF ERROR
282 000106 000000 ASB: OPEN ;EXPECTED DATA.
283 000110 000000 AAS: OPEN ;ACTUAL DATA.
284 000112 000234' RSTR: RESTRT ;RESTART ADDRESS AFTER END OF PASS
285 000114 000000 WDFO: OPEN ;WORDS TO MEMORY PER ITERATION
286 000116 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
287 000120 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
288 000122 000001 IDNUM: 1 ;MODULE IDENTIFICATION NUMBER=1
289 000124 000000 RES3: OPEN
290 000126 000000 RES4: OPEN
291 000130 000000 RES5: OPEN
292 000132 000000 RES6: OPEN
293 000040 .REPT SPSIZ ;MODULE STACK STARTS HERE.
298 000234 MODDSP:
299 ;*****
    
```

```

304 ;*****
305 ;START - GET RANDOM DATA
306 ;*****
307
308 000234 START:
309 000234 012706 003676' RESTRI: MOV #STACK,SP ;SET STACK POINTER TO MODULE STACK
310 000240 012700 003700' MOV #BUFF,R0 ;SET SOURCE ADDRESS FOR DATA
311 000244 012701 004520' MOV #BUF200,R1 ;SET DST ADDRESS FOR DATA
312 000250 012702 000310 MOV #200.,R2 ;GET 200 DECIMAL RANDOM NUMBERS
313 000254 005037 000102' CLR ACSR ;CLEAR STATUS REG ADDRESS
314 000260 104417 000000' 18: RANDS ,BEGIN ;GO GET RANDOM NUMBER
315 000264 013710 000054' MOV RANNUM,(R0) ;GET THE RANDOM NUMBER PRODUCED
316 000270 042710 100200 BIC #100200,(R0) ;CLEAR HIGH BIT OF EACH BYTE
317 000274 012021 MOV (R0)+,(R1)+ ;MOVE TO DEST FOR COMPARE
318 000276 077210 SUB R2,18 ;IF NOT DUNK, GET NEXT RANDOM NUMBER
319 000300 042737 000400 177776 BIC #BIT8,PSW ;CLEAR BIT 8 IN CASE MONITOR SET IT
  
```

```

320 ;SBTTL TEST CMPC INSTRUCTION
321 ;*****
322 ;TEST CMPC INSTRUCTION
323 ;COMPARE SRC 1 TO DST 1
324 ;*****
325
326 000306 004737 002412' COMP: JSR PC,SETUP ;GO SET UP THE DESCRIPTORS
327 000312 076144 002770' 002774' CMPC1 ,SRC.10,DST.10,' ;COMPARE STRINGS, FILL WITH SPACES
328 000322 001410 BEQ MOVE ;NO ERROR GO TO NEXT TEST
329 000324 004737 002444' JSR PC,ERROR0 ;GET ERROR DATA
330 000330 002772' 002776' .WORD SRC.1A,DST.1A ;PARAMETERS FOR GETTING ERROR DATA
331 000334 104404 000000' DATERS ,BEGIN ;***>>>>DATA ERROR*<<<<*<*<
      000340 104410 000000' ENDS ,BEGIN ;**FATAL ERROR** CANNOT CONTINUE MODULE
  
```

```
332 .SBTTL MOVE CHARACTER STRING
333 ;*****
334 ;MOVE CHARACTER STRING
335 ;*****
336 000344 004737 002412' MOVE: JSR PC,SETUP ;GO SET UP THE DESCRIPTORS
337 000350 076067 002770' 002774' L3DT ,SRC.1D,DST.1D,CHAR ;LOAD DESC INTO REG
338 000360 076030 MOVVC ;MOVE STRING
339 000362 076144 002770' 002774' CMPCI ,SRC.1D,DST.1D,' ;COMPARE SRC AND DST
340 000372 001411 BEQ SCAN ;IF EQUAL NEXT TEST
341 000374 004737 002444' JSR PC,ERROR0 ;GET ERROR DATA
342 000400 002772' 002776' .WORD SRC.1A,DST.1A ;PARAMETERS FOR GETTING ERROR DATA
343 000404 104404 000000' DATERS ,BEGIN ;**>>>>>>DATA ERROR**<<<<<<
344 000410 042737 000400 17777b BIC #BIT8,PSW ;CLEAR BIT 8 IN CASE ERROR ROUTINE SET IT
```

```
344 .SBTTL SCAN,MOVVC
345 ;*****
346 ;SCAN,MOVVC
347 ;*****
348
349 000416 004737 002412' SCAN: JSR PC,SETUP ;GO SET UP THE DESCRIPTORS
350 000422 112737 000001 003034' MOVVB ,#1,SET.1D ;SET CHAR MASK FOR SPAN AND SCAN
351 000430 076142 002770' 003034' 18: SCANCL ,SRC.1D,SET.1D ;SCAN
352 000436 001003 BNE Z8 ;CHAR FOUND MOVE STRING
353 000440 106337 003034' ASLB SET.1D ;NOT FOUND SHIFT MASK
354 000444 000771 BR 18 ;LOOK AGAIN
355 000446 010037 002770' 28: MOV R0,SRC.1D ;MOV NEW ADDRESS TO DESC
356 000452 010137 002772' MOV R1,SRC.1A ;MOV NEW LENGTH TO DESC
357 000456 076130 002770' 002774' MOVCI ,SRC.1D,DST.1D,' ;MOV TEXT STARTING WITH CHAR FOUND
358 000466 076144 002770' 002774' CMPCI ,SRC.1D,DST.1D,' ;COMPARE SRC AND DST
359 000476 001411 BEQ SPAN ;STRINGS EQUAL NEXT TEST
360 000500 004737 002444' JSR PC,ERROR0 ;NOT EQUAL GET ERROR DATA
361 000504 002772' 002776' .WORD SRC.1A,DST.1A ;PARAMETERS FOR GETTING ERROR DATA
362 000510 104404 000000' DATERS ,BEGIN ;**>>>>>>DATA ERROR**<<<<<<
363 000514 042737 000400 17777b BIC #BIT8,PSW ;CLEAR BIT 8 IN CASE ERROR ROUTINE SET IT
```

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```

385                                     ,SBTTL MATCH
386                                     ;*****
387                                     ;MATCH
388                                     ;*****
389
390 000636 004737 002412' MATCH: JSR PC,SETUP ;GO SET UP THE DESCRIPTORS
391 000642 076027 002770' 003000' L2D7 ;SRC.ID,OBJ.ID ;LOAD DESC INTO REGISTERS, SOURCE & OBJECT PTRS
392 000650 076045 MATC ;MATCH STRINGS
393 000652 010137 002772' MOV R1,SRC.1A ;GET NEW SRC ADDRESS
394 000656 012737 000031 002770' MOV #25,,SRC.ID ;GET NEW SRC LENGTH
395 000664 076067 002770' 003000' L3D7 ;SRC.ID,OBJ.ID,CHAR ;LOAD DESCRIPTORS
396 000674 076044 CMPC ;COMPARE RESULTS
397 000676 001411 BEQ ;FOUND NEXT TEST
398 000700 004737 002444' JSR PC,ERRNOU ;ERROR DATA
399 000704 002772' 002776' .WORD SRC.1A,DST.1A ;PARAMETERS FOR GETTING ERROR DATA
400 000710 104404 000000' DATERS ;*****DATA ERROR<<<*****
000714 042737 000400 177776 BIC #BIT8,PSW ;CLEAR BIT 8 IN CASE ERROR ROUTINE SET IT

```



```

401 .SBTTL MOVE REVERSE
402 ;*****
403 ;MOVE REVERSE
404 ;*****
405
406 000722 004737 002412' MOVER: JSR PC,SETUP ;GO SET UP THE DESCRIPTORS
407 000726 076131 002770' 002774' MOVRCI ,SRC.1D,DST.1D,' ;MOVE THE STRING REVERSED
408 000736 076067 002774' 002774' L3D7 ,DST.1D,DST.1D,CHAR ;LOAD DESCRIPTORS
409 000746 076031 002770' 002774' MOVRC ,SRC.1D,DST.1D,CHAR ;MOVE REVERSE AGAIN
410 000750 076067 002770' 002774' L3D7 ,SRC.1D,DST.1D,CHAR ;LOAD DESCRIPTORS
411 000760 076044 001411 001411 C&PC ;COMPARE RESULTS
412 000762 001411 001411 HEW MOVF ;FOUND NEXT TEST
413 000764 004737 002444' JSR PC,ERROR0 ;ERROR DATA
414 000770 002772' 002776' .WORD SRC.1A,DST.1A ;PARAMETERS FOR GETTING ERROR DATA
415 000774 104404 000000' DATERS ,BEGIN ;**>>>>*DATA ERROR**<<<<*
      001000 042737 000400 177776 BIC #BIT8,PSW ;CLEAR BIT 8 IN CASE ERROR ROUTINE SET IT

```

```

416 .SBTTL MOVE TRANSLATE
417 ;*****
418 ;MOVE TRANSLATE
419 ;*****
420
421 001006 004737 002412' MOVT: JSR PC,SETUP ;GO SET UP THE DESCRIPTORS
422 001012 076132 002770' 002774' MOVTCI ,SRC.1D,DST.1D,' ,TRANS ;MOVE TRANSLATE, FILL WITH SPACES, TRANS TABLE ADRS
423 001024 076132 002774' 002774' MOVTCI ,DST.1D,DST.1D,' ,TRANS ;MOVE TEXT AGAIN SHOULD BE SAME AS SOURCE
424 001036 076144 002770' 002774' CNPCI ,SRC.1D,DST.1D,' ;COMPARE SOURCE AND DESTINATIONS
425 001046 001411 001411 BEQ LOCATE ;IF OK, GO TO NEXT CHECK
426 001050 004737 002444' JSR PC,ERROR0 ;GET ERROR DATA
427 001054 002772' 002776' .WORD SRC.1A,DST.1A ;PARAMETERS FOR GETTING ERROR DATA
428 001060 104404 000000' DATERS ,BEGIN ;**>>>>*DATA ERROR**<<<<*
      001064 042737 000400 177776 BIC #BIT8,PSW ;CLEAR BIT 8 IN CASE ERROR ROUTINE SET IT

```

```
429          ,SBITL LOCATE AND MOVE CHARACTER
430          ;*****
431          ;LOCATE AND MOVE CHARACTER
432          ;*****
433
434 001072 004737 002412' LOCATE: JSR PC,SETUP          ;GO SET UP THE DESCRIPTORS
435 001076 012737 000040 001110'      MOV      #',15+4      ;RESET ASCII SPACE CHARACTER IN LOCATION BELOW
436 001104 076140 002770' 000040 15: LOCCI      ,SRC.1D,'      ;LOCATE CHARACTER
437 001112 001003          BNE      ZS          ;FOUND MOVE STRING
438 001114 105237 001110'          INCB      15+4      ;NOT FOUND INC CHAR FOR SEARCH
439 001120 000771          RR          15          ;LOOK AGAIN FOR NEW CHAR
440 001122 010037 002770'          25: MOV      R0,SRC.1D      ;ADDRESS OF CHAR FOUND TO SCR.1
441 001126 010137 002772'          MOV      R1,SRC.1A      ;LENGTH OF STRING
442 001132 013703 002776'          MOV      DST.1A,R3      ;MOVE DST ADDRESS TO R2
443 001136 013702 002774'          MOV      DST.1D,R2      ;MOVE STRING LENGTH TO R3
444 001142 012704 000040          MOV      #',R4          ;FILL CHAR
445 001146 076030          MOVC      ,MOVE STRING BEGINING WITH CHAR FOUND
446 001150 076144 002770' 002774' CMPC      ,SRC.1D,DST.1D,' ;COMPARE SOURCE AND DEST
447 001160 001411          BEQ      SKIP          ;STRINGS EQUAL NEXT TEST
448 001162 004737 002444'          JSR      PC,ERRORR      ;NOT EQUAL ERROR
449 001166 002772' 002776'          .WORD   SRC.1A,DST.1A    ;PARAMETERS FOR GETTING ERROR DATA
450 001172 104404 000000'          DATERS  ,BEGIN          ;**>*>*>*DATA ERROR*<*<*<*<
      001176 042737 000400 177776      BIC      #BIT8,PSW      ;CLEAR BIT 8 IN CASE ERROR ROUTINE SET IT
```

```
451          ,SBITL SKIP AND MOVE CHAR STRING
452          ;*****
453          ;SKIP AND MOVE CHAR STRING
454          ;*****
455
456 001204 004737 002412' SKIP: JSR PC,SETUP          ;GO SET UP THE DESCRIPTORS
457 001210 012737 000040 001222'      MOV      #',15+4      ;RESET ASCII SPACE CHARACTER IN LOCATION BELOW
458 001216 076141 002770' 000040 15: SKPCI      ,SRC.1D,'      ;SKIP CHAR
459 001224 001003          BNF      ZS          ;FOUND A CHAR CHECK IT
460 001226 105237 001222'          INCB      15+4      ;NOT FOUND INC CHAR
461 001232 000771          BR          15          ;LOOK AGAIN
462 001234 010037 002770'          25: MOV      R0,SRC.1D      ;GET NEW SRC ADDRESS
463 001240 010137 002772'          MOV      R1,SRC.1A      ;NEW SOURCE LENGTH
464 001244 076130 002770' 002774'      MOVC      ,SRC.1D,DST.1D,' ;MOVE STRING
465 001254 076067 002770' 002774'      L3D7      ,SRC.1D,DST.1D,CHAR ;LOAD DESCRIPTORS FOR COMPARE
466 001264 076044          CMPC          ;COMPARE STRINGS
467 001266 001411          BEQ      DECDAT          ;IF EQUAL, GO TO NEXT TEST
468 001270 004737 002444'          JSR      PC,ERRORR      ;NOT EQUAL GET ERROR DATA
469 001274 002772' 002776'          .WORD   SRC.1A,DST.1A    ;PARAMETERS FOR GETTING ERROR DATA
470 001300 104404 000000'          DATERS  ,BEGIN          ;**>*>*>*DATA ERROR*<*<*<*<
      001304 042737 000400 177776      BIC      #BIT8,PSW      ;CLEAR BIT 8 IN CASE ERROR ROUTINE SET IT
```

```

471          .SBTTL  DECIMAL ARITHMETIC TESTS
472          ;*****
473          ;DECIMAL ARITHMETIC TESTS
474          ;SETUP DECIMAL DATA
475          ;*****
476
477 001312 004737 002412'  DECDAT: JSR    PC,SETUP          ;GU SET UP THE DESCRIPTORS
478 001316 005000          CLR    R0          ;CLR R0 FOR COUNTER
479 001320 005002          18:   CLR    R2          ;CLEAR R2, ACCUMULATE NIPPLES
480 001322 012704 002734'   MOV    #MSKTAB,R4      ;MOV MASK TABLE ADDRESS TO R4
481 001326 012703 002744'   MOV    #NINTAB,R3      ;MOV NINE TABLE ADDRESS TO R3
482 001332 012705 002754'   MOV    #SIXTAB,R5      ;MOV SIX TABLE ADDRESS TO R5
483 001336 016001 003700'   26:   MOV    BUFF(R0),R1      ;GET DATA TO CONVERT
484 001342 042401          BIC    (R4)+,R1      ;CLEAN ALL NIBBLES EXCEPT ONE
485 001344 022301          CMP    (R3)+,R1      ;NIBBLE LESS THAN 9 (HEX)
486 001346 002001          BGE    J8          ;YES, CHECK NEXT NIBBLE
487 001350 161501          SUB    (R5),R1      ;NO, SUBTRACT SIX, MAKE SURE R1< 9
488 001352 005725          36:   TST    (R5)+      ;INC R5 BY 2 FOR TABLE ENTRY
489 001354 050102          BIS    R1,R2          ;SAVE NIBBLE IN R2
490 001356 020327 002754'   CMP    R3,#SIXTAB      ;DONE WITH ALL NIBBLES
491 001362 002765          HLT    J6          ;NO DO NEXT NIBBLE
492 001364 010260 003700'   MOV    R2,BUFF(R0)      ;STORE NUMBER IN SOURCE FIELD
493 001370 005720          TST    (R0)+      ;INC R0, INDEX FOR BUFF
494 001372 020027 000070   CMP    R0,#56.      ;DONE WITH ALL WORDS
495 001376 003750          BLE    J8          ;IF NOT, GET NEXT WORD, OTHERWISE MODIFY DATA TYPE
496 001400 012701 003004'   MOV    #A.DSC,R1      ;SET DATA TYPE
497 001404 042711 070000   46:   BIC    #070000,(R1)      ;CLEAR TYPE BITS
498 001410 052711 010000   BIS    #010000,(R1)      ;MAKE UNSIGNED ZONED DATA
499 001414 062701 000004   ADD    #4,R1          ;GET NEXT DATA TYPE SPECIFIER
500 001420 020127 003030'   CMP    R1,#D.DSC      ;TEST FOR DONE
501 001424 101767          BLUS    J6          ;NOT DONE DO AGAIN

```

```

502          .SBTTL  TEST COMPARE NUMERIC
503          ;*****
504          ;TEST COMPARE NUMERIC
505          ;*****
506
507 001426 004737 002412'  CHPNUM: JSR    PC,SETUP          ;GU SET UP THE DESCRIPTORS
508 001432 076152 003004' 003004'  CMPNI    ,A.DSC,A.DSC      ;COMPARE EQUAL STRINGS
509 001440 001410          BEQ    NUMRIC          ;EQUAL NEXT TEST
510 001442 004737 002444'   JSR    PC,ERRORDO      ;GET ERROR DATA
511 001446 003006' 003006'   .WORD    A,A          ;PARAMETERS FOR EXPECTED AND RECEIVED DATA
512 001452 104404 000000'   DATERS  ,BEGIN      ;**FATAL ERROR** CANNOT CONTINUE MODULE
                    ENDS    ,BEGIN

```

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```

546          .SBTTL CONVERT DATA TYPES
547          ;*****
548          ;CONVERT DATA TYPES
549          ;LONG -> NUMERIC -> LONG
550          ;NUMERIC -> PACKED -> NUMERIC
551          ;*****
552
553 001630 004737 002412'          CONNUM: JSR      PC,SETUP          ;GO SET UP THE DESCRIPTORS
554 001634 076157 003020' 003040'  CIVLNI    ,E.DSC,LONG,1      ;CONVERT LONG TO NUMERIC
555 001642 076153 003020' 003044'  CIVNLI    ,E.DSC,LONG,2      ;CONVERT NUMERIC TO LONG
556 001650 023737 003040' 003044'  CMP        LONG,1,LONG,2      ;CHECK FIRST HALF $LONG WORD
557 001656 001004                      BNE        1$              ;NOT EQUAL ERROR
558 001660 023737 003042' 003046'  CMP        LONG,1+2,LONG,2+2    ;EQUAL CHECK SECOND HALF
559 001666 001407                      BEQ        NUMPAC          ;EQUAL NEXT TEST
560 001670 004737 002502'          1$: JSR      PC,ERRHOW1      ;GET ERROR DATA
561 001674 104404 000000'          DATERS    ,BEGIN
562 001700 023737 000400 177776  BIC        $BIT8,PSW      ;CLEAR BIT 8 IN CASE ERROR ROUTINE SET IT

```

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PACKED DECIMAL ARITHMETIC

SEQUENCE 22

```

574                                     .SBTTL  PACKED DECIMAL ARITHMETIC
575                                     *****
576                                     ;PACKED DECIMAL ARITHMETIC
577                                     *****
578
579 001770 004737 002412'          PACDAR: JSR      PC,SETUP          ;GO SET UP THE DESCRIPTORS
580 001774 012701 003004'          MOV      #A,USC,R1              ;SET DATA TYPE
581 002000 004711 070000          BIC      070000,(R1)            ;CLEAR DATA TYPE BITS
582 002004 052711 060000          BIST     060000,(R1)            ;MAKE SIGNED PACKED DATA
583 002010 062701 000004          ADD      #4,R1                  ;NEXT DATA TYPE SPEC
584 002014 020127 003030'          CMP      R1,#D,USC             ;DOONE YET
585 002020 101767                  BLOS     IS                      ;NO DO AGAIN
586 002022 143777 002764' 000756  R1CB  HIMASK,#A              ;CLEAR HI NIBBLE OF A
587 002030 143777 002764' 000754  R1CB  HIMASK,#B              ;CLEAR HI NIBBLE OF B
588 002036 143777 002764' 000752  R1CB  HIMASK,#C              ;CLEAR HI NIBBLE OF C
589 002044 143737 002765' 003716  R1CB  LUMASK,BUFF+14.         ;CLEAR SIGN NIBBLE A
590 002052 153737 002766' 003716  R1SH  SIGN,BUFF+14.           ;SET SIGN NIBBLE A
591 002060 143737 002765' 003743  R1CB  LUMASK,BUFF+35.          ;CLEAR SIGN B
592 002066 153737 002766' 003743  R1SH  SIGN,BUFF+35.           ;SET SIGN B
593 002074 143737 002765' 003757  R1CB  LUMASK,BUFF+47.          ;CLEAR SIGN NIBBLE C
594 002102 153737 002766' 003757  R1SH  SIGN,BUFF+47.           ;SET SIGN C

```

```
595 .SRTTL TEST COMPARE PACKED
596 ;*****
597 ;TEST COMPARE PACKED
598 ;*****
599
600 002110 004737 002412' CMPPAK: JSR PC,SETUP ;GO SET UP THE DESCRIPTORS
601 002114 076172 003004' 003004' CMPP1 ,A.DSC,A.DSC ;COMPARE EQUAL STRINGS
602 002122 001410 BEW PACKED ;EQUAL GO TEST
603 002124 004737 002444' JSR PC,ERRORD ;GET ERROR DATA
604 002130 003006' 003006' .WORD A,A ;PARAMETERS FOR EXPECTED AND RECEIVED DATA
605 002134 104404 000000' DATERS ,BEGIN ;**>>>>>>DATA ERROR**<<<<<<<<
002140 104410 000000' ENDS ,BEGIN ;**FATAL ERROR** CANNOT CONTINUE MODULE
```

```
606 .SRTTL CALCULATE 10*((B+C))=10*((B**2)-(C**2)/(B-C))
607 ;*****
608 ;CALCULATE 10*((B+C))=10*((B**2)-(C**2)/(B-C))
609 ;*****
610
611 002144 004737 002412' PACKED: JSR PC,SETUP ;GO SET UP THE DESCRIPTORS
612 002150 076174 003010' 003010' 1$ MULP1 ,B.DSC,B.DSC,D.DSC ;MULTIPLY B*B, PUT RESULTS IN D
613 002160 076174 003014' 003014' MULP1 ,C.DSC,C.DSC,E.DSC ;MULTIPLY C*C, PUT RESULTS IN E
614 002170 076171 003020' 003030' SUBP1 ,E.DSC,D.DSC,E.DSC ;SUBTRACT E FROM D, ((B**2)-(C**2)) RESULTS IN E
615 002200 076171 003014' 003010' SUBP1 ,C.DSC,H.DSC,F.DSC ;SUBTRACT B FROM C, RESULTS IN F
616 002210 001005 BNE Z$ ;BRANCH IF DENOMINATOR IS NON-ZERO
617 002212 076176 003024' 003014' ASHP1 ,F.DSC,C.DSC,I ;SHIFT F TO C
618 002222 000752 BR 1$ ;BRANCH BACK TO TRY OVER
619 002224 076067 003030' 003020' 2$ L3D7 ,D.DSC,E.DSC,ONE ;LOAD DESC FOR ASHP
620 002234 076175 003024' 003020' DIVP1 ,F.DSC,E.DSC,D.DSC ;DIVIDE ((B**2)-(C**2)) BY (B-C) (E / F), RESULT IN G
621 002244 076076 ASHP ;SHIFT 10*D
622 002246 076067 003010' 003014' L3D7 ,H.DSC,C.DSC,D.DSC ;LOAD DESCRIPTORS
623 002256 076070 ADDP ;ADD B AND C, RESULT IN F
624 002260 076067 003030' 003024' L3D7 ,D.DSC,F.DSC,ONE ;LOAD DESCRIPTORS
625 002270 076076 ASHP ;SHIFT 10*D
626
627 ;*****THE RESULTS OF THE LEFT HALF OF THE EQUATION IS NOW IN D - COMPARE RESULTS
628 002272 076172 003020' 003024' CMPP1 ,E.DSC,F.DSC ;DOES THE EQUATION BALANCE (LEFT=RIGHT)
629 002300 001411 BEW CUNPAK ;BRANCH TO NEXT SECTION IF OK
630 002302 004737 002444' JSR PC,ERRORD ;GET ERROR DATA
631 002306 003022' 003026' .WORD E,F ;PARAMETERS FOR EXPECTED AND RECEIVED DATA
632 002312 104404 000000' DATERS ,BEGIN ;**>>>>>>DATA ERROR**<<<<<<<<
002316 042737 000400 177776 BIC ,B1TH,PSW ;CLEAR BIT 8 IN CASE ERROR ROUTINE SET IT
```

```

633      .SBTTL CONVERT DATA TYPES
634      ;*****
635      ;CONVERT DATA TYPES
636      ;LONG -> PACKED -> LONG
637      ;*****
638
639 002324 004737 002412' CONPAK: JSK PC,SETUP ;GO SET UP THE DESCRIPTORS
640 002330 076177 003020' 003040' CVTLP1 ,E,USC,LONG.1 ;CONVERT LONG TO PACKED
641 002336 076173 003020' 003044' CVTLP1 ,E,USC,LONG.2 ;CONVERT PACKED TO LONG
642 002344 023737 003040' 003044' CMP LONG.1,LONG.2 ;COMPARE RESULTS
643 002352 001004 BNE IS ;BRANCH TO CALL ERROR IF NOT EQUAL
644 002354 023737 003042' 003046' CMP LONG.1+2,LONG.2+2 ;COMPARE RESULTS OF SECOND WORD
645 002362 001407 BEQ DONE ;BRANCH AROUND ERROR IF EQUAL
646 002363 004737 002502' 1$: JSK PC,ERRORD1 ;GET PC,ERROR DATA
647 002370 104404 000000' DATERS ,BEGIN ;>>>>>>>>DATA ERROR<<<<<<<<
        002374 042737 000400 177776 BIC *BIT0,PSW ;CLEAR BIT 0 IN CASE ERROR ROUTINE SET IT

```

```

648 002402 104413 000000' DONE: ENOTS ,BEGIN ;SIGNAL END OF ITERATION, MONITOR TO TEST END OF PASS
649 002406 000137 000234' JMP RESIKT ;RETURN TO START

```

```
650 .SBTTL SUBROUTINES
651 ;*****
652 ;SUBROUTINE-SETUP > SETS UP CHAR STRING DESCRIPTORS
653 ;USAGE: JSR PC,SETUP
654 ;*****
655
656 002412 012737 000310 002770' SETUP: MOV #200,,SRC.1D ;SET SOURCE LENGTH
657 002420 012737 003700' 002772' MOV #BUFF,SRC.1A ;SET SOURCE ADDRESS
658 002426 012737 000310 002774' MOV #200,,DST.1D ;DEST LENGTH
659 002434 012737 004520' 002776' MOV #BUF200,DST.1A ;DEST ADDRESS
660 002442 000207 RTS PC ;RETURN
```

```
661 .SBTTL SUBROUTINES-ERROR0 AND ERROR1
662 ;*****
663 ;SUBROUTINES-ERROR0 AND ERROR1
664 ;LOAD THE EXPECTED AND RECEIVED DATA AND ADDRESSES INTO THE PROPER LOCATIONS
665 ;FOR THE ERROR CALL
666 ;USAGE: JSR PC,ERROR0
667 ; .WORD ADDRESS,DATA ;LOCATIONS CONTAINING THE ADDRESS OF EXPECTED AND
668 ; RECEIVED DATA
669
670 ;FOR> JSR PC,ERROR1
671 ; NO ARGUMENTS
672 ;*****
673 002444 011600 ERROR0: MOV (SP),R0 ;LOAD ADDRESS OF FIRST PARAMETER TO RU
674 002446 011037 000102' MOV (R0),SHADR ;LOAD EXPECTED ADDRESS
675 002452 005037 000106' CLR ASB ;CLEAR EXPECTED DATA LOCATION
676 002456 113037 000106' MOVB @ (R0)+,ASB ;LOAD EXPECTED DATA LOCATION BYTE
677 002462 011037 000104' MOV (R0),^ASADR ;LOAD RECEIVED ADDRESS
678 002466 005037 000110' CLR ^AS ;CLEAR RECEIVED DATA LOCATION
679 002472 113037 000110' MOVB @ (R0)+,^AS ;LOAD RECEIVED DATA
680 002476 010016 MOV R0,(SP) ;FUDGE RETURN OVER PARAMETERS
681 002500 000207 RTS PC ;RETURN
682
683 002502 012737 003040' 000102' ERROR1: MOV #LUNG.1,SBADR ;LOAD EXPECTED ADDRESS
684 002510 013737 003040' 000106' MOV LUNG.1,ASB ;LOAD EXPECTED DATA
685 002516 012737 003044' 000104' MOV #LUNG.2,WASADR ;LOAD RECEIVED ADDRESS
686 002524 013737 003044' 000110' MOV LUNG.2,^AS ;LOAD RECEIVED DATA
687 002532 000207 RTS PC ;RETURN
```



```
688          .SBTTL DATA TABLES
689          ;*****
690          ;TRANSLATE AND CHARACTER ATTRIBUTE TABLE
691          ;USED BY MOVE TRANSLATE
692          ;USED BY SPAN AND SCAN INSTRUCTIONS
693          ;128 CHAR ASCII
694          ;*****
695
696 002534      177      176      175      TRANS: .BYTE 177,176,175,174,173,172,171,170,167,166,165,164,163,162,161,160
697 002554      157      156      155          .BYTE 157,156,155,154,153,152,151,150,147,146,145,144,143,142,141,140
698 002574      147      136      135          .BYTE 137,136,135,134,133,132,131,130,127,126,125,124,123,122,121,120
699 002614      117      116      115          .BYTE 117,116,115,114,113,112,111,110,107,106,105,104,103,102,101,100
700 002634      077      076      075          .BYTE 077,076,075,074,073,072,071,070,067,066,065,064,063,062,061,060
701 002654      057      056      055          .BYTE 057,056,055,054,053,052,051,050,047,046,045,044,043,042,041,040
702 002674      037      036      035          .BYTE 037,036,035,034,033,032,031,030,027,026,025,024,023,022,021,020
703 002714      017      016      015          .BYTE 017,016,015,014,013,012,011,010,007,006,005,004,003,002,001,000
```

```
704          ;*****
705          ;MASK TABLES
706          ;FOR MAKING VALID DECIMAL DATA
707          ;*****
708
709 002734      177700      177417      170377      MSKTAB: .WORD 177700,177417,170377,007777
710 002744      000011      000220      004400      NINTAB: .WORD 000011,000220,004400,070000
711 002754      000006      000140      003000      SIXTAB: .WORD 000006,000140,003000,100000
712 002764      360          HIMASK: .BYTE 360
713 002765      017          LOWMASK: .BYTE 017
714 002766      014          SIGN: .BYTE 014
715          .EVEN
716
717          ;CHARACTER STRING DESCRIPTOR TABLE
718
719 002770      000310      SRC.ID: .WORD 200.
720 002772      003700      SRC.1A: .WORD 00FF
721 002774      000310      DST.ID: .WORD 200.
722 002776      004520      DST.1A: .WORD 00FF200
723 003000      000031      003762      OBJ.ID: .WORD 25.,00FF+50.
724 003004      000034      A.DSC: .WORD 28.
725 003006      003700      A: .WORD 00FF
726 003010      000016      B.DSC: .WORD 14.
727 003012      003734      B: .WORD 00FF+28.
728 003014      000012      C.DSC: .WORD 10.
729 003016      003752      C: .WORD 00FF+42.
730 003020      000037      E.DSC: .WORD 31.
731 003022      003770      E: .WORD 00FF+56.
732 003024      000037      F.DSC: .WORD 31.
733 003026      004030      F: .WORD 00FF+88.
734 003030      000037      D.DSC: .WORD 31.
735 003032      004070      D: .WORD 00FF+120.
736 003034      000001      002534      SET.ID: .WORD 1,TRANS
737 003040      001020      000000      LONG.1: .WORD 000520.,0
738 003044          .BLKW 2
739 003050      000001      ONE: .WORD 1
740 003052      000040      002534      CHAR: .WORD 1,TRANS
741 003056          .BLKW 200.
742 003676      000000      STACK: .WORD 0
743          ;*****
744
745          ;BUFFER SPACE 200 WORDS LONG USED FOR SOURCE AND DESTINATIONS
746          ;*****
747
748 003700          .BLKW 200.
749 004520      BUF200: .BLKW 200.
750 005340      000000      ENDALL: .WORD 0
751      000001          .END
```

```

A      003006R      C4PN = 076052      HKIPAS 000050R      MSKTAB 002734R      R6      =0000006
ACSR   000102R      C4PNI = 076152      ICUNI1 000036R      MULP   = 076074      R7      =0000007
ADDN   = 076050      CMPNUM 001426R      ICOUNT 000040R      MULP1  = 076174      SBADK   000102R
ADDNI  = 076150      C4PP  = 076072      IDNUM  000122H      NCPUDP= 000020      SCAN    000416R
ADDP   = 076070      CMPPAK 002110R      INDPAR= 000040      NINTAB 002744R      SCANC   = 076042
ADDP1  = 076170      CMPPI  = 076172      INIT   000030R      NUAPTY= 000002      SCANCI= 076142
ADDR   000006R      CUMP   000306R      INTR   000120H      NULL   = 000000      SETUP   002412R
ADDR22= 001000      CONFIG 000056R      KIPRES= 000400      NUMPAC 001706R      SET.1D 003034R
APTPRE= 000200      CONNUM 001630R      KXTIND= 040000      NUMRIC 001402R      SIGN    002766R
ASB     000106R      CONPAK 002324R      LOCATE 001072H      OBJ.1D 003000H      SIXTAB  002754R
ASHN    = 076056      CSRA    000100R      LOCC   = 076040      ONE     003050H      SKIP    001204R
ASHNI   = 076156      CVTLN   = 076057      LUCCI  = 076140      UPEN    = 000000      SKPC    = 076041
ASHP    = 076076      CVTLNI  = 076157      LUMASK 002765H      UTUAS   = 104420      SKPC1   = 076141
ASHP1   = 076176      CVTLP   = 076077      LONG.1 003040R      PACDAT  001770R      SOFCNT  000042R
ASTAT   000104R      CVTLPI  = 076177      LONG.2 003044R      PACKED  002144R      SOFES   = 104406
AUTO    = 000010      CVTNL   = 076053      L200   = 076020      PAKPRE  = 002000      SOFPAS  000046R
AWAS    000110R      CVTNLI  = 076153      L201   = 076021      PASCNT  000034H      SPAN    000522H
AASC    003004R      CVTNLP  = 076055      L202   = 076022      PDPI11= 000002      SPANC   = 076043
B        003012R      CVTNPI  = 076155      L203   = 076023      PDPLS1= 020000      SPANCI= 076143
BEGIN   000000H      CVTLP   = 076073      L204   = 076024      PDP44   = 100000      SPOINT  000032R
BIT0    = 000001      CVTPL   = 076173      L205   = 076025      PDP60   = 004000      SPSIZ   = 000040
BIT1    = 000002      CVTPN   = 076054      L206   = 076026      PDP70   = 010000      SKC.1A  002772H
BIT10   = 002000      CVTPNI  = 076154      L207   = 076027      PIKGS   = 000004      SRC.1D  002770R
BIT11   = 004000      C.DSC   003014R      L300   = 076060      POPSP   = 005726      SK1     000016R
BIT12   = 010000      D        003032R      L301   = 076061      POPSP2= 022626      SR2     000020R
BIT13   = 020000      DATCKS= 104411      L302   = 076062      PRHMS6= 000002      SK3     000022R
BIT14   = 040000      DATEKS= 104404      L303   = 076063      PRT1    = 000000      SK4     000024R
BIT15   = 100000      DECIAI  001312H      L304   = 076064      PRTY0   = 000000      STACK   003676R
BIT2    = 000004      DIVP    = 076075      L305   = 076065      PRTY1   = 000040      START   000234R
BIT3    = 000010      DIVPI   = 076175      L306   = 076066      PRTY2   = 000100      STAF    000026H
BIT4    = 000020      DONE    002402R      L307   = 076067      PRTY3   = 000140      STKLIM  003056R
BIT5    = 000040      DST.1A  002776H      MAP225= 104416      PRTY4   = 000200      SUBN    = 076051
BIT6    = 000100      DST.1D  002774R      MATC    = 076045      PRTY5   = 000240      SUBN1   = 076151
BIT7    = 000200      DIVID1  000014R      MATCH   000636H      PRTY6   = 000300      SUBP    = 076071
BIT8    = 000400      D.DSC   003030R      MATC1   = 076145      PRTY7   = 000340      SUBP1   = 076171
BIT9    = 001000      E        003022R      MED6X   = 076000      PS       = 177776      SVR0    000062R
BREAKS= 104407      ECCMEM= 000100      MED74C= 076001      PS*     = 177776      SVR1    000064R
BR1     000012R      ENDALL  005340R      MFPI    = 076010      PUSH    = 005746      SVR2    000066R
BR2     000013R      ENDITS= 104413      MODNAM  000000R      PUSH2   = 024646      SVK3    000070R
BTDS    = 104421      ENDS    = 104410      MODSP   000234R      FARFLG= 000002      SVK4    000072R
BUFF    003700R      ERROR0  002444H      MOV     = 076030      QMON22= 000010      SVK5    000074R
BUF200  004520R      ERROR1  002502R      MOVCI   = 076130      RANDB   = 104417      SVR6    000076R
B.DSC   003010R      ERRTYP  000106R      MOVE    000344H      RANNUM  000054R      SYSCNT  000052R
C        003016R      EXITS   = 104400      MOVER   000722H      RESTRT  000234R      TRANS   002534R
CAPRES= 000004      F        003020R      MOVRC   = 076031      RES1    000056H      TRPFD   000023
CDATAS= 104412      F.DSC   003026R      MOVTC   = 076131      RES2    000060R      USTACK= 000001
CHAR     003052H      F.DSC   003024H      MOVTC   = 001006H      RES3    000124R      VECTOR  000010K
CKHNGS= 000001      GEPAS   = 104415      MOVVIC  = 076034      RES4    000126R      WASADR  000104R
CLKPRE= 000001      GMBUFS= 104414      MSG45   = 104403      RES5    000130R      WDFR    000116K
CLKSPS= 104422      HIMASK  002764R      MSGS    = 104402      RES6    000132R      WDTU    000114K
CMPC    = 076044      HRDCNT  000044H      MSGS    = 104401      RSTRT   000112H      XFLAG   000005R
CMPC1   = 076144      HDRRS   = 104405      MSGS    = 104401

```

. AHS. 000000 000
005342 001
ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 6766 WORDS (27 PAGES)

DYNAMIC MEMORY: 7362 WORDS (28 PAGES)
ELAPSED TIME: 00:00:25
CXCIAA,CXCIAA,SEQ=SP/CR/NL:TUC=DDXCUM.P11,CXCIAA.P11

CXCIAA CREATED BY MACRO ON 14-JAN-82 AT 09:19 PAGE 3
 SEQUENCE 35
 CREF V01

SYMBOL	CROSS REFERENCE	VALUE	REFERENCES
SYMBOL			
ERRORI	002502 R		19-560 24-646 #27-683
ERRTYP	000106 R		#5-281
EXIT8	= 104400		#5-301
E.DSC	003020 R		18-524 18-525 18-527 18-540 19-554 19-555 20-567
			20-569 23-613 23-614 23-614 23-619 23-620 24-640 24-641
			#29-730
F	003026 R		18-544 20-572 23-631 #29-733
F.DSC	003024 R		18-531 18-533 18-535 18-540 #20-565 20-566 20-567 23-615 23-617
			23-620 23-624 23-628 #29-732
GETPAS	= 104415		#5-301
GWBUFF	= 104414		#5-301
HIMASK	002764 R		21-586 21-587 21-588 #29-712
HRDCNT	000044 R		#5-261
HRDEK8	= 104405		#5-301
HRDPAS	000050 R		#5-263
ICOUNT	000036 R		#5-258
ICOUNT	000040 R		#5-259
IDNUM	000122 R		#5-288
INDPAR	= 000040		#5-301
INIT	000030 R		#5-255
INTR	000120 R		#5-287
KTPRES	= 000400		#5-301
KXTEND	= 040000		#5-301
LOCATE	001072 R		13-425 #14-434
LOCC	= 076040		#4-158
LOCCI	= 076140		#4-191 14-436
LDMASK	002765 R		21-589 21-591 21-593 #29-713
LONG.1	003040 R		19-554 19-556 19-558 24-640 24-642 24-644 27-683 27-684 #29-737
LONG.2	003044 R		19-555 19-558 24-641 24-642 24-644 27-685 27-686 #29-738
L2D0	= 076020		#4-147
L2D1	= 076021		#4-148
L2D2	= 076022		#4-149
L2D3	= 076023		#4-150
L2D4	= 076024		#4-151
L2D5	= 076025		#4-152
L2D6	= 076026		#4-153
L2D7	= 076027		#4-154
L3D0	= 076060		#4-172
L3D1	= 076061		#4-173
L3D2	= 076062		#4-174
L3D3	= 076063		#4-175
L3D4	= 076064		#4-176
L3D5	= 076065		#4-177
L3D6	= 076066		#4-178
L3D7	= 076067		#4-179
			8-337 10-370 10-377 10-379 11-395 12-408 12-410 15-465
			18-533 18-535 23-619 23-622 23-624
MAP228	= 104416		#5-301
MATC	= 076045		#4-163 11-392
MATCH	000636 R		10-381 #11-390
MAICI	= 076145		#4-196
MED6X	= 076600		#4-213
MED74C	= 076601		#4-214

CXCIAA CREATED BY MACRO ON 14-JAN-82 AT 09:19 PAGE 4
 SEQUENCE 36
 CREF V01

SYMBOL	CROSS REFERENCE	VALUE	REFERENCES
SYMBOL			
MFPT	= 076010		#4-146
MUDNAM	000000 R		#5-242
MUDSP	000234 R		5-256 #5-298
MOVC	= 076030		#4-155 8-338 10-378 14-445
MOVCI	= 076130		#4-188 9-357 15-464
MOVE	000344 R		7-328 #8-336
MOVER	000722 R		11-397 #12-406
MOVRC	= 076031		#4-156 12-409
MOVRCI	= 076131		#4-189 12-407
MOVT	001006 R		12-412 #13-421
MOVIC	= 076032		#4-157
MOVTCI	= 076132		#4-190 13-422 13-423
MSGNS	= 104403		#5-301
MSGSS	= 104402		#5-301
MSG	= 104401		#5-301
MSKTAB	002734 R		16-480 #29-709
MULP	= 076074		#4-184
MULP1	= 076174		#4-209 23-612 23-613
NCPUPP	= 000020		#5-301
NINTAR	002744 R		16-481 #29-710
NOAPTY	= 000002		#5-301
NULL	= 000000		#5-301
NUNPAC	001706 R		19-559 #20-564
NUMRIC	001462 R		17-509 #18-522
OBJ.ID	003000 R		11-391 11-395 #29-723
ONE	003050 R		18-535 23-619 23-624 #29-734
OPEN	= 000000		5-243 5-249 5-250 5-251 5-252 5-269 5-270 5-271 5-272
			5-273 5-274 5-275 5-276 5-278 5-280 5-282 5-283 5-285
			5-286 5-287 5-289 5-290 5-291 5-292 #5-301
OTDAS	= 104420		#5-301
PACDAT	001770 R		20-570 #21-579
PACKED	002144 R		22-602 #23-611
PARPRE	= 002000		#5-301
PASCNT	000034 R		#5-257
PDPF11	= 000002		#5-301
PDPPLSI	= 020000		#5-301
PDP44	= 100000		#5-301
PDP60	= 004000		#5-301
PDP70	= 010000		#5-301
PIHQ6	= 000004		#5-301
POPSP	= 005726		#5-301
POPSP2	= 022626		#5-301
PRHMS	= 000002		#5-301
PRTY	= 000000		#5-301
PRTY0	= 000000		5-246 5-247 #5-301
PRTY1	= 000040		#5-301
PRTY2	= 000100		#5-301
PRTY3	= 000140		#5-301
PRTY4	= 000200		#5-301
PRTY5	= 000240		#5-301
PRTY6	= 000300		#5-301
PRTY7	= 000340		#5-301

CXCIAA CREATED BY MACRO ON 14-JAN-82 AT 09:19 PAGE 5
 SEQUENCE 37
 CREF V01

SYMBOL CROSS REFERENCE		REFERENCES											
SYMBOL	VALUE												
PS	= 177776	#5-301											
PSW	= 177776	#4-215	#5-301	#6-319	#8-343	#9-362	#10-384	#11-400	#12-415	#13-428			
		#14-450	#15-470	#16-545	#19-561	#20-573	#23-632	#24-647					
PUSH	= 005746	#5-301											
PUSH2	= 024640	#5-301											
PWRFLG	= 000002	#5-301											
QMUN22	= 000010	#5-301											
RANDS	= 104417	#5-301	6-314										
RANNUM	= 000054 R	#5-265	6-315										
RESTRT	= 000234 R	5-284	#6-309	25-649									
RES1	= 000056 R	#5-267											
RES2	= 000060 R	#5-268											
RES3	= 000124 R	#5-289											
RES4	= 000126 R	#5-290											
RES5	= 000130 R	#5-291											
RES6	= 000132 R	#5-292											
RH70	= 001000	#5-301											
RSTRI	= 000112 R	#5-284											
R6	= 0000006	#4-142	4-145	#5-301									
R7	= 0000007	#4-143	4-144	#5-301									
SBADR	= 000102 R	#5-277	#27-674	#27-683									
SCAN	= 000416 R	8-340	#9-349										
SCANC	= 076042	#4-160											
SCANCI	= 076142	#4-193	9-351										
SETUP	= 002412 R	7-326	8-336	9-349	10-368	11-390	12-406	13-421	14-434	15-456			
		16-477	17-507	18-522	19-553	20-564	21-579	22-600	23-611	24-639			
		#26-656											
SET.10	= 003034 R	#9-350	9-351	#9-353	#10-369	10-370	#10-373	#29-736					
SIGN	= 002766 R	21-590	21-592	21-594	#29-714								
SIXTAB	= 002754 R	16-442	16-490	#29-711									
SKIP	= 001204 R	14-447	#15-456										
SKPC	= 076041	#4-159											
SKPCI	= 076141	#4-192	15-458										
SOFcnt	= 000042 R	#5-260											
SOFERS	= 104406	#5-301											
SOFPAS	= 000046 R	#5-262											
SPAN	= 000522 R	9-359	#10-368										
SPANC	= 076043	#4-161	10-371										
SPANCI	= 076143	#4-194											
SPOINT	= 000032 R	#5-256											
SPSIZ	= 000040	#1-25	5-293										
SRC.1A	= 002772 R	7-330	8-342	#9-356	9-361	#10-376	10-383	#11-393	11-399	12-414			
		13-427	#14-441	14-449	#15-463	15-469	#26-657	#29-720					
SRC.1D	= 002770 R	7-327	8-337	#9-339	9-351	#9-355	9-357	9-358	10-370	#10-375			
		10-377	10-379	11-391	#11-394	11-395	12-407	12-410	13-422	13-424			
		14-436	#14-440	14-446	15-458	#15-462	15-464	15-465	#26-656	#29-719			
SK1	= 000016 R	#5-249											
SK2	= 000020 R	#5-250											
SK3	= 000022 R	#5-251											
SK4	= 000024 R	#5-252											
STACK	= 003676 R	6-309	#29-742										
STAKI	= 000234 R	5-255	#6-308										

CXCIAA CREATED BY MACRO ON 14-JAN-82 AT 09:19 PAGE 6
 SEQUENCE 38
 CREF V01

SYMBOL CROSS REFERENCE		REFERENCES										
SYMBOL	VALUE											
STAT	= 000026 R	#5-254										
STKLTH	= 003056 P	#29-741										
SUBN	= 076051	#4-165	18-532									
SUBNI	= 076151	#4-198	18-527									
SUBP	= 076071	#4-181										
SUBPI	= 076171	#4-206	23-614	23-615								
SVR0	= 000062 R	#5-269										
SVR1	= 000064 R	#5-270										
SVR2	= 000066 R	#5-271										
SVR3	= 000070 R	#5-272										
SVR4	= 000072 R	#5-273										
SVR5	= 000074 R	#5-274										
SVR6	= 000076 R	#5-275										
SYSCNT	= 000052 R	#5-264										
TRANS	= 002534 R	13-422	13-423	#28-696	29-736	29-740						
TRPDFD	= 000023	#5-301	5-301	5-301	#5-301	5-301	5-301	#5-301	5-301	5-301		
		#5-301	5-301	5-301	#5-301	5-301	5-301	#5-301	5-301	5-301		
		#5-301	5-301	5-301	#5-301	5-301	5-301	#5-301	5-301	5-301		
		#5-301	5-301	5-301	#5-301	5-301	5-301	#5-301	5-301	5-301		
		#5-301	5-301	5-301	#5-301	5-301	5-301	#5-301	5-301	5-301		
		#5-301	5-301	5-301	#5-301	5-301	5-301	#5-301	5-301	5-301		
		#5-301	5-301	5-301	#5-301	5-301	5-301	#5-301	5-301	5-301		
		#5-301	5-301	5-301	#5-301	5-301	5-301	#5-301	5-301	5-301		
USTACK	= 000001	#5-301										
VECTOR	= 000010 R	#5-245										
WASADR	= 000104 R	#5-279	#27-677	#27-685								
WDFR	= 000116 R	#5-286										
WDT0	= 000114 R	#5-285										
XFLAG	= 000005 R	#5-243										

MACRU CROSS REFERENCE

MACRU NAME	REFERENCES									
BKMOD	#1-121									
BREAK	#1-219									
BTOD	#1-238									
CKDATA	#1-274									
CLKSP	#1-146									
DATAEK	#1-283									
	#5-220	#8-343	#9-362	#10-384	#11-400	#12-415	#13-428	#14-450	#15-470	#18-545
	#19-561	#20-573	#23-632	#24-647						
DATEK	#1-172									
DFSEVN	#1-306	5-301	5-301	5-301	5-301	5-301	5-301	5-301	5-301	5-301
	5-301	5-301	5-301							
DSEVNT	#1-316	5-301	5-301	5-301	5-301	5-301	5-301	5-301		
END	#1-209									
ENDIT	#1-200									
ENDMOD	#1-205									
EQUATS	#1-322	#5-301								
EXIT	#1-154									
FATLFR	#5-225	7-331	17-512	22-605						
GETPA	#1-265									
GRUFF	#1-253									
HRDER	#1-162									
IOMOD	#1-117									
IOMODP	#1-141									
IOMODR	#1-137									
IOMODX	#1-133									
MAP22	#1-269									
MODULE	#1-26									
MSG	#1-188									
MSGN	#1-192									
MSGS	#1-196									
NBKMOD	#1-129									
OTOA	#1-224									
PIRQ	#1-213									
RAND	#1-158									
SBKMOD	#1-125									
SUPER	#1-178									
STARS	#5-217	#6-304	#8-306	#7-321	#7-324	#8-333	#8-335	#9-345	#9-347	#10-364
	#10-366	#11-386	#11-388	#12-402	#12-404	#13-417	#13-419	#14-430	#14-432	#15-452
	#15-454	#16-472	#16-475	#17-503	#17-505	#18-514	#18-518	#19-547	#19-551	#21-575
	#21-577	#22-596	#22-598	#23-607	#23-609	#24-634	#24-637	#26-651	#26-654	#27-662

IDENTIFICATION

PRODUCT NAME: AC-S866A-MC
PRODUCT NAME: CHQFSA0 XXDP+ FILE STRUCT DOC
PRODUCT DATE: APRIL 1981
MAINTAINER: DIAGNOSTIC SYSTEMS ENGINEERING

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XXDP+ FILE STRUCTURE
SPECIFICATION
REVISION 0.0

MAINTAINED BY: DIAGNOSTIC SYSTEM ENGINEERING
REVISION HISTORY
REVISION 0.0 FEB 17 1981
BY MICHAEL CINNAMON

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1.0 INTRODUCTION

THE STRUCTURE THAT XXDP+ USES FOR STORING FILES ON MEDIA IS UNIQUE TO XXDP+. THE STRUCTURE WAS ORIGINALLY BASED ON DUS-11 BUT IT HAS SINCE BEEN MODIFIED TO ACCOMMODATE THE NEEDS OF XXDP+, ALTHOUGH MANY SIMILARITIES STILL EXIST.

XXDP+ SUPPORTS BOTH RANDOM ACCESS AND SEQUENTIAL ACCESS TYPE DEVICES. A DIRECTORY INDEX STRUCTURE IS USED FOR ACCESSING FILES ON RANDOM ACCESS TYPE DEVICES SUCH AS DISKS. FOR SEQUENTIAL DEVICES LIKE MAGTAPE, A HEADER RECORD CONTAINING FILE INFORMATION PRECEDES EACH FILE.

2.0 DATA STRUCTURES

2.1 DATA BLOCKS

THE BASIC UNIT OF DATA TRANSFERRED IN XXDP+ FILE I/O IS A DATA BLOCK. A DATA BLOCK IS DEFINED AS A GROUP OF 512(10) 8 BIT BYTES.

2.1.1 RANDOM ACCESS DEVICES.

ON RANDOM ACCESS DEVICES, DATA BLOCKS ARE ADDRESSED BY THEIR LOGICAL BLOCK NUMBER. LOGICAL BLOCKS ARE DATA BLOCKS THAT ARE ADDRESSABLE IN A LINEAR ORDERED FASHION. THE ORDER RANGES FROM 0 TO N-1, WHERE N IS THE MAXIMUM NUMBER OF BLOCKS SUPPORTED ON THE DEVICE. THE VARIABLE N VARIES DEPENDING ON THE DEVICE BUT IS NOT GREATER THAN 65535 (10). THIS MEANS THAT NO MATTER HOW MANY DATA BLOCKS PHYSICALLY RESIDE ON A DEVICE ONLY THE FIRST N BLOCKS ARE ACCESSIBLE.

LOGICAL BLOCKS ARE NOT NECESSARILY THE SAME AS PHYSICAL BLOCKS. A PHYSICAL BLOCK IS USUALLY LIMITED TO THE AMOUNT OF DATA A PHYSICAL SECTOR CONTAINS WHICH MAY BE LESS THAN 512 (10) BYTES. ALSO A PHYSICAL BLOCK HOUSES AT MOST ONE LOGICAL BLOCK.

A LINKED LIST OF LOGICAL BLOCKS IS SET UP USING LINKED BLOCKS. A LINKED BLOCK IS A LOGICAL BLOCK THAT DEVOTES THE FIRST WORD OF THE BLOCK TO CONTAIN A LINK WORD. THE LINK WORD CONTAINS THE LOGICAL BLOCK NUMBER OF ANOTHER BLOCK. A ZERO LINK WORD INDICATES THAT THE BLOCK CONTAINING IT IS THE LAST BLOCK OF THE LIST.

2.1.2 SEQUENTIAL ACCESS DEVICES

ON SEQUENTIAL ACCESS DEVICES SUCH AS MAGTAPE AND CASSETTE, DATA BLOCKS ARE STORED AS 512 BYTE RECORDS. BECAUSE XXDP+ USES THE SAME READ/WRITE ROUTINES FOR BOTH RANDOM AND SEQUENTIAL ACCESS DEVICES, LOGICAL LINKED BLOCKS ARE STORED ON SEQUENTIAL TYPE DEVICES.

HOWEVER, THE LINK WORD IS ONLY SOME NON-ZERO VALUE TO INDICATE THAT THERE ARE MORE BLOCKS IN THE LIST. A ZERO LINK WORD INDICATES THE LAST BLOCK OF THE LIST.

3.0 FILE STRUCTURES

3.1 TYPES OF FILES

THERE ARE THREE TYPES OF FILES SUPPORTED BY XXDP+. THEY ARE CONTIGUOUS FILES, TEXT FILES AND BINARY FILES. THIS IS NOT TO MEAN THAT OTHER TYPES OF FILES CANNOT BE STORED ON AN XXDP+ MEDIUM, BUT XXDP+ ONLY HAS THE CAPABILITY TO PRODUCE THESE TYPES.

3.1.1 CONTIGUOUS FILES

A CONTIGUOUS FILE IS A SET OF LOGICAL BLOCKS WHICH PHYSICALLY RESIDE IMMEDIATELY ADJACENT TO ONE ANOTHER ON THE MEDIA. THE TERM 'IMMEDIATELY ADJACENT' IS OBVIOUS FOR SEQUENTIAL DEVICES BUT NOT QUITE AS APPARENT FOR RANDOM ACCESS DEVICES. FOR THESE IT MEANS FOR ANY LOGICAL BLOCK N, THE NEXT CONTIGUOUS BLOCK IS LOCATED AT N+1.

CONTIGUOUS FILES ARE NORMALLY USED TO STORE CORE IMAGE DATA SUCH AS THE XXDP+ MONITOR.

3.1.2 TEXT FILES

TEXT FILES ARE MADE UP OF A SERIES OF LINKED BLOCKS. EACH BLOCK CONTAINS 510(10) 8 BIT ASCII CHARACTERS. AN ASCII NULL CHARACTER (A BYTE WITH A VALUE OF ZERO) IS USED TO INDICATE THE END OF THE FILE.

3.1.3 BINARY FILES

BINARY FILES ARE USED TO STORE EXECUTABLE PROGRAMS. THEY ARE MADE UP OF A SERIES OF LINKED BLOCKS EACH OF WHICH CONTAIN SECTIONS OF THE PROGRAM. THESE SECTIONS ARE IN ABSOLUTE FORMATTED BINARY AND THERE IS AT LEAST ONE PER LOGICAL BLOCK.

THE ABSOLUTE FORMATTED BINARY SPECIFICATION IS AS FOLLOWS:

- BYTE 1 - CONTAINS A VALUE OF 1 TO INDICATE STARTING POINT.
- BYTE 2 - CONTAINS A VALUE OF 0. THIS MUST FOLLOW BYTE 1.
- BYTES 3 AND 4 - CONTAINS NUMBER OF BYTES (N) IN THIS BINARY BLOCK. IT INCLUDES BYTES 1, 2, 3 AND 4 BUT EXCLUDES THE CHECKSUM BYTE.
- BYTES 5 AND 6 - CONTAINS THE STARTING MEMORY ADDRESS WHERE THE FOLLOWING DATA BYTES ARE TO BE STORED.
- BYTES 7 TO N - DATA BYTES. $N \leq 509$. THE MAXIMUM NUMBER OF DATA BYTES IS 503.
- BYTE N+1 - CONTAINS THE CHECKSUM BYTE. THE CHECKSUM IS THE 2'S COMPLEMENT OF THE SUM OF THE DATA IN ALL N BYTES. IT IS GENERATED IGNORING OVERFLOW AND CARRY CONDITIONS.

THE END OF A BINARY FILE IS INDICATED BY A BINARY BLOCK THAT HAS A BYTE COUNT OF 6. BYTES 5 AND 6 CONTAIN THE PROGRAM'S TRANSFER ADDRESS. THIS BLOCK IS KNOWN AS THE 'TRANSFER BLOCK'.

A BINARY BLOCK WITH A BYTE COUNT OF 5 INDICATES A 'BIAS BLOCK'. BITS 0 AND 1 OF BYTE 5 REPRESENT BITS 16 AND 17, RESPECTIVELY, OF THE LOAD ADDRESS FOR THE NEXT BINARY BLOCK.

4.0 MEDIA STRUCTURE

4.1 RANDOM ACCESS STRUCTURE

ALL XXDP+ RANDOM ACCESS DEVICES ARE SET UP TO CONTAIN THE FOLLOWING PIECES OF INFORMATION: BOOTSTRAP, MONITOR CORE IMAGE, MASTER FILE DIRECTORIES (MFD), USER FILE DIRECTORIES (UFD), AND BIT MAPS.

THE BOOTSTRAP IS A PROGRAM THAT ALWAYS OCCUPIES LOGICAL BLOCK 0 ON THE DEVICE. IT IS A CORE IMAGE THAT IS PLACED THERE BY THE UTILITY COMMAND 'SAVM'. THE BOOTSTRAP KNOWS WHERE ON THE DISK THE MONITOR CORE IMAGE RESIDES.

THE MONITOR CORE IMAGE IS A CONTIGUOUS FILE THAT IS 16 BLOCKS LONG. IT IS PLACED ON THE DISK BY THE UTILITY COMMAND 'SAVM'. ITS POSITION DEPENDS ON THE TYPE OF DISK. THE MFD IS A TABLE OF INFORMATION WHICH CONTAINS POINTERS TO THE UFDs AND THE BIT MAPS.

4.1.1 MASTER FILE DIRECTORY

THE MFD IS PLACED ON THE DISK BY THE UTILITY 'ZERO' COMMAND. THE TYPE OF DISK DETERMINES WHICH OF TWO VARIETIES OF MFD IS USED AND WHERE IT IS ON THE DEVICE.

MFD VARIETY #1

MFD1:	WORD OFFSET INTO BLOCK
LINK TO MFD2	0
INTERLEAVE FACTOR	1
HIT MAP START BLOCK #	2
POINTER TO BIT MAP #1	3
POINTER TO BIT MAP #2	4
.	
.	
POINTER TO BIT MAP #N	N+2
0	N+3
UNUSED	N+4 TO 255

THE INTERLEAVE FACTOR IS USED AS PART OF THE BLOCK ALLOCATION ALGORITHM. NORMALLY BLOCKS ARE ALLOCATED CONTIGUOUSLY IF POSSIBLE BUT ON CERTAIN DEVICES, THE AMOUNT OF TIME IT TAKES TO ACCESS A LINKED LIST OF BLOCKS IS REDUCED BY PLACING EACH BLOCK OF THE LIST A CONSTANT NUMBER OF BLOCKS APART. THIS CONSTANT NUMBER IS THE INTERLEAVE FACTOR. THE POINTERS IN THE TABLE ARE THE LOGICAL BLOCK NUMBERS OF THE RESPECTIVE BIT MAPS.

MFD2:	WORD OFFSET INTO BLOCK
0	0
401	1
POINTER TO FIRST UFD BLOCK	2
9 (10)	3
0	4
UNUSED	5-255

THE FIRST WORD IS A LINK OF ZERO INDICATING NO MORE MFDS. THE SECOND WORD CORRESPONDS TO THE DUS-11 UIC (1,1). THE THIRD IS THE LOGICAL BLOCK NUMBER OF THE FIRST UFD BLOCK. THE FOURTH IS THE NUMBER OF WORDS IN EACH UFD ENTRY; THIS IS ALWAYS NINE (9).

MFD VARIETY #2

MFD 1/2:	WORD OFFSET INTO BLOCK
0	0
POINTER TO FIRST UFD BLOCK	1
# OF UFD BLOCKS	2
POINTER TO FIRST BIT MAP BLOCK	3
# OF BIT MAP BLOCKS	4
POINTER TO MFD 1/2	5
0	6
NUMBER OF SUPPORTED BLOCKS	7
# OF BLOCKS PREALLOCATED	8
INTERLEAVE FACTOR	9
0	10
POINTER TO FIRST BLOCK OF MONITOR CORE IMAGE	11
0	12
TRACK + SECTOR ADDRESS FOR BAD SECTOR FILE (SINGLE DENSITY)	13
CYLINDER ADDRESS FOR BAD SECTOR FILE (SINGLE DENSITY)	14
TRACK + SECTOR ADDRESS FOR BAD SECTOR FILE (DOUBLE DENSITY)	15
CYLINDER ADDRESS FOR BAD SECTOR FILE (DOUBLE DENSITY)	16

THE ENTRY AT OFFSET 8 MEANS THE NUMBER OF BLOCKS ON THE DEVICE RESERVED FOR DEVICE STRUCTURE INFORMATION. THE LAST 4 ENTRIES IN THE TABLE REFER TO THE LOCATION ON THE DISK OF THE DEC STD 144 BAD SECTOR FILE.

4.1.2 USER FILE DIRECTORY

THE USER FILE DIRECTORY (UFD) IS A LIST OF THE FILES ON THE MEDIA. IT IS CREATED BY THE UTILITY 'ZERO' COMMAND. THE UFD IS ARRANGED AS A LINKED LIST OF LOGICAL BLOCKS AND THE NUMBER OF BLOCKS THAT THE UFD OCCUPIES DEPENDS ON THE DEVICE. EACH BLOCK OF THE UFD CONTAINS SPACE FOR 28(10) FILE ENTRIES.

```

+-----+
| LINK TO NEXT UFD BLOCK |
+-----+
| FILE ENTRY #1          |
+-----+
| FILE ENTRY #2          |
+-----+
|                         |
|                         |
|                         |
+-----+
| FILE ENTRY #28(10)     |
+-----+

```

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EACH FILE ENTRY IS A TABLE OF 9(10) WORDS THAT CONTAINS THE FOLLOWING INFORMATION ABOUT THE FILE.

SEQ 0010

	WORD
FILE NAME	1
	2
FILE EXTENSION	3
FILE DATE	4
ACT-11 LOGICAL END	5
FIRST BLOCK	6
FILE LENGTH	7
LAST BLOCK	8
ACT-11 LOGICAL 52	9

WORDS 1, 2 AND 3 - THE FILE'S SIX CHARACTER FILENAME AND 3 CHARACTER EXTENSION ENCODED IN PAD-50. A DELETED FILE IS INDICATED BY A ZERO IN THESE THREE WORDS.

WORD 4 - THE DUS-11 FORMAT FOR THE DATE GIVEN THE FILE WHEN IT WAS PUT ON THE MEDIA.

WORD 5 AND 9 - ACT-11 USE ONLY. NOT USED IN AXDP+.

WORD 6 - THE BLOCK NUMBER OF THE FIRST LOGICAL BLOCK THAT THE FILE OCCUPIES.

WORD 7 - THE NUMBER OF LOGICAL BLOCKS THAT THE FILE OCCUPIES.

WORD 8 - THE BLOCK NUMBER OF THE LAST LOGICAL BLOCK THAT THE FILE OCCUPIES.

4.1.3 BIT MAP

THE BIT MAP IS A FILE THAT CONTAINS THE CURRENT STATUS OF EVERY SUPPORTED LOGICAL BLOCK ON THE MEDIA.

THE BIT MAP IS ARRANGED AS A LINKED LIST OF LOGICAL BLOCKS. THE NUMBER OF BLOCKS THAT THE MAP OCCUPIES DEPENDS ON THE DEVICE. IT IS CREATED BY THE UTILITY 'ZERO' COMMAND. ONLY THE FIRST 64 WORDS OF EACH MAP BLOCK HAVE MEANING.

	WORD
LINK TO NEXT MAP BLOCK	1
MAP NUMBER	2
60(10)	3
LINK TO FIRST MAP	4
MAP FOR BLOCKS 0-15 (10)	5
MAP FOR BLOCKS 16-31(10)	6
.	.
.	.
MAP FOR BLOCKS 944-959(10)	64

- WORD 1 - THE LOGICAL BLOCK NUMBER OF THE NEXT MAP BLOCK CONTAINS ZERO IF IT IS THE LAST MAP.
- WORD 2 - WHICH MAP THIS ONE IS.
- WORD 3 - NUMBER OF WORDS USED FOR MAP.
- WORD 4 - THE LOGICAL BLOCK NUMBER OF FIRST BIT MAP.
- WORDS 5-64 - MAP FOR 960 BLOCKS. BIT IS SET WHEN BLOCK IS USED. BIT IS CLEARED WHEN BLOCK IS FREE.
- WORDS 65-255 - NOT USED.

4.1.4 RANDOM ACCESS DEVICE INFORMATION

DEVICE	MEMORIC	1ST UFD BLOCK #	# OF UFD BLOCKS	1ST HIT MAP HLA	# OF MAPS	MFD1	MFD2
TU58	DD	3	4	7	1	1	2
RPU4,5,6	DB	3	170.	173.	50.	1	2
RK03,5	DK	3	16.	4795.	5.	1	4794.
RL01/2	DL	24.	146.	2	22.	1	-
RK06,7	DM	31.	96.	2	29.	1	-
RP02,3	DP	3	170.	173.	50.	1	2
RM03	DM	52.	170.	2.	50.	1	-
RS03,4	DS	3	4	7	2	1	2
TU56	DI	102	2	104	1	100	101
RX01	DX	3	4	7	1	1	2
RX02	DY	3	16.	19.	4	1	2

DEVICE	# OF BLKS ON DEV	# OF BLKS TO PREALLOCATE	INTER- LEAVE	ROOT BLK #	MONITOR BLOCK #
TU58	511.	40.	1	0	8.
RP04,5,6	48000.	255.	1	0	223.
RK03,5	4800.	69.	5	0	30
RL01/2	20460. OR 10200.	200.	1	0	170.
RK06,7	27104.	157.	1	0	127.
RP02,3	48000.	255.	1	0	223.
RM03	48000.	255.	1	0	222.
RS03,4	989.	41.	1	0	9.
TU56	576.	69.	5	0	30
RX01	494.	40.	1	0	8.
RX02	988.	55.	1	0	23.

4.2 SEQUENTIAL ACCESS DEVICES

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SEQ 0014

4.2.1 MAGTAPE

ALTHOUGH MAGTAPE IS NOT USUALLY CONSIDERED AS A FILE STRUCTURED DEVICE, CERTAIN STRUCTURE FEATURES ARE IMPLEMENTED TO ENABLE CREATION AND RETRIEVAL OF MULTIPLE FILES.

THE FILES ON MAGTAPE ARE TERMINATED BY AN END-OF-FILE MARK (EOF) OR TAPE MARK (TM). THE LAST FILE ON THE TAPE IS TERMINATED BY 2 CONSECUTIVE EOFs TO INDICATE LOGICAL-END-OF-TAPE.

```

+-----+
! 1ST FILE ! EOF ! 2ND FILE ! EOF ! LAST FILE ! EOF ! EOF !
+-----+

```

EACH FILE ON MAGTAPE IS MADE UP OF A HEADER AND DATA RECORDS.

```

+-----+
! HEADER ! 1RG ! DATA RECORD ! 1RG ! DATA RECORD ! 1RG ! EOF ! 1RG !
! 7 WORDS ! ! 256 WORDS ! ! 256 WORDS ! ! ! !
+-----+

```

THE HEADER RECORD IS STRUCTURED AS FOLLOWS:

	WORD
FILE NAME	1
	2
FILE EXTENSION	3
401	4
0	5
FILE DATE	6
FILE SIZE	7

WORD 1, 2 AND 3 - THE FILENAME AND EXTENSION ENCODED IN RAD-50.

WORD 4 - DOS-11 UIC (1,1)

WORD 5 - SET TO ZERO.

WORD 6 - DATE GIVEN THE FILE WHEN WRITTEN ON THE TAPE. IT IS IN DOS-11 FORMAT. BIT 15 OF THE DATE, WHEN SET, INDICATES A CONTIGUOUS FILE.

WORD 7 - THE NUMBER OF LOGICAL BLOCKS (RECORDS) IN THE FILE.

THE FIRST FILE ON A MAGTAPE IS NORMALLY THE XDP+ MONITOR CORE IMAGE. IT IS WRITTEN AS A CONTIGUOUS FILE. EVERY NEW FILE IS WRITTEN AT THE LOGICAL END OF TAPE. THE LAST FILE ON TAPE MAY BE WRITTEN SO PART LIES AFTER THE PHYSICAL EOF MARKER BUT NO FILE WILL BE WRITTEN ENTIRELY AFTER THE PHYSICAL EOF MARK.

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SEQ 0016

4.2.2 CASSETTE (TU60)

CASSETTE IS STRUCTURED SIMILARLY TO MAGTAPE IN THAT EACH FILE IS PRECEDED BY A HEADER AND A MARKER THAT IDENTIFIES THE LOGICAL END OF TAPE. HOWEVER, THE ACTUAL DATA IN THE HEADER AND END-OF-FILE MARKER ARE DIFFERENT.

THE FILES ON CASSETTE ARE TERMINATED BY A FILE GAP. THE LAST FILE ON THE TAPE IS TERMINATED BY A SENTINEL FILE. A FILE GAP MUST PRECEDE THE FIRST FILE ON THE CASSETTE. THE TAPE IS FORMATTED AS FOLLOWS:

```
+-----+
! GAP ! FILE A ! GAP ! FILE B ! GAP ! FILE C ! GAP ! SENTINEL !
+-----+
```

THE SENTINEL FILE IS A 32 BYTE RECORD CONTAINING ALL ZERO'S.

EACH FILE ON CASSETTE IS MADE UP OF A FILE HEADER RECORD AND FILE DATA RECORDS IN MULTIPLES OF 4.

```
+-----+
! FILE HEADER ! DATA RECORD ! DATA RECORD ! DATA RECORD ! DATA RECORD ! GAP !
! 32 BYTES ! 128 BYTES ! 128 BYTES ! 128 BYTES ! 128 BYTES ! !
+-----+
```

	BYTE
FILE NAME	1-6
FILE EXTENSION	7,8,9
FILE TYPE	10
100000	11,12
0	13-16
DATE	17,18
FILE LENGTH	19,20
0	21,32

BYTES 1-9 - FILENAME AND EXTENSION ENCODED IN RAD-50.
 BYTE 10 - FILE TYPE INDICATOR. 0=6000, 14=DELETED.
 BYTES 11,12 - WORD INDICATING RECORD LENGTH SET TO 100,000 (128 BYTE RECORDS).
 BYTE 13 - SEQUENCE NUMBER NOT USED IN XXDP+.
 BYTE 14 - HEADER CONTINUATION NOT USED IN XXDP+.
 BYTES 15,16 - NOT USED.
 BYTES 17,18 - DOS-11 FORMATTED DATE GIVEN THE FILE WHEN PUT ON THE TAPE.
 BYTES 19,20 - NUMBER OF LOGICAL BLOCKS IN THE FILE. HAS MEANING FOR CONTIGUOUS FILES ONLY.
 BYTES 21-32 - NOT USED.

5.0 GLOSSARY

PAGE 13

SEQ 0018

IRG - INTERRECORD GAP. THE GAP THAT IS WRITTEN BETWEEN RECORDS ON MAGTAPE.
 MFD - MASTER FILE DIRECTORY
 RAD-50 - RADIX-50. A METHOD OF ENCODING 3 ASCII CHARACTERS INTO ONE 16 BIT WORD.
 UFD - USER FILE DIRECTORY.
 UIC - USER IDENTIFICATION CODE.

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1 000000 .REPT 0

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I D E N T I F I C A T I O N

* *
* DEC/X11 MODULE *
* *

PRODUCT CODE: AC-S330A-MC
PRODUCT NAME: CXCMJA0 CMR11 DEC/X11 MODULE
PRODUCT DATE: 18-NOV-80
MAINTAINER: SPECIAL SYSTEMS, KANATA (002)

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HISTORY:

CXCMJAO

18-NOV-80

FIRST RELEASE- NEW OPTION

D.G.W.

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- 5. MODULE OPERATION
 - 5.1 CONFIGURATION REQUIREMENTS
 - 5.2 TEST SEQUENCE
- 6. ERROR REPORTS

1. ABSTRACT

THE PURPOSE OF THIS MODULE IS TO TEST SYSTEM INTEGRATION OF THE CMR11 HOST MODULE (M8990) ON THE UNIBUS BY TRANSFERRING DATA TO AND FROM THE DEVICE AND CAUSING NON SYNCHRONOUS INTERRUPTS IN A SYSTEM ENVIRONMENT.

THE MODULE MUST EASILY RECOVER FROM A POWER FAILURE EVEN THOUGH THE CMR11 FIRMWARE COMPLETELY RESTARTS UPON A POWER-UP.

IT IS NOT THE PURPOSE OF THIS MODULE TO GENERATE ANY HOST-TO-REMOTE ACTIVITY OTHER THAN TO INITIATE A "BACKGROUND SCAN" SO THAT THE L.E.D.'S ON THE MODEM MODULES "BLINK" AND SO THAT THE HOST MAY DETECT ANY INCOMING REMOTE "ALARMS" ON PORT 0.

IF THE REMOTES HAD BEEN SET UP PRIOR TO RUNNING DEC/X11 TO SCAN AND REPORT ALARMS, THEY WILL CONTINUE TO DO SO BUT THE REPORTING OF THEM TO THE USER BY DEC/X11 WILL NOT BE DETAILED. A SIMPLE ALARM MESSAGE WILL BE PRINTED INDICATING WHICH REMOTE HAD AN ALARM.

IF THIS MODULE IS DROPPED, "BACKGROUND SCAN" WILL BE CLEARED AND THE HOST INITIALIZED SO THAT NO FURTHER COMMUNICATION WILL TAKE PLACE.

2. REQUIREMENTS

HARDWARE: PDP-11 PROCESSOR WITH A CMR11 HOST MODULE (M8990) ON THE UNIBUS.

OTHER: THIS MODULE IS MEANT TO BE CONFIGURED AND RUN WITH DEC/X11 ONLY.

ALL PROCESSOR DIAGNOSTICS AND CMR11 HOST DIAGNOSTICS MUST BE RUN SUCCESSFULLY PRIOR TO RUNNING THIS MODULE.

3. ASSUMPTIONS

IT IS ASSUMED THAT THE CMR11 HOST FIRMWARE IS INSTALLED AND RUNNING PROPERLY IN THE TWO 'EPROM' CHIPS E66 AND E61 ON THE M8990.

IT IS ALSO ASSUMED THAT IF ANY REMOTE "ALARMS" OCCUR, THEY WILL BE CLEARED MANUALLY OR IGNORED. THIS MODULE WILL PRINT AN ALARM MESSAGE REPEATEDLY AS LONG AS AN ALARM CONDITION EXISTS ON ANY OF THE REMOTES ON PORT 0.

THE ALARM MAY BE IGNORED BY DISCONNECTING THE COMMUNICATION CABLE FROM THE PLUG FOR THE PORT CONNECTED TO THE ALARM-ING REMOTE, OR BY SETTING BIT 0 IN SR2 (CMAA0 20).

4. OPERATING INSTRUCTIONS

4.1 LOADING AND STARTING:

THIS IS A DEC/X11 OBJECT MODULE WHICH MUST BE CONFIGURED AS PART OF A DEC/X11 'RUN-TIME' SYSTEM USING THE DEC/X11 RUN TIME MONITOR. REFERENCE DXQBA DEC/X11 USERS DOCUMENTATION AND REFERENCE GUIDE.

4.2 ENVIRONMENT:

THIS PROGRAM RUNS UNDER DEC/X11 ONLY.

4.3 OPTIONS:

SR1 BIT 15 SET (1)	IF ERROR RETRY LIMIT IS EXCEEDED, RESET THE RETRY COUNT AND CONTINUE. (DON'T DROP)
SR1 BIT 15 CLR (0)	IF ERROR RETRY LIMIT IS EXCEEDED, A HARD ERROR IS ASSUMED AND THE MODULE IS DROPPED.
SR2 BIT 0 SET (1)	IF A REMOTE ALARM OCCURS, READ IT BUT DON'T PRINT ANYTHING AS A RESULT. (INHIBIT CMR ALARM REPORT).
SR2 BIT 0 CLR (0)	IF A REMOTE ALARM OCCURS, READ IT AND PRINT ALARM MESSAGE GIVING REMOTE NUMBER.

4.4 EXECUTION TIMES:

(STANDALONE) = ABOUT 49 SECONDS PER PASS

5. MODULE OPERATION

5.1 CONFIGURATION REQUIREMENTS:

DEFAULT PARAMETERS:

DEVICE ADDRESS: 170000
DEVICE VECTOR: 170
PRIORITY: 4
OPTIONS (SR1): 000000 (DROP MODULE IF RETRY LIMIT EXCEEDED)
(SR2): 000000 (PRINT ON REMOTE ALARMS)

REQUIRED PARAMETERS:

(NONE)

5.2 TEST SEQUENCE:

- A. SET UP VECTOR, ADDRESSES, MODULE VARIABLES
- B. CHECK IF MODULE TO BE DROPPED:
-WAIT FOR PWR-UP COMPL.
-DO "DIE" FUNCTION
-READ CSR SHOULD BE 137600
-IF NOT AFTER 777 COUNTS, EXIT TO DROP MODULE
-IF YES, INIT DEVICE AND CONTINUE.
- C. PERFORM CHECK OF POWER UP BIT TO SEE IF AN INTERRUPT IS ON ITS WAY. IF SET, EXIT WITH INTR ENAB SET; IF CLR, CONTINUE TO 'D.'
- D. SET INTR ENAB & BACKGROUND SCAN.
DO A DATA IN-OUT TEST ON REQUEST SCRATCHPAD MEMORY WITH BREAKS TO MONITOR.
LOOK AT "ALMINT" SOFTWARE FLAG. IF SET, PRINT CMR11 ALARM MESSAGE, THEN CLEAR "ALMINT".
- E. PERFORM AN INIT OF THE CMR11 TO START ITS POWER-UP SEQUENCE. THEN SET BACKGROUND SCAN.
- F. GO TO "C".

UPON INTERRUPT:

1. CHECK FOR ALARM INTERRUPT, AND IF ANY, SET "ALMINT" FLAG AND READ REMOTE ADDRESS FROM SCRATCHPAD. THEN PERFORM "PIRQ" TO QUEUE UP RETURN TO "2" (BELOW).
2. CHECK FOR POWER-UP AND INTERRUPT BITS
3. IF SET, DO DATA IN-OUT TEST ON THE RESULT SCRATCHPAD MEMORY WITH BREAKS TO MONITOR. THEN DO CLR TOP BYTE AND SIGNAL ***END OF PASS***.
4. -IF PWR-UP CLR, FLAG AN ERRONEOUS INTERRUPT.

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5. GO TO "C" ABOVE.

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6. ERROR REPORTS

- A. THE STANDARD DEC/X11 ERROR ROUTINES ARE USED IN THIS
 MODULE.
- B. ERROR REPORTS WHICH DUMP AN EXTENDED PRINTOUT OF THE
 DEVICE REGISTERS, DO SO IN THE FOLLOWING ORDER:

 CSR SPAR SPDR
- C. REMOTE "FIELD ALARMS" ARE INDICATED AS FOLLOWS:

 --->CMR11 ALARM. REMOTE #NNN<---

.ENDR


```

1      ;*****
2
3      :CMR11 DEC/X11 SYSTEM EXERCISER MODULE
4
5      .TITLE CMAA DEC/X11 SYSTEM MODULE
6
7      IOMOD <CMAA >,170000,170,4,0,0,8,,0
          MODULE 140000,CMAA ,170000,170,4,0,0,8,,0
          .TITLE CMAA DEC/X11 SYSTEM EXERCISER MODULE
          ;      DDSCOM VERSION 6      23-MAY-78
          .LIST BIN
          ;*****
          BEGIN:
          MUDNAM: .ASCII /CMAA / ;MODULE NAME.
          000000      103      115      101
          000000      101      040
          000005      000
          000006      170000
          000010      000170
          000012      200
          000013      000
          000014      000001
          000016      000000
          000020      000000
          000022      000000
          000024      000000
          XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUF USAGE
          ADDR: 170000+0 ;1ST DEVICE ADDR.
          VECTOR: 170+0 ;1ST DEVICE VECTOR.
          BR1: .BYTE PRTY4+0 ;1ST BR LEVEL.
          BR2: .BYTE PRTY0+0 ;2ND BR LEVEL.
          DIVID1: 0+1 ;DEVICE INDICATOR 1.
          SR1: OPEN ;SWITCH REGISTER 1
          SR2: OPEN ;SWITCH REGISTER 2
          SR3: OPEN ;SWITCH REGISTER 3
          SR4: OPEN ;SWITCH REGISTER 4
          ;*****
          000026      140000
          000030      000224'
          000032      000224'
          000034      000000
          000036      000010
          000040      000000
          000042      000000
          000044      000000
          000046      000000
          000050      000000
          000052      000000
          000054      000000
          000056
          000056      000000
          000060      000000
          000062      000000
          000064      000000
          000066      000000
          000070      000000
          000072      000000
          000074      000000
          000076      000000
          000100      000000
          000102
          000102      000000
          000104
          000104      000000
          000106
          000106      000000
          000110      000000
          000112      000224'
          STAT: 140000 ;STATUS WORD.
          INIT: START ;MODULE START ADDR.
          SPOINT: MUDSP ;MODULE STACK POINTER.
          PASCNT: 0 ;PASS COUNTER.
          ICONT: 8. ;# OF ITERATIONS PER PASS=8.
          ICOUNT: 0 ;LOC TO COUNT ITERATIONS
          SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
          HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
          SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
          HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
          SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
          RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
          CONFIG:
          RES1: 0 ;RESERVED FOR MONITOR USE
          RES2: 0 ;RESERVED FOR MONITOR USE
          SVH0: OPEN ;LOC TO SAVE R0.
          SVR1: OPEN ;LOC TO SAVE R1.
          SVR2: OPEN ;LOC TO SAVE R2.
          SVR3: OPEN ;LOC TO SAVE R3.
          SVR4: OPEN ;LOC TO SAVE R4.
          SVR5: OPEN ;LOC TO SAVE R5.
          SVR6: OPEN ;LOC TO SAVE R6.
          CSRA: OPEN ;ADDR OF CURRENT CSR.
          SBADR: ;ADDR OF GOOD DATA, OR
          ACSR: OPEN ;CONTENTS OF CSR.
          WASADR: ;ADDR OF BAD DATA, OR
          ASTAT: OPEN ;STATUS REG CONTENTS.
          ERRTYP: ;TYPE OF ERROR
          ASB: OPEN ;EXPECTED DATA.
          AWAS: OPEN ;ACTUAL DATA.
          RSTRT: RESTRT ;RESTART ADDRESS AFTER END OF PASS

```

```

          000114      000000
          000116      000000
          000120      000000
          000122      000000
          000040
          WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
          WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
          INTR: OPEN ;# OF INTERRUPTS PER ITERATION
          IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0
          .REPT SPSIZ ;MODULE STACK STARTS HERE.
          .NLIST
          .WORD 0
          .LIST
          .ENDR
          000224
          MODSP:
          ;*****
          ;DEFINITIONS
          8
          9
          10      000000
          11      001000
          12      002000
          13      010000
          14      100000
          15      000200
          16      000100
          17      000020
          18      000004
          19
          20
          REQAD = 0
          RESAD = 1000
          PWRUP = 2000
          ALMOET = 10000
          INT = 100000
          MAIN = 200
          IE = 100
          CLRTPB = 20
          ENABKS = 4
          ;-----

```

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1
2
3
4
5 000224
6 000224 005767 177610
7 000230 001024
8 000232 004767 000620
9 000236 012777 000002 001450
10 000244 104407 000000'
11 000250 104407 000000'
12 000254 104407 000000'
13 000260 104407 000000'
14 000264 004767 000666
15 000270 105767 001431
16 000274 001402
17 000276 000167 000464
18 000302
19 000302 104407 000000'
20 000306 104407 000000'
21 000312 104407 000000'
22 000316 104407 000000'
23 000322 105067 001400
24 000326 032777 002000 001360
25 000334 001405
26 000336 052777 000104 001350
27 000344 104400 000000'
28
29 000350 052777 000104 001336
30 000356 012767 000000 001322
31 000364 004767 001036
32 000370 104407 000000'
33 000374 104407 000000'
34 000400 105767 001322
35 000404 001404
36 000406 012746 000350'
37 000412 000167 000366
38
39 000416 105767 001302
40 000422 001415
41
42
43
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;*****
;START AND RESTART AT SAME LOCATION:

START:
RESTR1: TST ICOUNT ;STARTING POINT
;ESPECIALLY FOR DT03,DT07
BNE ENT2 ;*****
JSR PC,SETUP ;GEN. DEVICE ADDRESSES
MOV #2,CCSR ;INIT CMR11 ONLY
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
JSR PC,TSTDRP ;CHECK IF MODULE TO BE DROPPED
TSTB DROP
BEQ ENT2 ;IF NOT, BEGIN TESTING
JMP FINI ;GO DROP MODULE

ENT2:
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
CLRB ENFLG ;CLEAR ERROR INDICATOR
BIT #PWRUP,CCSR ;IS POWER UP BIT SET?
BEQ ENT3 ;NO, DO REG. MEM TEST
BIS #IEIENABKS,CCSR ;SET INTR EN & HKG SCAN
EXITB,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

ENT3:
BIS #IEIENABKS,CCSR ;SET THEM HERE ALSO
MOV #HEQAD,MPOINT ;SET MEM POINTER TO REG. MEM
JSR PC,MEMTST ;GO DO SCR PAD MEM TEST
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
TSTB ENFLG ;ANY ERRORS?
BEQ ENT4 ;NO, CONTINUE
MOV #ENT3,-(SP) ;YES, PUSH RETRY ADDRESS
JMP RETRY ;AND RETRY UP TO LIMIT

ENT4:
TSTB ALMINT ;IS ALARM FLAG SET?
BEQ ENT5 ;NO, CONTINUE
;*****
;CONVERT REMNM TO ASCII AND
;STORE AT MSG2
000424 104420 000000' 001710'
000432 002032'
OTUAS,BEGIN,REMNM,MSG2

;*****
BIT #1,SR2 ;CHECK IF ALLOWED TO PRINT ALARM
BNE 1$ ;NOT IF BIT 0 OF SR2 IS SET!!
MSGMS,BEGIN,REMNM ;ASCII MESSAGE CALL WITH COMMON HEADER
CLRB ALMINT ;CLR FLAG

1$:
ENT5: MOV #CLRTPB,CCSR ;CLR TOP BYTE
MOV #2,CCSR ;INIT CMR11
MOV #ENABKS,CCSR ;SET BACKGROUND SCAN
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....

```

```

000514 104407 000000'
46 000520 104407 000000'
47 000524 104407 000000'
48 000530 000167 177546
49
;-----
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
JMP ENT2

```

```

1          ;*****
2
3          ;INTERRUPT ENTRY POINT.
4          ;THIS ROUTINE CHECKS FOR EXPECTED TYPES OF INTERRUPTS
5          ;AND FLAGS OTHER TYPES AS ERRONEOUS.
6          ;
7          ;EXPECTED TYPES ARE: PWR-UP AND ALM DET.
8
9 000534      CMRINT:      ;INTERRUPT ENTRY POINT
10 000534      042777      000100      001152      BIC      #IE,PCSR      ;CLEAR INTR EN ONLY
11 000542      022777      110004      001144      CMP      #INT:ALMDET:ENABKS,PCSR ;ALARM DETECTED FROM REMOTE?
12 000550      001021      000000      000000      BNE      10$      ;NO, MUST BE PWRUP
13 000552      112767      177777      001144      MOV     #-1,ALMINT      ;YES, SET SFTWR FLAG
14 000560      017746      001132      001144      MOV     #SPAR,-(SP)      ;SAVE CURRENT SPAR ON STACK
15 000564      012777      001003      001124      MOV     #1003,SPAR      ;LOAD ADDRESS OF REMOTE #
16 000572      017767      001122      001110      MOV     #SPDR,HEMNM      ;GET REMOTE #
17 000600      012677      001112      001102      MOV     (SP)+,SPAR      ;REPLACE CONTENTS OF SPAR
18 000604      052777      000120      001102      BIS      #CLRTPB:IE,PCSR      ;SET TO LEAVE
19 000612      000002      000000      000000      RTI      ;WE'LL DO OUR OWN THIS TIME!
20
21 000614      10$:
22 000614      000004      000000      000622      PIRQS,BEGIN,ENTZ      ; QUEUE UP TO CONTINUE AT ENTZ AND RTI
23
24 000622      022777      102004      001064      ENTZ:  CMP      #INT:PWRUP:ENABKS,PCSR ;PWR-UP & INTR BITS SET?
25 000630      001414      000000      000000      BEQ     ENTZ1      ;IF YES, GO CHECK R&S, SCPAD MEM
26 000632      004767      001026      000000      JSR     PC,ERSUB      ;GET ERROR INFO
27 000636      012767      000011      177242      MOV     #11,ERRTP      ;ILLEGAL INTR OR DONE CLR
28
29 000644      104405      000000      001714      HRDRS,BEGIN,TABL1 ;UNEXPECTED INTERRUPT
30 000652      112767      177777      001046      MOV     #-1,ERFLG      ;SET ERROR FLAG
31 000660      000420      000000      000000      BR      ENTXX      ;AND LEAVE
32
33 000662      012767      001000      001016      ENTZ1: MOV     #RESAD,MPPOINT      ;SET MEMM PNTR TO RES, SCPAD
34 000670      004767      000532      000000      JSR     PC,MEMTST      ;GO DO SCPAD MEM TEST
35 000674      104407      000000      000000      BREAKS,BEGIN      ;TEMPORARY RETURN TO MONITOR....
36 000700      104407      000000      000000      BREAKS,BEGIN      ;THEN CONTINUE AT NEXT INSTRUCTION.
37 000704      105767      001016      000000      TSTB     ERFLG      ;CHECK ERROR FLAG
38 000710      001413      000000      000000      BEQ     ENTZ2      ;NO ERRORS, LEAVE
39 000712      012746      000662      000000      MOV     #ENTZ1,-(SP)      ;STACK RETRY ADDR
40 000716      000167      000062      000000      JMP      RETRY      ;STACK RETRY ADDR
41
42 000722      012777      000002      000764      ENTXX: MOV     #2,PCSR      ;INITIALIZE CMR
43 000730      012746      000302      000000      MOV     #ENTZ,-(SP)      ;STACK RETRY ADDRESS
44 000734      000167      000044      000000      JMP      RETRY      ;AND RETRY UP TO LIMIT
45
46 000740      052777      000020      000746      ENTZX: BIS      #CLRTPB,PCSR      ;CLEAR TOP BYTE
47 000746      105067      000755      000000      CLRB     TRY      ;TRY
48 000752      105067      000750      000000      CLRB     ERFLG      ;TRY
49 000756      104413      000000      000000      ENDITS,BEGIN      ;SIGNAL END OF ITERATION.
50 000762      000167      177314      000000      JMP      ENT2      ;MONITOR SHALL TEST END OF PASS
51 000766      000167      177314      000000      JMP      ENT2      ;ANOTHER LOOP
52
53          ;-----

```

```

1          ;*****
2
3          ;DROP POINT!!!  MODULE IS DROPPED AT THIS POINT.
4
5 000766      052777      000020      000720      FINI:  BIS      #CLRTPB,PCSR      ;CLR TOP BYTE OF CSR
6 000774      005077      000714      000000      CLR      #CSR      ;AND CLR THE REST
7 001000      104410      000000      000000      ENDS,BEGIN      ;
8
9          ;-----

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11 001004 105267 000717 RETRY: INCB TRY ;UPDATE TRIES
12 001010 122767 000012 000711 CMPB #10,,TRY ;DONE 10.?
13 001016 001016 BNE RTR1 ;IF NOT, RETRY
14 001020 005767 176772 TST SR1 ;IF YES, CHECK OPTION BIT 15
15 001024 100411 BMI RTRO ;IF SET, CONTINUE
16 001026 104403 000000' 001730' MSGNS,BEGIN,EXCED ;ASCII MESSAGE CALL WITH COMMON HEADER
17 001034 012777 000020 000652 MOV #CLWTPB,0CSR ;CLR THE DEVICE
18 001042 005726 TST (SP)+ ;DUMP THE RETRY ADDRESS
19 001044 000167 177716 JMP FINI ;AND GO DROP THE MODULE
20
21 001050 105067 000653 RTRO: CLRB TRY ;CLR RETRY COUNTER
22 001054 000207 RTR1: RTS PC ;TRY AGAIN
23
24
25
26
27
28
29
30
31
32
33
34 001056 016700 176724 SETUP: MOV ADDR,R0 ;GET BASE ADDR TO R0
35 001062 010067 000626 MOV R0,CSR ;GEN. CSR ADDR
36 001066 005720 TST (R0)+ ;R0 = R0+2
37 001070 010067 000622 MOV R0,SPAR ;GEN. SPAR ADDR
38 001074 005720 TST (R0)+
39 001076 010067 000616 MOV R0,SPDR ;AND GEN. SPDR ADDR
40
41 001102 016700 176702 MOV VECTOR,R0 ;GET DEVICE VECTOR
42 001106 010067 000570 MOV R0,CMRVEC ;LOAD DEVICE VECTOR
43 001112 005720 TST (R0)+
44 001114 116710 176672 MOVB BR1,(R0) ;SAVE PRIORITY
45 001120 012777 000534' 000554 MOV #CMRINT,ACMRVEC ;POINT VECTOR TO INTR ENTRY
46 001126 105067 000574 CLRB ENFLG ;CLR FLAGS,
47 001132 105067 000567 CLRB DROP
48 001136 105067 000565 CLRB TRY
49 001142 105067 000556 CLRB ALMINT
50 001146 012767 000000 000532 MOV #REQAD,MPOINT ;INIT POINTER
51 001154 000207 RTS PC ;RETURN
52
53

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10 001156 TSDRP: ;ENTRY POINT
11 001156 105067 000543 CLRB DROP ;CLEAR DROP FLAG
12 001162 012767 000777 000514 MOV #777,CNTR0 ;SET UP TIMER
13 001170 022777 102000 000516 TDRO: CMP #PWRUP:INT,0CSR ;PWR UP & INTR SET?
14 001176 001425 BEQ IDR1 ;YES, CONTINUE
15 001200 005367 000500 DEC CNTR0 ;NO, COUNT TIMER
16 001204 001405 BEQ TDRE ;ERROR IF TIMEOUT!!
17 001206 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
18 001212 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
19 001216 000764 BR TURO ;KEEP LOOKING
20 001220 112767 177777 000477 TDRE: MOVB #-1,DROP ;NO GOOD? SET DROP FLAG
21 001226 004767 000432 JSR PC,ERSUB ;GET ERROR INFO
22 001232 012767 000006 176646 MOV #0,ERRTP ;DEVICE NOT ON-LINE
23
24 001240 104405 000000' 001714' HDRERS,BEGIN,TAB1 ;INIT SEQ WON'T COMPLETE
25 001246 000167 000136 JMP TUOX ;NOW LEAVE
26
27 001252 052777 000020 000434 TDR1: BIS #CLRTPB,0CSR ;CLR TOP BYTE OF CSR
28 001260 005077 000432 CLR #SPAR ;ADDR TOP OF REQ SCPAD
29 001264 012777 000213 000426 MOV #213,SPDR ;LOAD MAINT FUNC = SUB F #3
30 001272 005277 000420 INC #SPAR ;NEXT SP ADDR
31 001276 012777 000001 000414 MOV #1,SPDR ;DUMMY # OF WORDS
32 001304 005277 000406 INC #SPAR ;NEXT SP ADDR
33 001310 012777 000001 000402 MOV #1,SPDR ;DUMMY # OF REQUESTS
34 001316 052777 000200 000370 BIS #MAIN,0CSR ;SET "MAIN"
35 001324 012767 000777 000352 MOV #777,CNTR0 ;SET UP A COUNTER
36 001332 022777 137600 000354 TDR2: CMP #137600,0CSR ;CSR SHOULD GO TO 137600
37 001340 001423 BEQ TDRE ;EXIT IF IT DOES
38 001342 005367 000336 DEC CNTR0 ;IF NOT, WAIT A BIT
39 001346 001405 BEQ TDRE1 ;THEN DROP MODULE
40 001350 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
41 001354 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
42 001360 000764 BR TDR2 ;KEEP A WATCH
43
44 001362 112767 177777 000335 TDRE1: MOVB #-1,DROP ;SET "DROP" FLAG
45 001370 004767 000270 JSR PC,ERSUB ;GET ERROR INFO
46 001374 012767 000033 176504 MOV #33,ERRTP ;
47 001402 104405 000000' 001714' HDRERS,BEGIN,TAB1 ;WRONG RESPONSE TO "DIE" FUNC
48
49 001410 012777 000020 000276 TDRX: MOV #CLRTPB,0CSR ;CLR TOP BYTE OF CSR
50 001416 012777 000002 000270 MOV #2,0CSR ;INIT C4R11
51 001424 000207 RTS PC ;AND RETURN
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17 001426
18 001426 105067 000274
19 001432 005067 000246
20 001436 016702 000244
21 001442 005067 000244
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23 001446 010277 000240
24 001452 016777 000234
25 001460 120227 000377
26 001464 001404
27 001466 005202
28 001470 005267 000216
29 001474 000764
30
31 001476
32 001502 104407 000000
33 001506 016702 000174
34 001512 005067 176370
35 001516 010277 000174
36 001522 017767 000172 176360
37 001530 026767 176352 176352
38 001536 001415
39 001540 004767 000120
40 001544 010267 176334
41 001550 012767 000106 176324
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43 001556 104404 000000
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46 001572 120227 000377
47 001576 001404
48 001600 005202
49 001602 005267 176300
50 001606 000743
51 001610 016702 000072
52 001614 005067 000072
53 001620 010277 000072
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54 001624 016777 000062 000066      MOV      WDRD,@SPDK      ;DEPOSIT 0'S
55 001632 120227 000377          CMPB     K2,#377      ;DONE ALL?
56 001636 001402          BEQ      MRPT1      ;YES. SEE IF WE TRY AGAIN
57 001640 005202          INC      K2              ;NO. NEXT ADDRESS
58 001642 000766          BR       MRPT0      ;AND CONTINUE CLEARING
59 001644 026727 000034 000200 MRPT1:  CMP     CNTR0,#200    ;DONE 200(8) LOOPS?
60 001652 001403          BEQ      MRX              ;YES, EXIT
61 001654 005267 000024          INC      CNTR0      ;KEEP A COUNT OF LOOPS
62 001660 000666          BR       MRSTRT
63
64 001662 000207          MRX:    RTS       PC
65
66 ;-----
67
68 ;+++++
69
70 ;ERROR INFO SUBROUTINE:
71 ;GET INFO FOR DEC/X11 MONITOR ERROR ROUTINES
72
73
74
75 001664 016767 000024 176206 ERSUB:  MOV     CSK,CSRA      ;LOAD ADDR OF STAT REG.
76 001672 017767 000016 176202      MOV     @CSR,ACSR     ;LOAD CONTENTS OF CSR
77 001700 000207          RTS       PC              ;AND RETURN
78
79 ;-----
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7 001702 000000      CHMVEC: .WORD 0          ;CMR VECTOR
8 001704 000000      CNTR0:  .WORD 0          ;ALL PURPOSE COUNTER
9 001706 000000      MPOINT: .WORD 0          ;POINTER FOR MEM. TEST ROUTINE
10 001710 000000      REMNM:  .WORD 0          ;STORAGE FOR REMOTE #
11 001712 000000      WORD:   .WORD 0          ;DATA STORAGE FOR MEM TEST
12
13 001714             TABL1:
14 001714 000000      CSR:     .WORD 0          ;COMMAND & STATUS REGISTER
15 001716 000000      SPAN:    .WORD 0          ;SCRATCHPAD ADDRESS REGISTER
16 001720 000000      SPDR:    .WORD 0          ;SCRATCHPAD DATA REGISTER
17 001722 177777      .WORD 177777          ;TABLE TERMINATOR
18
19
20
21 001724 000         ALMINT: .BYTE 0          ;INDICATOR FOR REMOTE ALARMS
22 001725 000         DROP:   .BYTE 0          ;MODULE DROP FLAG
23 001726 000         ERFLG:  .BYTE 0          ;ERROR INDICATOR FLAG
24 001727 000         TRY:     .BYTE 0          ;ATTEMPT COUNT FOR ERRORS
25
26
27
28
29
30 001730 001742'      EXCED: MSG0
31 001732 177777      .WORD 177777
32 001734 001777'      REMALM: MSG1
33 001736 002032'      MSG2
34 001740 177777      .WORD 177777
35
36 001742 045 105 122 MSG0: .ASCIZ '%ERRRUK RETRY LIMIT EXCEEDED%'
37 001745 122 117 122
38 001750 040 122 105
39 001753 124 122 131
40 001756 040 114 111
41 001761 115 111 124
42 001764 040 105 130
43 001767 103 105 105
44 001772 104 105 104
45 001775 045 000
46 001777 045 055 055 MSG1: .ASCIZ '%--->CHM11 ALARM. REMOTE #'
47 002002 055 076 103
48 002005 115 122 061
49 002010 061 040 101
50 002013 114 101 122
51 002016 115 056 040
52 002021 122 105 115
53 002024 117 124 105
54 002027 040 043 000
55
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59 002032             .EVEN
60 002040 074 055 055 MSG2: .BLKB 6
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41 002043 055 045 000 .EVEN
42 000001 .END

```


ACSR	000102R	CONFIG	000056H	INIT	000030R	PRTY1 =	000040	SPDR	001720R
ADDM	000006R	CSR	001714H	IHT	= 10000U	PRTY2 =	000100	SPOINT	000032P
ADDR22=	001000	CSRA	000100K	INTR	000120H	PRTY3 =	000140	SPSIZ =	000040
ALMODET=	010000	UATCK\$=	104411	MAIN =	00020UV	PRTY4 =	000200	SR1	000016R
ALMHINT	001724R	DATER\$=	104404	MAP22\$=	104416	PRTY5 =	000240	SR2	000020R
ASB	000106R	DROP	001725R	MEMTST	001426R	PRTY6 =	000300	SR3	000022R
ASTAT	000104R	DVID1	000014R	LOAD	001446R	PRTY7 =	000340	SR4	000024R
AWS	000110R	ENABKS=	000004	WODNAM	000000R	PS =	17176	STAT	000026R
BEGIN	000000R	ENDITS=	104413	MODSP	000224R	PUSH	005746	START	000028R
BIT1 =	000000	ENG8 =	104410	PPOINT	001706R	PUSH2 =	024646	SVH0	000006Z
BIT1 =	000002	ENTEX	000722R	PREAD	001476R	PHRUP =	002000	SVH1	0000064R
BIT10 =	002000	ENTZ	000622H	MMP1	001610R	HANDS =	104417	SVR2	000006R
BIT11 =	004000	ENTZ2	000740R	MMP10	001620R	RANNM	000054H	SVR3	000070R
BIT12 =	010000	ENTZ1	000662R	MMP11	001644R	REMLM	001734R	SVH4	000072R
BIT13 =	020000	ENT2	000302H	MRSTMT	001436R	REMMN	001710H	SVMS	000074R
BIT14 =	040000	ENT3	000350R	MRX	001662R	RESQD =	000000	SVR6	000076H
BIT15 =	100000	ENT4	000416H	MR1	001516R	RESAD =	001000	SYSCNT	000052R
BIT2 =	000004	ENT5	000456R	MR2	001572R	HES1	000056R	TABL1	001714R
BIT3 =	000010	ERFLG	001726R	MSGNS =	104403	HES2	000224R	TDRF	001220H
BIT4 =	000020	ERTYP	000106R	*SGSS =	104402	KES1	000060R	TDLK1	001362U
BIT5 =	000040	ERSUB	001664H	*SGS =	104401	KES2	000100R	TURX	001410R
BIT6 =	000100	EXCED	001730R	*SGO	001742R	RETRY	001004R	TDR0	001170H
BIT7 =	000200	EXIT\$ =	104400	*SGI	001777R	HSTR1	000112R	TDM1	001252R
BIT8 =	000400	FINI	000766R	*SG2	002032R	RTRO	001050R	TDM2	001332R
BIT9 =	001000	GETPAS=	104415	NULL =	000000	WTR1	001054R	TRPDFD=	000002
BREAK\$=	104407	*GBUF\$=	104414	OPEN =	000000	R6	%000006	TRY	001727H
BR1	000012R	HROCRN	000044R	OTOA\$ =	104420	K7	%000007	TSIDRP	001156R
BR2	000013R	HRODER\$=	104405	PASCNT	000034R	SBAOD	000102R	VECTOR	000010H
BTDD\$ *	104421	HROPAS	000050H	PIRQS =	000004	SETUP	001056R	WASADM	000104R
CDATA\$ =	104412	ICONT	000036R	POPSP =	005726	SDFCNT	000042R	wOFr	000116R
CLRTPB=	000020	ICOUNT	000040R	POPPSZ=	022626	SDFERS =	104406	WDTO	000114R
CMRINT	000534R	IDNUM	000122R	PRTY =	000000	SUFFAS	000046R	WHD0	001712H
CMRVEC	001702R	IE	= 000100	PRTY0 =	000000	SPAR	001716R	XFLAG	000005H
CNTR0	001704R								

```

. ABS. 000000 000
        002046 001
ERRORS DETECTED: 0

```

VIRTUAL MEMORY USED: 7986 WORDS (32 PAGES)
DYNAMIC MEMORY AVAILABLE FOR 66 PAGES
DK1:CMAA,CMAA/C/N:TTM=DK1:DDXCOM.P11,CMRDY

[illegible]

CHAA DEC/X11 SYSTEM EXERCISER M MACRO V03.02B11-NOV-80 11:04:54 PAGE S-3
CROSS REFERENCE TABLE (CREF V04.00)

[illegible]

.REM 2

IDENTIFICATION

PRODUCT CONF: AC-S260B-MC
PRODUCT NAME: CXCMB0 CSS CMS11K DEC/X MOD
DATE: 19 JUL 1980
UPDATE: 10 FEB 1981
MAINTAINER: CSS PRODUCT GROUP DIAGNOSTICS
AUTHOR(S): ROBERT J. COLLINS
UPDATED BY: JAMES M. DUPRE
VIJAY ANANDWALA

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1. ABSTRACT

CMA IS AN TCMOD THAT EXERCISES THE CMS11-K CARD READER. IT
READS A PREPRINTED MARKSENSE DECK, FORMING A CHECKSUM
FOR EACH CARD READ. THE CALCULATED CHECKSUM IS COMPARED
AGAINST A KNOWN CHECKSUM AND ANY ERRORS ARE REPORTED ON THE
TTY. THE MODULE TESTS BOTH THE IMAGE AND ENCODED DATA.

2. REQUIREMENTS

HARDWARE: CMS11-K READER AND INTERFACE
MARKSENSE CARD DECK - MAINDEC-11-DZCMF-C01

3. PASS DEFINITION

ONE PASS OF READING 80 80-COLUMN
CARDS (6400 CHARACTERS), AT WHICH TIME THE INPUT HOPPER
SHOULD BE EMPTY.

4. EXECUTION TIME

ONE PASS OF BBN RUNNING ALONE ON A PDP-11/40 TAKES
APPROXIMATELY 20 SECONDS.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:
DEVADR: 177160, VECTOR: 230, BR1: 6, DEVCNT: 1
REQUIRED PARAMETERS:
NONE

6. DEVICE/OPTION SETUP

- A. POWER UP THE READER
- B. LOAD THE CARD DECK
- C. READY THE READER

7. MODULE OPERATION

TEST SEQUENCE:

- A. SETUP DEVICE REGISTER ADDRESSES AND MODULE VARIABLES
- B. IF ON-LINE, GO TO C; ELSE WAIT FOR READY
- C. READ A CARD - ENABLE INTERRUPT
- D. INTERRUPT SERVICE:
 - 1. CHECK FOR NON-DATA ERRORS (FLING, ETC.)
 - 2. COUNT COLUMN
 - 3. FORM CHECKSUMS (DIRECT AND ENCODED)
 - 4. IF 80 COLUMNS READ: CHECK DATA - REPORT ANY ERRORS
- E. IF HOPPER NOT EMPTY COUNT A CARD AND GO TO B.
- F. AT HOPPER EMPTY (OFF-LINE) AND NO CARDS READ, REPORT
 END OF PASS AND START AT A. ELSE REPORT ERROR AND
 GO TO A.

8. OPERATION OPTIONS

NONE

9. NON-STANDARD PRINTOUTS

NONE

```

491                                     .LIST  SEQ,BIN,LOC
492
493 000000' 10*00 <CMAB >,177160,230,6,0,0,1
(1) 000000' MODULE 140000,CMAB ,177160,230,6,0,0,1,
(2)                                     .TITLE  CMAB DEC/X11 SYSTEM EXERCISER MODULE
(2)                                     : DDXCOM  VERSION 6      23-MAY-78
(2)                                     .LIST  BIN
(2)
(2) *****
(2) BEGIN:
(2) MODNAM: .ASCII /CMAB / :MODULE NAME.
(2) XFLAG: .BYTE  OPEN          :USED TO KEEP TRACK OF *BUFF USAGE
(2) ADDR: 177160+0             :1ST DEVICE ADDR.
(2) VECTOR: 230+0              :1ST DEVICE VECTOR.
(2) BR1: .BYTE  PRTY6+0        :1ST BR LEVEL.
(2) BR2: .BYTE  PRTY0+0        :2ND BR LEVEL.
(2) DVID1: 0+1                 :DEVICE INDICATOR 1.
(2) SR1: OPEN                  :SWITCH REGISTER 1
(2) SR2: OPEN                  :SWITCH REGISTER 2
(2) SR3: OPEN                  :SWITCH REGISTER 3
(2) SR4: OPEN                  :SWITCH REGISTER 4
(2) *****
(2) STAT: 140000                :STATUS WORD.
(2) INIT: START                :MODULE START ADDR.
(2) SPOINT: MUOSP              :MODULE STACK POINTER.
(2) PASCNT: 0                   :PASS COUNTER.
(2) ICONT: 1                   :# OF ITERATIONS PER PASS=1
(2) ICONT: 0                   :LOC TO COUNT ITERATIONS
(2) SUPCNT: 0                   :LOC TO SAVE TOTAL SOFT ERRORS
(2) HRDCNT: 0                   :LOC TO SAVE TOTAL HARD ERRORS
(2) SOFPAS: 0                   :LOC TO SAVE SOFT ERRORS PER PASS
(2) HRDPAS: 0                   :LOC TO SAVE HARD ERRORS PER PASS
(2) SYSCNT: 0                   :# OF SYS ERRORS ACCUMULATED
(2) RANUM: 0                    :HOLDS RANDOM # WHEN RAND MACRO IS CALLED
(2) CONFIG:
(2) RES1: 0                     :RESERVED FOR MONITOR USE
(2) RES2: 0                     :RESERVED FOR MONITOR USE
(2) SVR0: OPEN                  :LOC TO SAVE R0.
(2) SVR1: OPEN                  :LOC TO SAVE R1.
(2) SVR2: OPEN                  :LOC TO SAVE R2.
(2) SVR3: OPEN                  :LOC TO SAVE R3.
(2) SVR4: OPEN                  :LOC TO SAVE R4.
(2) SVR5: OPEN                  :LOC TO SAVE R5.
(2) SVR6: OPEN                  :LOC TO SAVE R6.
(2) CSRA: OPEN                  :ADDR OF CURRENT CSR.
(2) SBADR:                      :ADDR OF GOOD DATA, OR
(2) ACSR: OPEN                  :CONTENTS OF CSR.
(2) BASADR:                      :ADDR OF BAD DATA, OR
(2) ASTAT: OPEN                  :STATUS REG CONTENTS.
(2) ERRTP:                      :TYPE OF ERROR.
(2) ASB: OPEN                   :EXPECTED DATA.
(2) AWAS: OPEN                  :ACTUAL DATA.
(2) RSTRT: RSTRT               :RESTART ADDRESS AFTER END OF PASS
(2) *DTC: OPEN                  :WORDS TO MEMORY PER ITERATION
(2) *DPR: OPEN                  :WORDS FROM MEMORY PER ITERATION
(2) INTR: OPEN                  :# OF INTERRUPTS PER ITERATION

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(2) 000122'
(2) 000040
(2)
(2)
(2)
(2)
(2) 000222'
(2)

IDNUM: .REPT SPSIZ :MODULE IDENTIFICATION NUMBER=
.NLTST :MODULE STACK STARTS HERE.
.WORD 0
.LIST
.ENDR

MODSP: *****

495 000222'
496 000222' 016700 177560
497 000226' 010067 000766
498 000232' 005720
499 000234' 010067 000762
500 000240' 005720
501 000242' 010067 000756
502 000246' 016700 177536
503 000252' 012720 000426'
504 000256' 116710 177530
505
506 000262' 005077 000732
507 000266' 005067 000722
508 000272' 032777 000400 000720
509 000300' 001410
510 000302' 005767 177526
511 000306' 001005
512 000310' 004767 000562
513
(1) 000314' 104405 000000' 000000
(1)
514 000322' 005067 000670
515 000326' 005067 000656
516 000332' 005067 000654
517 000336' 012767 077777 000662
518 000344' 032777 000400 000646
519 000352' 001416
520 000354' 104407 000000'
(1) 000360' 104407 000000'
521 000364' 005367 000636
522 000370' 001365
523 000372' 004767 000500
524
(1) 000376' 104405 000000' 000000
(1)
525 000404' 104410 000000'
526
527 000410' 005267 000600
528 000414' 012777 000101 000576
529 000422' 104400 000000'
530
531
532
533
534 000426' 105777 000566
535 000432' 100011
536 000434' 067767 000562 000546
537 000442' 067767 000556 000542
538 000450' 005267 000542
539 000454' 000002

START:
RESTR: MOV ADDR,R0 :SETUP DEVICE
MOV R0,CRS
TST (0)+
MOV R0,CRB1
TST (0)+
MOV R0,CRB2
MOV VECTOR,R0
MOV #INTER,(0)+
MOVR BR1,(0)
;

CLR #CRS :CLR ANY JUNK
CLR CRUCNT :ZERO CARD COUNTER
BIT #BIT8,#CRS :HEADER ONLINE?
BEQ NUCARD :YES, BEGIN THE TEST
TST PASCNT :FIRST PASS?
BNE NUCARD :NO, DO NOT LOG ERROR YET
JSR PC,ERSUB :YES, LOAD ERROR INFORMATION
;*****
HRDEKS,BEGIN,NULL :READER OFF-LINE ... WAITING
;*****
NUCARD: CLR COLUMN :O COL. COUNTER
CLR SUM1 :O IMAGE SUM
CLR SUM2 :O ENCODED SUM
MOV #77777,CLK :SET CLOCK COUNTER
BIT #BIT8,#CRS :READY?
BEQ GO :YES, CONTINUE
BREAKS,BEGIN :TEMPORARY RETURN TO MONITOR....
HREKS,BEGIN :THEN CONTINUE AT NEXT INSTRUCTION.
DEC CLK :NO, WAIT SOME MORE
BNE IS :LOOP
JSR PC,ERSUB :LOAD ERROR INFO
;*****
HRDEKS,BEGIN,NULL :READER STILL NOT READY ... DROP
;*****
ENDS,BEGIN ;
GO: INC CRUCNT :COUNT A CARD
MOV #101,#CRS :ENABLE PI AND READ
EXITS,BEGIN :EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
;

;INTERRUPT SERVICE
INTER: ISTH #CRS :COLUMN READY TO BE READ?
BPL IS :NO, FIND OUT WHY
ADD #CRB1,SUM1 :ADD IMAGE TO SUM
ADD #CRB2,SUM2 :ADD ENCODED TO SUM
INC COLUMN :COUNT A COLUMN
RTI :EXIT

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541 000456' 004767 000414 1S: JSK PC.ERSUB :SAVE ERROR INFO
542 000462' 042777 000100 000530 BIC #BIT6,ACRS :DISABLE INTERRUPT
543 -----
(1) 000470' 000004 000000' 000476' PIRDS,BEGIN,WHY : QUEUE UP TO CONTINUE AT WHY AND RTI
(1) -----
544 000476' 005767 177400 WHY: TST ACSR :ERROR?
545 000402' 100444 BMI 2S :YES
546 000504' 032767 040000 177370 RIT #BIT14,ACSR :CARD DONE?
547 000512' 001027 BNE 1S :YES, CHECK SUMS
548 000514' 032767 002000 177360 BIT #BIT10,ACSR :TRANS. TO ONLINE?
549 000522' 001405 BEQ 5S :NO CONTINUE TESTING
550 -----
(1) 000524' 104405 000000' 000000 HRDERS,BEGIN,NULL :ON-LINE TRANSITION DETECTED!
(1) -----
551 000532' 000167 000356 JMP EJECT
552 000536' 032767 000400 177336 5S: BIT #BIT8,ACSR :HEADER READY?
553 000544' 001405 BEQ 11S :YES GO REPORT UNKNOWN INTERRUPT
554 -----
(1) 000546' 104405 000000' 000000 HRDERS,BEGIN,NULL :HEADER OFF-LINE !
(1) -----
555 000554' 000167 177542 JMP NUCARD
556 000560' 11S: -----
(1) 000560' 104405 000000' 000000 HRDERS,BEGIN,NULL :UNKNOWN INTERRUPT PROCESSED!
(1) -----
557 000566' 000167 177530 JMP NUCARD
558 -----
559 000572' 022767 000120 000416 1S: CMP #0, COLUMN :ALL COLUMNS CHECKED?
560 000600' 001451 BEQ CHECK :YES
561 -----
(1) 000602' 104405 000000' 000000 HRDERS,BEGIN,NULL :CARD DONE SET BEFORE ALL CUL. READ
(1) -----
562 000610' 000167 177506 JMP NUCARD :TRY ANOTHER CARD
563 000614' 032767 004000 177260 2S: BIT #BIT11,ACSR :TIMING ERROR?
564 000622' 001405 BEQ 3S :NO
565 -----
(1) 000624' 104405 000000' 000000 HRDERS,BEGIN,NULL :TIMING ERROR
(1) -----
566 000632' 000167 177464 JMP NUCARD :TRY ANOTHER
567 000636' 032767 010000 177236 3S: BIT #BIT12,ACSR :FEED ERROR?
568 000644' 001405 BEQ 31S :NO
569 -----
(1) 000646' 104405 000000' 000000 HRDERS,BEGIN,NULL :FEED ERROR
(1) -----
570 000654' 000167 177442 JMP NUCARD :TRY AGAIN
571 000660' 032767 020000 177214 31S: BIT #BIT13,ACSR :HOPPER EMPTY?
572 000666' 001411 BEQ 4S :BR=NO
573 000670' 022767 000121 000316 CMP #0, CRUCNT :DONE #0?
574 000676' 001473 BEQ FINI :BR=YES
575 -----
(1) 000700' 104405 000000' 000000 HRDERS,BEGIN,NULL :CARD COUNT NOT 80 OR STACKER FULL
(1) -----
576 000706' 000167 000154 JMP FINI
577 000712' 4S: -----
(1) 000712' 104405 000000' 000000 HRDERS,BEGIN,NULL :ERR RIT SET BY ITSELF!

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(1) -----
578 000720' 000167 177376 JMP NUCARD

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580 000724' 026767 000260 000252 CHECK: CMP SUM1,CKSUM1 :IS IMAGE SUM CORRECT?
581 000732' 001416 BFO 1$ :YES, CHECK ENCODED
582 000734' 012767 001204' 177140 MOV #CKSUM1,SHADR
583 000742' 016767 000236 177136 MOV CKSUM1,ASH
584 000750' 012767 001210' 177126 MOV #SUM1,ASADR
585 000756' 016767 000226 177124 MOV SUM1,AWAS
586 ;*****
(1) 000764' 104404 000000' DATFRS,BEGIN :DATA ERROR!!!
(1) ;*****
587
588 000770' 026767 000216 000210 1$: CMP SUM2,CKSUM2 :IS ENCODED CORRECT?
589 000776' 001416 BEQ 2$ :YES
590 001000' 012767 001206' 177074 MOV #CKSUM2,SHADR
591 001006' 016767 000174 177072 MOV CKSUM2,ASH
592 001014' 012767 001212' 177062 MOV #SUM2,ASADR
593 001022' 016767 000164 177060 MOV SUM2,AWAS
594 ;*****
(1) 001030' 104404 000000' DATFRS,BEGIN :DATA ERROR!!!
(1) ;*****
595
596 001034' 032777 000400 000156 2$: BIT #BIT8,ACRS :ON-LINE?
597 001042' 001407 BEQ 3$ :BR=YES
598 001044' 022767 000121 000142 CMP #81,,CRDCNT :DONE 80 CARDS?
599 001052' 001405 BEQ FINI :BR=YES
600 ;*****
(1) 001054' 104405 000000' 000000 HRDERS,BEGIN,NULL :READER OFF-LINE CHECK READER
(1) ;*****
601 001062' 000167 177234 3$: JMP NUCAKD :GET NEXT
602 001066' FINI:
(1) 001066' 104413 000000' ENDITS,BEGIN :SIGNAL END OF ITERATION.
(1) 001072' 000167 177124 JMP HESTRT :MONITOR SHALL TEST END OF PASS
604
605 001076' 016767 000116 176774 ERSUB: MOV CRS,CSRA :LOAD ERROR INFO
606 001104' 017767 000110 176770 MOV #CRS,ACSR
607 001112' 000207 RTS PC :EXIT
608
609
610 001114' 012777 001140' 176666 EJECT: MOV #2$,WVECTION :LOAD NEW RETURN
611 001122' 005077 000072 CLR #CRS :CLEAR IT
612 001124' 012777 000103 000064 1$: MOV #103,#CRS :SET I.E. READ+EJECT
613 001134' 104400 000000' EXITS,BEGIN :EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
614 001140' 017767 000054 176734 2$: MOV #CRS,ACSR :SAVE CRS
615 001146' 042777 000100 000044 BIC #100,#CRS :DISABLE INTERRUPTS
616 ;-----
(1) 001154' 000004 000000' 001162' PIRDS,BEGIN,3$ : QUEUE UP TO CONTINUE AT 3$ AND RTI
(1) ;-----
617 001162' 032767 020000 176712 3$: BIT #BIT13,ACSR :HOPPER EMPTY?
618 001170' 001756 BEQ 1$ :RR=NO
619 001172' 012777 000426' 176610 MOV #INTER,WVECTION :RESTORE OLD RETURN
620 001200' 000167 177662 JMP FINI
621
622 001204' 107767 CKSUM1: 107767
623 001206' 004573 CKSUM2: 4573
624 001210' 000000 SUM1: 0
625 001212' 000000 SUM2: 0

```

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626 001214' 000000 CRDCNT: 0
627 001216' 000000 COLUMN: 0
628 001220' 000000 CRS: 0
629 001222' 000000 CRB1: 0
630 001224' 000000 CRB2: 0
631 001226' 000000 CLK: 0
632
633 000001 .END :THAT'S ALL FOLKS!

```

CMAB DFC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 10-MAR-81 15:18 PAGE 9-1
CXCMAB.SRC 10-MAR-81 10:34 CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0012

[illegible]

502 610* 619*
584* 592*

544*

CXCMA8,CXCMA8.SEG/CRF=DDXCOM.C6,CXCMA8.SRC
RUN-TIME: 6 8 .9 SECONDS
RUN-TIME RATIO: 97/15=6.2
CORE USED: 7K (13 PAGES)

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.REM_

PRODUCT CODE: AC-S257A-MC

PRODUCT NAME: CXDRKAO CSS DR70 DEC/X MOD

DATE: 15 -JUL -1980

AUTHOR(S): JAMES M. DUPRE

MAINTAINER: C.S.S. PRODUCT GROUP

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MAYNARD MASS.

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ABSTRACT:

DRK IS AN IUMUDX WHICH EXERCISES THE DR70 INTERFACE
BOTH WRITE AND READ FUNCTIONS ARE EXERCISED.
THIS MODULE IS PDP11/70 COMPATABLE.

RUNTIME/PASS: 1MINUTE/PASS

SWITCH REGISTER VALUES: NONE

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REQUIREMENTS:

A. SOFTWARE

LOCATION DVID1 MUST BE SET TO THE UNIT UNDER TEST
EXAMPLE: BIT0=UNIT 0,BIT2=UNIT1,ETC.

B. HARDWARE

THE OUTPUT AND INPUT OF THIS INTERFACE MUST BE
WRAPPED AROUND I.E. NOT CONNECTED TO AN EXTERNAL
DEVICE.

THIS MODULE RUN THE DR70 IN READ AND WRITE
OPERATIONAL MODE.

C. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DEVADR: 176700,VECTOR:254,BRI:4

REQUIRED PARAMETERS: NONE

463
464
465
466
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468
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STARTING PROCEDURE:

A. LOAD AND START THE DEC/X11 PROGRAM ACCORDING TO THE
DEC/X11 USERS HANDBOOK

B. INSURE THAT THE DEVICE ADDRESS AND VECTOR ADDRESS ARE
CORRECT FOR THE PARTICULAR DR70 IN TEST

C. SET LOCATION DVID1 AS DESCRIBED IN THE REQUIREMENTS
SECTION OF THIS DOCUMENT.

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PRELIMINARY OPERATIONS:

BE CERTAIN THAT THE INTERFACE IS WRAPPED AROUND AS
SPECIFIED IN THE REQUIREMENTS SECTION

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ERRORS:

ALL ERRORS ARE IN STANDARD DEC/X11 FORMAT
THE ORDER OF REGISTER PRINTOUT ON ERROR IS
AS FOLLOWS:

RHCS1 RHCS2 RHWC RHBA RHIS

```
496
497 000000'
(1) 000000'
(2)
(2)
(2)
(2)
(2) 000000'
(2) 000000' 051104 040513 040
(2) 000005' 000
(2) 000006' 176700
(2) 000010' 000254
(2) 000012' 200
(2) 000013' 000
(2) 000014' 000001
(2) 000016' 000000
(2) 000020' 000000
(2) 000022' 000000
(2) 000024' 000000
(2)
(2) 000026' 150000
(2) 000030' 000260'
(2) 000032' 000252'
(2) 000034' 000000
(2) 000036' 030000
(2) 000040' 000000
(2) 000042' 000000
(2) 000044' 000000
(2) 000046' 000000
(2) 000050' 000000
(2) 000052' 000000
(2) 000054' 000000
(2) 000056'
(2) 000056' 000000
(2) 000060' 000000
(2) 000062' 000000
(2) 000064' 000000
(2) 000066' 000000
(2) 000070' 000000
(2) 000072' 000000
(2) 000074' 000000
(2) 000076' 000000
(2) 000100' 000000
(2) 000102'
(2) 000102' 000000
(2) 000104'
(2) 000104' 000000
(2) 000106'
(2) 000106' 000000
(2) 000110' 000000
(2) 000112' 000252'
(2) 000114' 000000
(2) 000116' 000000
(2) 000120' 000000
(2) 000122' 000000
(2) 000124' 001436'

      IGMUDX<DRKA>176700,254,4,,,30000,0,RBUF,100,100
      MODULE 150000,DRKA,176700,254,4,,,30000,0,RBUF,100,100
      .TITLE DRKA DEC/X11 SYSTEM EXERCISER MODULE
      ; DDACUM VERSION 6 23-MAY-78
      .LIST BIN
      ;*****
      BEGIN:
      MUDNAM: .ASC11 /DRKA / ;MODULE NAME.
      XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
      ADDR: 176700+0 ;1ST DEVICE ADDR.
      VECTOM: 254+0 ;1ST DEVICE VECTOM.
      BR1: .BYTE PRTY4+0 ;1ST BR LEVEL.
      BR2: .BYTE PRTY+0 ;2ND BR LEVEL.
      DVID1: +1 ;DEVICE INDICATOR 1.
      SR1: OPEN ;SWITCH REGISTER 1
      SR2: OPEN ;SWITCH REGISTER 2
      SR3: OPEN ;SWITCH REGISTER 3
      SR4: OPEN ;SWITCH REGISTER 4
      ;*****
      STAT: 150000 ;STATUS WORD.
      INIT: START ;MODULE START ADDR.
      SPOINT: MODDSP ;MODULE STACK POINTER.
      PASCNT: 0 ;PASS COUNTER.
      ICNT: 30000 ;# OF ITERATIONS PER PASS=30000
      ICOUNT: 0 ;LOC TO COUNT ITERATIONS
      SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
      HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
      SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
      HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
      SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
      RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
      CONFIG:
      RES1: 0 ;RESERVED FOR MONITOR USE
      RES2: 0 ;RESERVED FOR MONITOR USE
      SVR0: OPEN ;LOC TO SAVE R0.
      SVR1: OPEN ;LOC TO SAVE R1.
      SVR2: OPEN ;LOC TO SAVE R2.
      SVR3: OPEN ;LOC TO SAVE R3.
      SVR4: OPEN ;LOC TO SAVE R4.
      SVR5: OPEN ;LOC TO SAVE R5.
      SVR6: OPEN ;LOC TO SAVE R6.
      CSRA: OPEN ;ADDR OF CURRENT CSR.
      SBADR: ;ADDR OF GOOD DATA, OR
      ACSR: OPEN ;CONTENTS OF CSR.
      WASADR: ;ADDR OF BAD DATA, OR
      ASTAT: OPEN ;STATUS REG CONTENTS.
      ERRTYP: ;TYPE OF ERROR
      ASB: OPEN ;EXPECTED DATA.
      AWAS: OPEN ;ACTUAL DATA.
      RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
      WDTO: OPEN ;WORDS TO MEMORY PER ITERATION
      WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
      INTR: OPEN ;# OF INTERRUPTS PER ITERATION
      IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0
      RBUFVA: RBUF ;READ BUFFER VIRTUAL ADDRESS
```

```
(2) 000126' 000000
(2) 000130' 000000
(2) 000132' 000100
(2) 000134' 000000
(2) 000136' 000000
(2) 000140' 000100
(2) 000142' 000000
(2) 000144' 000000
(2) 000146' 000000
(2) 000150' 000000
(2) 000040
(2)
(2)
(2)
(3)
(2) 000252'
(2)
498
499 000252' 005767 177556
500 000256' 001065
501 000260' 016702 177522
502 000264' 010267 001054
503 000270' 005722
504 000272' 010267 001052
505 000276' 005722
506 000300' 010267 001046
507 000304' 022222
508 000306' 010267 001034
509 000312' 005722
510 000314' 010267 001036
511 000320' 005067 001044
512 000324' 052777 002000 001012
513 000332' 032777 002000 001004
514 000340' 001006
515 000342' 005167 001022
516 000346' 062702 000016
517 000352' 010267 000776
518 000356' 016767 177426 000774 18:
519 000364' 016767 000770
520 000372' 062767 000002 000762
521 000400' 116777 177406 000754
522 000406' 012700 001344'
523 000412' 012701 001402'
524 000416' 012021
525 000420' 022700 001354'
526 000424' 001374
527 000426' 016711 000724
528 000432' 012777 000740' 000720
529 000440' 016767 177350 000724
530 000446' 012767 000001 000720
531 000454' 005067 000716
532 000460' 036767 000710 000704 38:
533 000466' 001011
534 000470' 000241
535 000472' 006167 000676
536 000476' 005267 000674

      RBUFPA: OPEN ;READ BUFFER PHYSICAL ADDRESS
      RBUFEA: OPEN ;READ BUFFER EA BITS
      RBUFSZ: 100 ;SIZE OF THE READ BUFFER
      WBUFPA: OPEN ;WRITE BUFFER PHYSICAL ADDRESS
      WBUFEA: OPEN ;WRITE BUFFER EA BITS
      WBUFRQ: 100 ;WRITE BUFFER SIZE REQUESTED
      WBUFSZ: OPEN ;WRITE BUFFER SIZE AVAILABLE
      CDERRCI: OPEN ;C/DATA/DATCK ERROR COUNT
      CDWDCT: OPEN ;C/DATA/DATCK WORD COUNT
      FREE: OPEN ;RESERVED FOR FUTURE USE
      .REPT SPSIZ ;MODULE STACK STARTS HERE.
      .NLIST
      .WORD 0
      .LIST
      .ENDR
      MODDSP:
      ;*****
      RSTRT: TST PASCNT ;ANY PASSES YET?
      BNE RST1 ;YES GO RESTART
      START: MOV ADDR,R2 ;LOAD DEVICE REGISTER STORAGE
      MOV R2,RHCS1
      TST (R2)+
      MOV R2,RHWC
      TST (R2)+
      MOV R2,RHBA
      CMP (R2)+,(R2)+
      MOV R2,RHCS2
      TST (R2)+
      MOV R2,RHIS
      CLR FLG70
      BIS #BIT10,#RHCS1 ;CHECK FOR PDP-11/70
      ;BY TRYING TO SET PSEL
      ;NOT 11/70 IF SET
      BNE 18
      CUM FLG70 ;SET FLG70,-1=11/70
      ADD #16,R2
      MOV R2,RHBAE
      VECT,VCT ;LOAD VECTOR STORAGE
      VCT,VCTL
      #2,VCTL
      MOV BR1,#VCTL ;SET BREAK LEVEL
      #RHCS1,R0 ;LOAD ERROR TABLE#1
      MOV #TAB1,R1
      MOV (R0)+,(R1)+
      CMP #RHBAE,R0
      BNE 28
      MOV RHIS,#R1
      MOV #READ,#VCT ;SET RETURN
      DVID1,DEVVNT ;SAVE DRIVE #
      MOV #1,POINT ;SET POINT
      CLR DRVEM ;AND DRIVE NUMBER
      BIT POINT,DEVVNT ;IS IT THERE?
      BNE 38 ;BR=YES
      CLC ;STEP POINTER
      MOV POINT
      DRVEM
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537 000502' 022767 000011 000666      CMP    #11,DRVEN      ;MAXED OUT?
538 000510' 001363                      BNE     35             ;BR=NO
539 000512' 016777 000660 000626 338:  MOV    DRVEN,0RHCS2    ;LOAD DRIVE #
540 000520' 012777 000011 000616      MOV    #11,0RHCS1     ;CLEAR DEVICE
541 000526' 052777 000040 000612      BIS     #40,0RHCS2     ;CLEAR CONTROLLER
542 000534' 104414 000000'             GMBUFS, BEGIN      ;GET WRITE BUFFER INFORMATION
543 000540' 005767 000624             TST     FLG70          ;11/70?
544 000544' 001425                      BEQ     15             ;BR=NO
545 000546' 016767 177362 000642      MOV    WBUFPA,WAD70    ;GET 11/70 22 BIT ADDRESSES
546 000554' 016767 177356 000636      MOV    WBUFEA,WAD70X
547 000562' 104416 000000' 001416'    MAP22$, BEGIN,WAD70      ; GET 22-BIT ADDR FROM 18-BIT ADDR
548 000570' 016777 000626 000554      MOV    WAD22,0RHBA     ;LOAD BUSS ADDRESS REGISTER
549 000576' 016777 000622 000550      MOV    WAD22X,0RHBAE   ;AND EXTENDED ADDRESS BITS
550 000604' 005067 000570             CLR     FUNC          ;CLEAR FUNCTION WORD
551 000610' 052767 100000 000102      BIS     #100000,4$     ;MAKE IT A MOV8 INSTRUCTION
552 000616' 000425                      BR      2$             ;CONTINUE #2$
553 000620' 016777 177310 000524 1$:  MOV    WBUFPA,0RHBA     ;LOAD BUSS ADDRESS REGISTER
554 000626' 016767 177304 000530      MOV    WBUFEA,WBEA     ;POSITION E.A. BITS
555 000634' 042767 100000 000056      BIC     #100000,4$     ;MAKE IT A MOV INSTRUCTION
556 000642' 000241                      CLC
557 000644' 006167 000514             ROL     WBEA
558 000650' 006167 000510             ROL     WBEA
559 000654' 006167 000504             ROL     WBEA
560 000660' 006167 000500             ROL     WBEA
561 000664' 016767 000474 000506      MOV    WBEA,FUNC       ;LOAD E.A. BITS
562 000672' 016767 177244 177232 2$:  MOV    WBUFSZ,RBUFSZ
563 000700' 016777 177236 000442      MOV    WBUFSZ,0RHWC    ;LOAD WORD COUNT AND
564 000706' 005477 000436             NEG     0RHWC          ;NEGATE IT
565 000712' 062767 000161 000460      ADD     #161,FUNC      ;ADD WRITE AND GO + I.E. BITS
566 000720' 016777 000454 000416 4$:  MOV    FUNC,0RHCS1     ;GO!!(IF 11/70 THIS WILL BE A MOV8.)
567 000726' 052777 000020 000422      BIS     #20,0RHIS     ;PRIME CYCLE
568 000734' 104400 000000'             EXIT$,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

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570
571 000740' 042777 000100 000376 READ: BIC     #100,0RHCS1    ;DROP I.E. BIT
572 000746' 004767 000324             JSR     PC,ERROR      ;GO CHECK FOR ERROR
573
574 000752' 000004 000000' 000760'     ;-----
575 000760' 012777 001176' 000372 1$:  ;PIQS,BEGIN,1$      ; QUEUE UP TO CONTINUE AT 1$ AND RTI
576 000766' 032777 000200 000362      ;-----
577 000774' 001774                      MOV     #CMPLT,0VCT     ;SET RETURN
578 000776' 104415 000000' 000124'    BIT     #200,0RHIS    ;DEVICE READY?
579 000780' 005767 000360             BEQ     -6             ;LOOP UNTIL READY
580 000786' 001425                      GETPAS,BEGIN, RBUFVA    ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
581 000792' 016767 177110 000406      TST     FLG70          ;11/70
582 000798' 016767 177104 000402      BEQ     11$           ;BR=NO
583 000804' 104416 000000' 001426'    MOV    RBUFPA,RAD70    ;GET 22 BIT ADDRESS
584 000810' 016777 000372 000310      MAP22$, BEGIN,RAD70      ; GET 22-BIT ADDR FROM 18-BIT ADDR
585 000816' 016777 000366 000304      MOV    RAD22,0RHBA     ;LOAD BUSS ADDRESS REGISTER
586 000822' 005067 000324             MOV    RAD22X,0RHBAE   ;AND EXTENDED ADDRESS BITS
587 000828' 052767 100000 000074      CLR     FUNC          ;CLEAR FUNCTION WORD
588 000834' 016777 177036 000260 11$:  BIS     #100000,3$     ;MAKE IT A MOV8 INSTRUCTION
589 000840' 016767 177032 000266      BR      2$             ;CONTINUE # 2$
590 000846' 042767 100000 000050      MOV    RBUFPA,0RHBA     ;LOAD BUSS ADDRESS
591 000852' 000241                      MOV    RBUFEA,RBEA     ;POSITION E.A. BITS
592 000858' 006167 000252             BIC     #100000,3$     ;MAKE IT A MOV INSTRUCTION
593 000864' 006167 000246             CLC
594 000870' 006167 000242             ROL     RBEA
595 000876' 006167 000236             ROL     RBEA
596 000882' 016767 000232 000242      ROL     RBEA
597 000888' 016777 176770 000204 2$:  MOV    RBEA,FUNC       ;LOAD E.A. BITS
598 000894' 005477 000200             MOV    RBUFSZ,0RHWC    ;LOAD WORD COUNT AND
599 000900' 062767 000171 000222      NEG     0RHWC          ;NEGATE IT
600 000906' 016777 000216 000160 3$:  ADD     #171,FUNC      ;ADD READ AND GO + I.E.
601 000912' 052777 000020 000164      MOV    FUNC,0RHCS1     ;GO!!(IF 11/70 THIS WILL BE A MOV8.)
602 000918' 104400 000000'             BIS     #20,0RHIS     ;PRIME CYCLE
                                EXIT$,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

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SEQ 0011

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604
605 001176' 042777 000100 000140 CMPLT: BIC #100, @RHCS1 ;DROP I.E. BIT
606 001204' 004767 000066 JSR PC, ERROR ;GO CHECK FOR ERROR
607 -----
(1) 001210' 000004 000000' 001216' PIRQS, BEGIN, 1S ; QUEUE UP TO CONTINUE AT 1S AND RTI
(1) -----
608 001216' 032777 000200 000132 1S: BIT #200, @RHIS ;CHECK FOR DEVICE READY
609 001224' 001774 BEQ 1S ;LOOP UNTIL READY
610 001226' 104412 000000' 000126' CDATAS, BEGIN, RBUFP A ; REQUEST FOR MONITOR TO CHECK DATA
(1) 001234' 001236' .+2 ; IF ERROR, CONTINUE
611 001236' 104407 000000' BREAKS, BEGIN ;TEMPORARY RETURN TO MONITOR....
(1) 001242' 104407 000000' BREAKS, BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
612 001246' 104407 000000' BREAKS, BEGIN ;TEMPORARY RETURN TO MONITOR....
(1) 001252' 104407 000000' BREAKS, BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
613 001256' 104407 000000' BREAKS, BEGIN ;TEMPORARY RETURN TO MONITOR....
(1) 001262' 104407 000000' BREAKS, BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
614 001266' FINI:
(1) 001266' 104413 000000' ENDIT$, BEGIN ;SIGNAL END OF ITERATION.
(1) ;MONITOR SHALL TEST END OF PASS
615 001272' 000167 177134 JMP REST1
  
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SEQ 0012

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617 001276' 005777 000042 ERROR: TST @RHCS1 ;ERROR PRESENT?
618 001302' 100401 BMI 1S ;BX=YES
619 001304' 000207 RTS PC ;RETURN OK
620 001306' 016767 000032 176564 1S: MOV RHCS1, CSHA ;LOAD ERROR INFO
621 001314' 017767 000024 176560 MOV @RHCS1, ACSR
622 001322' 017767 000020 176554 MOV @RHCS2, ASTAT
623 ;*****
(1) 001330' 104405 000000' 001402' HKDEFS, BEGIN, TAB1 ;
(1) ;*****
624 -----
(1) 001336' 000004 000000' 000432' PIRQS, BEGIN, REST1 ; QUEUE UP TO CONTINUE AT REST1 AND RTI
(1) -----
  
```

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626
627
628 001344' 000000 RHCS1: 000
629 001346' 000000 RHCS2: 000
630 001350' 000000 RHWC: 000
631 001352' 000000 RHBA: 000
632 001354' 000000 RmBAE: 000
633 001356' 000000 RHIS: 000
634 001360' 000000 VCT: 000
635 001362' 000000 VCTL: 000
636 001364' 000000 WBEA: 000
637 001366' 000000 RBEA: 000
638 001370' 000000 FLG70: 000
639 001372' 000000 DEVCNT: 000
640 001374' 000000 POINT: 000
641 001376' 000000 DRVEN: 000
642 001400' 000000 FUNC: 000
643
644
645 001402' 001344' TAB1: RHCS1
646 001404' 001346' RHCS2
647 001406' 001350' RHWC
648 001410' 001352' RHBA
649 001412' 001356' RHIS
650 001414' 177777 -1
651
652
653
654 001416' 000000 WAD70: 000
655 001420' 000000 WAD70X: 000
656 001422' 000000 WAD22: 000
657 001424' 000000 WAD22X: 000
658
659 001426' 000000 RAD70: 000
660 001430' 000000 RAD70X: 000
661 001432' 000000 RAD22: 000
662 001434' 000000 RAD22X: 000
663
664 001436' 000156 RBUF: .RLKW 110.
665
666 000001 .END

```

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ACSR 000102R 497# 621#
ADDR 000006R 497# 501
ADDR22= 001000 497#
ASB 000106R 497#
ASTAT 000104R 497# 622#
AWAS 000110R 497#
BEGIN 000000R 497# 542 547 568 573 577 582 602 607 610 611 612 613
614 623 624
BIT0 = 000001 497#
BIT1 = 000002 497#
BIT10 = 002000 497# 512 513
BIT11 = 004000 497#
BIT12 = 010000 497#
BIT13 = 020000 497#
BIT14 = 040000 497#
BIT15 = 100000 497#
BIT2 = 000004 497#
BIT3 = 000010 497#
BIT4 = 000020 497#
BIT5 = 000040 497#
BIT6 = 000100 497#
BIT7 = 000200 497#
BIT8 = 000400 497#
BIT9 = 001000 497#
BREAKS= 104407 497# 611 612 613
BR1 000012R 497# 521
BR2 000013R 497#
BTODS = 104421 497#
CDATAS= 104412 497# 610
CDERCT 000144R 497#
CDWDCI 000146R 497#
CMPLT 001176R 574 605#
CONFIG 000056R 497#
CSRA 000100R 497# 620#
DATCKS= 104411 497#
DATERS= 104404 497#
DEVCNT 001372R 529# 532 639#
DRVEN 001376R 531# 536# 539 641#
DVIDI 000014R 497# 529
ENDIFS= 104413 497# 614
ENDS = 104410 497#
ERRUR 001276R 572 606 617#
ERRTYP 000106R 497#
EXIT$ = 104400 497# 568 602
FINI 001266R 614#
FLG70 001370R 511# 543 578 638#
FREE 000150R 497#
FUNC 001400R 550# 561# 565# 566 585# 596# 599# 600 642#
GETPAS= 104415 497# 577
GWBUFS= 104414 497# 542
HRDCNT 000044R 497#
HRDERS= 104405 497# 623
HRDPAS 000050R 497#
ICONT 000036R 497#
ICUUNT 000040R 497#
IDNUM 000122R 497#

```

SEQ 0016

[illegible]

BKMOD	95#			
BREAK	185#	611	612	613
BTOD	204#			
CKDAIA	240#	610		
DATACK	249#			
DATERR	138#			
DFSEVN	272#	497		
DSEVNT	282#	497		
END	175#			
ENDIT	166#	614		
ENDMOD	171#			
EQUATS	288#	497		
EXIT	120#	568	602	
GETPA	231#	577		
GWBUFF	219#	542		
HRDER	128#	623		
IOMOD	91#			
IOMODP	115#			
IOMODR	111#			
IOMODX	107#	497		
MAP22	235#	547	582	
MODULE	8#	497		
MSG	154#			
MSGN	158#			
MSGS	162#			
NBKMUD	103#			
OTOA	190#			
PIRQ	179#	573	607	624
RAND	124#			
SBKMUD	99#			
SOFER	144#			

. ABS. 000000 000
 001772 001

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

CXDRKA,CXDRKA.LST/CRF=DDXCOM.C6,CXDRKA.SRC
 RUN-TIME: 3 4 .8 SECONDS
 RUN-TIME RATIO: 38/9=4.1
 CORE USED: 7K (13 PAGES)

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.REM_

PRODUCT CODE: AC-S254A-MC

PRODUCT NAME: CXRHAA0 CSS RH01 DEC/X MOD

DATE: 15 JUL 1980

AUTHOR(S): JAMES M. DUPRE

UPDATE AUTHOR(S):JAMES M. DUPRE

MAINTAINER: C.S.S. PRODUCTS GROUP

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RHAA DEC/X11 SYSTEM EXERCISER MODULE
CXRHAA.SRC 25-AUG-80 11:18

MACY11 30A(1052) 25-AUG-80 11:20 PAGE 3

SEQ 0002

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ABSTRACT:

RHA IS AN SBKMOD THAT EXERCISES THE RH01 MASS BUSS SWITCH.
RHA ALSO CONTROLS THE MODULES THAT ACCESS THE RH01 SWITCH
UP TO NINE (9) MODULES PER RHA MODULE.

THE RHA MODULE CONTROLS THE MODULES FOR ANY DEVICES WHICH
RESIDE ON THE SHARED BUS.UPTO 9 MODULES CAN BE
CONTROLLED BY RHA.

RHAA DEC/X11 SYSTEM EXERCISER MODULE
CXRHAA.SRC 25-AUG-80 11:18

MACY11 30A(1052) 25-AUG-80 11:20 PAGE 4

SEQ 0003

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REQUIREMENTS:

RH01 AND AT LEAST ONE DEVICE TO ACCESS IT.
TWO SYSTEMS WITH RH01 AND AT LEAST ONE MASSBUS
DEVICE ON THE SHARED BUS.

THE RH01 MUST BE IN DUAL ACCESS MODE.

443 3 .0

PRELIMINARY OPERATIONS:

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1.INSERT MODULE POSITION (OF ACCESSING DEVICES) STARTING
AT LOCATION DVP1

2.IF EXERCISER IS TO CONNECT THE SHARED BUS ON THE
FIRST PASS A ONE MUST BE SET IN LOCATION "SWITCH"(IN
THIS MODULE RELATIVE ADDRESS 1000)

IT IS RECOMMENDED THAT THE RHA MODULE BE
SINKED IN FRONT OF THE MODULES FOR THOSE DEVICES WHICH
WILL BE RUN ON THE SHARED BUS.FOR EXAMPLE, A SYSTEM
CONTAINS AN RH01 AND A TU45 MAGTAPE DRIVE ON THE SHARED
BUS. THE MAP OF THE RUNTIME SYSTEM WOULD LOOK LIKE
THE ONE GIVEN BELOW.

THE RHA RH01 MODULE IS IN FRONT OF THE MODULE
FOR TU45.THE PHYSICAL POSITION OF THE TAPE DRIVE MODULE
IS THEN DETERMINED RELATIVE TO THE RH01 MODULE AND THAT
POSITION NUMBER (I.E. 1 IN THIS CASE)IS INSERTED IN TO
MODULE LOCATION DVP1.(MODULE RELATIVE POSITION 754).THIS
IS DONE WITH <MOD> COMMAND.

EX: MOD RHAA0 754 <CR>

THE MODULE RETURNS THE PHYSICAL ADDRESS OF THAT LOCATION
AND THE CONTENTS OF THAT ADDRESS.THEN TYPE IN A 1 <CR>.
EXAMPLE:

AFTER LINKING, RUN THIS EXERCISER.

.R DECX11

CMD> ;;WILL PROMPT AFTER SOME MESSAGES
CMD>MAP ;;COMMAND TO PRINT MAP ON CONSOLE

CMD>

RHAA0	AT VA:057266	STAT:040000
TMBJ0	AT VA:063006	STAT:150000
RKAG0	AT VA:072570	STAT:150000
CPAG0	AT VA:061124	STAT:040020

ALL OF THE ABOVE MODULES ARE LISTED RELATIVE TO THE TOP
MODULE RHAA0, SO RELATIVE POSITION OF THE TOP
MODULE RHAA0 IS ZERO AND RELATIVE POSITION OF
TMBJ0 MODULE IS ONE, RKAG0 MODULE IS NOT ON
THE SHARED BUS SO IT IS NOT INCLUDED.ONLY MODULE
ON THE SHARED BUS WILL BE INCLUDED.
SO ON.

IT IS ALWAYS GOOD PRACTICE TO PUT THE RHAA0 MODULE

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AT THE TOP.

IN THE ABOVE EXAMPLE ONLY ONE MODULE
IS ASSOCIATED WITH THE RHAA0. INSERT RELATIVE
POSITION OF THIS MODULE AT THE LOCATION "DVP1"
AS SHOWN BELOW. USE <MOD> COMMAND TO INSERT
THIS VALUE.

DVP1: 1

;;INCLUDE TMBJ0 MODULE

```

512
513
514 000000'
(1) 000000'
(2)
(2)
(2)
(2)
(2) 000000'
(2) 000000' 044122 040501 040
(2) 000005' 000
(2) 000006' 000000
(2) 000010' 000000
(2) 000012' 000
(2) 000013' 000
(2) 000014' 000001
(2) 000016' 000000
(2) 000020' 000000
(2) 000022' 000000
(2) 000024' 000000
(2)
(2) 000026' 040000
(2) 000030' 000224'
(2) 000032' 000224'
(2) 000034' 000000
(2) 000036' 000001
(2) 000040' 000000
(2) 000042' 000000
(2) 000044' 000000
(2) 000046' 000000
(2) 000050' 000000
(2) 000052' 000000
(2) 000054' 000000
(2) 000056'
(2) 000056' 000000
(2) 000060' 000000
(2) 000062' 000000
(2) 000064' 000000
(2) 000066' 000000
(2) 000070' 000000
(2) 000072' 000000
(2) 000074' 000000
(2) 000076' 000000
(2) 000100' 000000
(2) 000102'
(2) 000102' 000000
(2) 000104'
(2) 000104' 000000
(2) 000106'
(2) 000106' 000000
(2) 000110' 000000
(2) 000112' 000224'
(2) 000114' 000000
(2) 000116' 000000
(2) 000120' 000000
(2) 000122' 000000

      .GLOBAL MODQ
      SBKMOD <RHAA >0,0,0,0,0,1,0
      MODULE 40000,RHAA ,0,0,0,0,0,1,0
      .TITLE RHAA DEC/X11 SYSTEM EXERCISER MODULE
      ; DDSCOM VERSION 6 23-MAY-78
      .LIST BIN
;*****
BEGIN:
MODNAM: .ASCII /RHAA / ;MODULE NAME.
XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUF USAGE
ADDR: 0+0 ;1ST DEVICE ADDR.
VECTOR: 0+0 ;1ST DEVICE VECTOR.
BR1: .BYTE PRTY0+0 ;1ST BR LEVEL.
BR2: .BYTE PRTY0+0 ;2ND BR LEVEL.
DVID1: 0+1 ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1
SR2: OPEN ;SWITCH REGISTER 2
SR3: OPEN ;SWITCH REGISTER 3
SR4: OPEN ;SWITCH REGISTER 4
;*****
STAT: 40000 ;STATUS WORD.
INIT: START ;MODULE START ADDR.
SPDINT: MODSP ;MODULE STACK POINTER.
PASCNT: 0 ;PASS COUNTER.
ICONT: 1 ;# OF ITERATIONS PER PASS=1
ICOUNT: 0 ;LOC TO COUNT ITERATIONS
SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
HMDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
CONFIG:
RES1: 0 ;RESERVED FOR MONITOR USE
RES2: 0 ;RESERVED FOR MONITOR USE
SVR0: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVR5: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSR.
SBADH: ;ADDR OF GOOD DATA, OR
;CONTENTS OF CSR.
ASTAT: OPEN ;ADDR OF BAD DATA, OR
;STATUS REG CONTENTS.
ERRTP: ;TYPE OF ERROR
ASB: OPEN ;EXPECTED DATA.
AWAS: OPEN ;FACTUAL DATA.
RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
WDTO: OPEN ;WORDS TO MEMORY PER ITERATION
WDFK: OPEN ;WORDS FROM MEMORY PER ITERATION
INTR: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0

```

```

(2)
(2) 000040
(2)
(2)
(2)
(3)
(2) 000224'
(2)
515
516 000224'
517 000224' 005000
518 000226' 005001
519 000230' 005002
520 000232' 005003
521 000234' 012700 000732'
522 000240' 005020
523 000242' 022700 000754'
524 000246' 001374
525 000250' 012700 000754'
526 000254' 012701 000732'
527 000260' 005710
528 000262' 100421
529 000264' 001410
530 000266' 012003
531 000270' 006303
532 000272' 016321 000000G
533 000276' 022700 000776'
534 000302' 001366
535 000304' 000424
536 000306' 005720
537 000310' 022700 000776'
538 000314' 001002
539 000316' 000167 177762
540 000322' 000167 177732
541
542 000326' 111002
543 000330' 006302
544 000332' 016203 000000G
545 000336' 032763 040000 000026
546 000344' 001760
547 000346' 042763 040000 000026
548 000354' 000754
549
550
551 000356' 005767 000416
552 000362' 001066

      .REPT SPSIZ
      .NLIST
      .WORD 0
      .LIST
      .ENDR
MODSP:
;*****
RSTRT:
START: CLR R0 ;CLEAR REGISTERS
CLR R1
CLR R2
CLR R3
MOV #DV1,R0 ;CLEAR MODULE STORAGE
1$: CLR (R0)+
CMP #DV11+2,R0
BNE 1$
2$: MOV #DVP1,R0 ;GET POINTER
MOV #DV1,R1 ;GET STORAGE ADDRESS
MODS1: TST #R0 ;IS THERE A MODULE HERE?
BNE REJEC ;YES BUT DON'T SELECT IT
NOV ;NO
MODS2: MOV (R0)+,R3 ;SAVE POSITION INDICATOR
ASL R3 ;COMPENSATE
MOV MODQ(R3),(R1)+ ;STORE MODULE ADDRESS
CMP #DVP11+2,R0 ;FINISHED?
BNE MODS1 ;NO CONTINUE
MODS3: BK FLPLP ;GO CHECK STATUS
NOV ;STEP R0
TST (R0)+ ;CHECK FOR LAST MODULE
CMP #DVP11+2,R0 ;NOT DONE YET
BNE 1$
JMP MODS3 ;ALL DONE EXIT SUBROUTINE
1$: JMP MODS1 ;CONTINUE ON MACDUFF!
REJEC: MOV #R0,R2 ;CHECK FOR SELECTION
ASL R2
MOV MODQ(R2),R3
BIT #SELBIT,26(R3)
BEQ NOV
BIC #SELBIT,26(R3)
BR NOV
FLPLP: TST SWITCH
BNE TRNON

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554
555 000364' 005767 000342      TRNOF: TST      DV1          ;IS THERE ANY MODULES?
556 000370' 001457              BEQ      2$          ;NO DERE AINT SU LEAVE
557 000372' 012700 000732'      MOV      #DV1,R0
558 000376' 011001              MOV      (R0),R1
559 000400' 032761 040000 000026 1$:      BIT      #SELBIT,26(R1)
560 000406' 001444              BEQ      11$
561 000410' 052761 020000 000026 11$:      BIS      #STFLG,26(R1) ;SET STOP FLAG
562 000416' 042761 040000 000026      BIC      #SELBIT,26(R1) ;CLEAR SELECT BIT
563 000424' 016104 000006      MOV      6(R1),R4 ;GET DEVICE ADDRESS
564 000430' 016167 000014 000352      MOV      14(R1),DEV CNT ;GET DRIVE COUNT
565 000436' 012767 000001 000350      MOV      #1,POINT ;INITIALIZE POINTER
566 000444' 005067 000342      CLR      DRVEN ;SET DRIVE #
567 000450' 036767 000340 000332 3$:      BIT      POINT,DEV CNT ;DRIVE PRESENT?
568 000456' 001012              BNE      4$          ;BR=YES
569 000460' 000241              CLC
570 000462' 006167 000326      ROL      POINT
571 000466' 005267 000320      INC      DRVEN
572 000472' 022767 000011 000312      CMP      #11,DRVEN ;LAST ONE?
573 000500' 001363              BNE      3$          ;BR=NO
574 000502' 000406              BR      11$          ;BR=YES
575 000504' 016764 000302 000010 4$:      MOV      DRVEN,10(R4) ;SET DRIVE # TO CONTROLLER
576 000512' 012714 000013      MOV      #13,R4 ;RELEASE RH01
577 000516' 000760              BR      31$
578 000520' 062700 000002      ADD      #2,R0 ;CONTINUE
579 000524' 005710              TST      (R0)
580 000526' 001323              BNE      1$
581 000530' 005267 000244      INC      SWITCH
582 000534' 104413 000000'      ENDIT$,BEGIN ;SIGNAL END OF ITERATION.
(1)                               ;MONITOR SHALL TEST END OF PASS
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586
587
588
589 000540' 005767 000166      TRNOF: TST      DV1          ;IS DERE ANY MODULES HERE?
590 000544' 001465              BEQ      2$          ;NO BUT TURN ON ANYWAY
591 000546' 012700 000732'      MOV      #DV1,R0 ;GET TABLE ADDRESS
592 000552' 011001              MOV      (R0),R1
593 000554' 032761 040000 000026 1$:      BIT      #SELBIT,26(R1)
594 000562' 001052              BNE      11$
595 000564' 042761 020000 000026      BIC      #STFLG,26(R1) ;CLEAR STOP FLAG
596 000572' 052761 040000 000026      BIS      #SELBIT,26(R1) ;SET SEL
597 000600' 016104 000006      MOV      6(R1),R4 ;GET DEVICE ADDRESS
598 000604' 016167 000014 000176      MOV      14(R1),DEV CNT ;GET DRIVE COUNT
599 000612' 012767 000001 000174      MOV      #1,POINT ;INITIALIZE POINTER
600 000620' 005067 000166      CLR      DRVEN ;SET DRIVE #
601 000624' 036767 000164 000156 3$:      BIT      POINT,DEV CNT ;DRIVE PRESENT?
602 000632' 001012              BNE      4$          ;BR=YES
603 000634' 000241              CLC
604 000636' 006167 000152      ROL      POINT
605 000642' 005267 000144      INC      DRVEN
606 000646' 022767 000011 000136      CMP      #11,DRVEN ;LAST ONE?
607 000654' 001363              BNE      3$          ;BR=NO
608 000656' 000414              BR      11$          ;BR=YES
609 000660' 016764 000126 000010 4$:      MOV      DRVEN,10(R4) ;SET DRIVE # TO CONTROLLER
610 000666' 012714 000010      MOV      #10,R4 ;TRY TO SET BIT 2
611 000672' 032714 000010      BIT      #10,R4 ;SEE IF IT'S THERE
612 000676' 001773              BEQ      41$
613 000700' 012764 000377 000016      MOV      #377,16(R4) ;CLEAR ATTN. BIT
614 000706' 000752              BR      31$
615 000710' 062700 000002      ADD      #2,R0 ;INC ADDRESS TABLE
616 000714' 005710              TST      (R0)
617 000716' 001315              BNE      1$
618
619 000720' 005067 000054      CLR      SWITCH
620 000724' 104413 000000'      ENDIT$,BEGIN ;SIGNAL END OF ITERATION.
(1)                               ;MONITOR SHALL TEST END OF PASS
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626
627
628                ;SPECIAL ADDRESSES, CONSTANTS, & OTHER
629
630 000730' 000000    DV0: 0          ;DO NOT USE
631 000732' 000000    DV1: 0          ;DEVICE MODULE ADDRESS ABSOLUTE
632                                     ;THERE ARE 9 LOCATION TO INCLUDE
633                                     ;MAXIMUM 9 MODULES.
634 000734' 000000    DV2: 0
635 000736' 000000    DV3: 0
636 000740' 000000    DV4: 0
637 000742' 000000    DV5: 0
638 000744' 000000    DV6: 0
639 000746' 000000    DV7: 0
640 000750' 000000    DV10: 0
641 000752' 000000    DV11: 0
642 000754' 000000    DVP1: 0
643                                     ;MODULE POINTER, THERE ARE 9 LOCATIONS
644                                     ;TO INCLUDE MAXIMUM 9 MODULES.
645 000756' 000000    DVP2: 0
646 000760' 000000    DVP3: 0
647 000762' 000000    DVP4: 0
648 000764' 000000    DVP5: 0
649 000766' 000000    DVP6: 0
650 000770' 000000    DVP7: 0
651 000772' 000000    DVP10: 0
652 000774' 000000    DVP11: 0
653 001000' 000000    SWITCH: 0      ;RESTART ALL FLAG
654 001002' 000000    YES: 0
655 001004' 000000    SET: 0
656 001006' 000000    CNT: 0
657 001010' 000000    DEVCN1: 000
658 001012' 000000    DRVEN: 000
659 001014' 000000    POINT: 000
660 001016' 000000    SELBIT=40000
661 001018' 000000    STFLG=20000
662 001016' 000310    PATCH: ,BLKW 200.
663 000001            ,END

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ACSH      000102R      514#
ADDR      000006R      514#
ADDR22=   001000      514#
ASB       000106R      514#
ASTAT     000104R      514#
AWAS      000110R      514#
BEGIN     000000R      514#      582      620
BIT0 =    000001      514#
BIT1 =    000002      514#
BIT10 =   002000      514#
BIT11 =   004000      514#
BIT12 =   010000      514#
BIT13 =   020000      514#
BIT14 =   040000      514#
BIT15 =   100000      514#
BIT2 =    000004      514#
BIT3 =    000010      514#
BIT4 =    000020      514#
BIT5 =    000040      514#
BIT6 =    000100      514#
BIT7 =    000200      514#
BIT8 =    000400      514#
BIT9 =    001000      514#
BREAKS=   104407      514#
BR1       000012R      514#
BR2       000013R      514#
HTODS =   104421      514#
CDATA6=   104412      514#
CMT       001006R      656#
CONFIG    000056R      514#
CSRA      000100R      514#
DATCK$=   104411      514#
DATER$=   104404      514#
DEVCON1   001010R      564#      567      598*      601      657#
DRVEN     001012R      566#      571#      572      575      600*      605*      606      609      658#
DVID1     000014R      514#
DVP1      000754M      525      642#
DVP10     000772R      650#
DVP11     000774R      533      537      651#
DVP2      000756R      644#
DVP3      000760R      645#
DVP4      000762R      646#
DVP5      000764R      647#
DVP6      000766R      648#
DVP7      000770R      649#
DV0       000730R      630#
DV1       000732R      521      526      555      557      589      591      631#
DV10      000750R      640#
DV11      000752R      523      641#
DV2       000734R      634#
DV3       000736R      635#
DV4       000740R      636#
DV5       000742R      637#
DV6       000744R      638#
DV7       000746R      639#
ENDIT$=   104413      514#      582      620

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END\$	= 104410	514#					
ERRTYP	000106R	514#					
EXIT\$	= 104400	514#					
FLPFLP	000356R	535	551#				
GETPAS\$	= 104415	514#					
GWBUS\$	= 104414	514#					
HRDCNT	000044R	514#					
HRDRS\$	= 104405	514#					
HRDPAS	000050R	514#					
ICONT	000036R	514#					
ICOUNT	000040R	514#					
IDNUM	000122R	514#					
INIT	000030R	514#					
INTR	000120R	514#					
MAP22\$	= 104416	514#					
MODNAM	000000R	514#					
MODQ	= ***** G	513#	532	544			
MODSP	000224R	514#					
MODS1	000260R	527#	534	540			
MODS2	000266R	530#					
MODS3	000304R	535#	539				
MSGN\$	= 104403	514#					
MSG\$	= 104402	514#					
MSG\$	= 104401	514#					
NODV	000306R	529	536#	546	548		
NULL	= 000000	514#					
OPEN	= 000000	514#					
OTOA\$	= 104420	514#					
PASCNT	000034R	514#					
PATCH	001016R	662#					
PIRQ\$	= 000004	514#					
POINT	001014R	565#	567	570*	599*	601	604* 659#
POPSP	= 005726	514#					
POPSP2	= 022626	514#					
PRTY	= 000000	514#					
PRTY0	= 000000	514#					
PRTY1	= 000040	514#					
PRTY2	= 000100	514#					
PRTY3	= 000140	514#					
PRTY4	= 000200	514#					
PRTY5	= 000240	514#					
PRTY6	= 000300	514#					
PRTY7	= 000340	514#					
PS	= 177776	514#					
PSW	= 177776	514#					
PUSH	= 005746	514#					
PUSH2	= 024646	514#					
RAND\$	= 104417	514#					
RANUM	000054R	514#					
REJEC	000326R	528	542#				
RESTR	000224R	514	516#				
RES1	000056R	514#					
RES2	000060R	514#					
RSTR	000112R	514#					
SBADR	000102R	514#					
SELBIT	= 040000	545	547	559	562	593	596 660#

SET	001004R	655#					
SOFcnt	000042R	514#					
SOFER\$	= 104406	514#					
SOFPAS	000046R	514#					
SPOINT	000032R	514#					
SPSIZ	= 000040	7#	514				
SR1	000016R	514#					
SR2	000020R	514#					
SR3	000022R	514#					
SR4	000024R	514#					
START	000224R	514	517#				
STAT	000026R	514#					
STFLG	= 020000	561	595	661#			
SVR0	000062R	514#					
SVR1	000064R	514#					
SVR2	000066R	514#					
SVR3	000070R	514#					
SVR4	000072R	514#					
SVR5	000074R	514#					
SVR6	000076R	514#					
SWITCH	001000R	551	581*	619*	653#		
SYSNT	000052R	514#					
TRNOF	000364R	555#					
TRNUN	000540R	552	589#				
TRPDFD	= 000022	514#					
VECTOR	000010R	514#					
WASADR	000104R	514#					
WDFK	000116R	514#					
WDT0	000114R	514#					
XFLAG	000005R	514#					
YES	001002R	654#					
.	= 001636R	662#					

BKMOD	95#	
BREAK	185#	
BTOD	204#	
CKDATA	240#	
DATAK	249#	
DATERR	138#	
DFSEVN	272#	514
DSEVNT	282#	514
END	175#	
ENDIT	166#	582 620
ENDMOD	171#	
EQUATS	288#	514
EXIT	120#	
GETPA	231#	
GWBUFF	219#	
HRDER	128#	
IOMOD	91#	
IOMODP	115#	
IOMODR	111#	
IOMODX	107#	
MAP22	235#	
MODULE	8#	514
MSG	154#	
MSGN	158#	
MSGS	162#	
NBKM0D	103#	
OT0A	190#	
PIRO	179#	
RAND	124#	
SBKM0D	99#	514
SOFER	144#	

. ABS. 000000 000
001636 001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

CXRHAA,CXRHAA.LST/CRF=DDXCOM.C6,CXRHAA.SRC
RUN-TIME: 3 4 .7 SECONDS
RUN-TIME RATIO: 25/8=3.0
CORE USED: 7K (13 PAGES)

