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IDENTIFICATION

PRODUCT CUDL: AC-E992B-MC
PRODUCT NAME: CXKUAB0 KUV11-A MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

KUA IS AN NBKMOD FOR THE KUV11-AA LSI-11 WRITABLE CONTROL STORE OPTION. IT IS USED IN CONJUNCTION WITH THE CPB (EIS EXERCISER) AND FPA (FIS EXERCISER) DECA MODULES TO EXERCISE THE KUV11-AA IN ADDRESS MODE 1 OR 3. THE KUA MODULE RUNS FIRST, RUNS ONLY ONCE, AND WITH THE T-BIT OFF. ITS PURPOSE IS TO RUN A MEMORY TEST ON THE KUV11-AA RAM AND THEN TO LOAD THE RAM WITH THE APPROPRIATE MICRO-CODE FOR THE CPB AND FPA MODULES TO RUN. THUS, CPB AND FPA SHOULD BE CONFIGURED TO RUN ON AN LSI-11, IN ORDER TO FULLY EXERCISE THE KUV11-AA. ALL ERRORS DETECTED ARE REPORTED ON THE CONSOLE TERMINAL.

2. REQUIREMENTS

HARDWARE: AN LSI-11 (M7264-YC) WITH ONE KUV11-AA, SET UP FOR ADDRESS MODE 1 OR 3 (SEE SECTION 6).
STORAGE: KUA REQUIRES:
1. DECIMAL WORDS: 1071
2. OCTAL WORDS: 02057
3. OCTAL BYTES: 4136

3. PASS DEFINITION

SINCE THIS IS AN NBKMOD IT RUNS ONLY ONE PASS. THIS CONSISTS OF 4 ITERATIONS OF THE 'MOV1' RAM MEMORY TEST; LOADING OF THE MICRO-CODE INTO THE BOTTOM HALF OF THE KUV11-AA RAM; AND CHECKING THAT THE MICRO-CODE LOADED CORRECTLY.

4. EXECUTION TIME

KUA TAKES APPROXIMATELY 18 SECONDS, RUNNING IN AN MSV11-CD MEMORY.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:
DEVADR: 177540, VECTOR: 1, BR1: 0, DEVCNT: 1
REQUIRED PARAMETERS:
NONE

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6. DEVICE/OPTION SETUP

MAKE CERTAIN THAT THE KUV11-AA IS PROPERLY INSTALLED AND THAT THE MODULE (M8018) DIP SWITCHES ARE SET UP FOR EITHER ADDRESS MODE 1 OR 3.
 THESE SWITCH SETTINGS ARE:

MODE	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
1	ON	OFF	OFF	ON	OFF	ON	OFF	-
3	OFF	OFF	ON	ON	OFF	ON	OFF	-

7. MODULE OPERATION

A. RUN THE 'MOV1' MEMORY TEST ON THE KUV11-AA RAM MEMORY 4 TIMES; REPORT ERRORS ACCORDING TO SRI BIT01 SETTING.
 B. IF SRI BIT00 = 1 AND ANY MEMORY ERROR(S) OCCURRED, THEN SKIP TO E.
 C. LOAD MICRO-CODE INTO THE KUV11-AA MEMORY.
 D. CHECK THAT THE MICRO-CODE WAS LOADED CORRECTLY; REPORT ERROR IF NOT.
 E. EXIT THE MODULE.

8. OPERATING OPTIONS

SRI BIT00 CLEAR(0):
 LOAD THE MICRO-CODE FOR CPB AND FPA REGARDLESS OF THE RESULTS OF THE KUV11-AA RAM MEMORY TEST.
 SRI BIT00 SET(1):
 IF THE RAM MEMORY TEST FAILS, DO NOT LOAD THE MICRO-CODE FOR CPB AND FPA TO RUN.
 SRI BIT01 CLEAR(0):
 IN THE RAM MEMORY TEST, TYPE OUT AN ERROR MESSAGE FOR EACH ERROR ENCOUNTERED.
 SRI BIT01 SET(1):
 DO NOT TYPE OUT EACH RAM MEMORY TEST ERROR; JUST TYPE ONE MESSAGE AT THE END INDICATING HOW MANY ERRORS WERE ENCOUNTERED.

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9. NON-STANDARD PRINTOUTS

THE KUV11-AA MEMORY TEST ERROR REPORT DOES NOT USE THE STANDARD DEC/X11 ERROR PRINTOUT, SINCE THE MEMORY WORDS ARE 24 BITS LONG. THE ERROR PRINTOUT LOOKS AS FOLLOWS:

BAD DATA IN A KUV11-AA RAM WORD.	
RAM	GOOD DATA
ADDRESS CSR+4 CSR+2	CSR+4 CSR+2

THE FIRST 16 BITS ARE UNDER THE HEADING 'CSR+2'; THE LAST 8 BITS ARE FOUND IN THE LOWER BYTE OF 'CSR+4'. THE UPPER 8 BITS OF 'CSR+4' HAVE NO MEANING AND ARE ALWAYS ZEROES. THE 'RAM ADDRESS' CAN RANGE FROM 0 TO 1777.

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167 000000' NBKMOD <KUAB > 177540,1,0,0,1,1,163
168 000000' MODULE 41000,KUAB ,177540,1,0,0,1,1,163
169 .TITLE KUAB DEC/X11 SYSTEM EXERCISER MODULE
170 / DDSCOM VERSION 6 23-MAY-78
171 .LIST BIN
172 ;*****
173 BEGIN:
174 MODNAM: .ASC11 /KUAB / ;MODULE NAME.
175 XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
176 ADDR: 177540+0 ;1ST DEVICE ADDR.
177 VECTOR: 1+0 ;1ST DEVICE VECTOR.
178 BR1: .BYTE PRTY0+0 ;1ST BR LEVEL.
179 BR2: .BYTE PRTY0+0 ;2ND BR LEVEL.
180 DIVID: 1+1 ;DEVICE INDICATOR 1.
181 SR1: OPEN ;SWITCH REGISTER 1
182 SR2: OPEN ;SWITCH REGISTER 2
183 SR3: OPEN ;SWITCH REGISTER 3
184 SR4: OPEN ;SWITCH REGISTER 4
185 ;*****
186 STAT: 41000 ;STATUS WORD.
187 INIT: STAKT ;MODULE START ADDR.
188 SPOINI: MODSP ;MODULE STACK POINTER.
189 PASCNT: 0 ;PASS COUNTER.
190 ICNT: 1 ;# OF ITERATIONS PER PASS=1
191 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
192 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
193 HRUCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
194 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
195 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
196 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
197 RANNUM: 0 ;HOLDS RANDOM # WHEN HAND MACRO IS CALLED
198 CONFIG:
199 RES1: 0 ;RESERVED FOR MONITOR USE
200 RES2: 0 ;RESERVED FOR MONITOR USE
201 SVR0: OPEN ;LOC TO SAVE R0.
202 SVR1: OPEN ;LOC TO SAVE R1.
203 SVR2: OPEN ;LOC TO SAVE R2.
204 SVR3: OPEN ;LOC TO SAVE R3.
205 SVR4: OPEN ;LOC TO SAVE R4.
206 SVR5: OPEN ;LOC TO SAVE R5.
207 SVR6: OPEN ;LOC TO SAVE R6.
208 CSRA: OPEN ;ADDR OF CURRENT CSR.
209 SBADR: ;ADDR OF GOOD DATA, OR
210 ACSR: OPEN ;CONTENTS OF CSR.
211 WASADR: ;ADDR OF BAD DATA, OR
212 ASTAT: OPEN ;STATUS REG CONTENTS.
213 ERRTP: ;TYPE OF ERROR
214 ASB: OPEN ;EXPECTED DATA.
215 AWAS: OPEN ;ACTUAL DATA.
216 RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
217 WDTU: OPEN ;WORDS TO MEMORY PER ITERATION
218 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
219 INIR: OPEN ;# OF INTERRUPTS PER ITERATION
220 IDNUM: 163 ;MODULE IDENTIFICATION NUMBER=163
221 .REPT SPSIZ ;MODULE STACK STARTS HERE.
222 .NLISI

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223 .WORD 0
224 .LIST
225 .ENDR
226 000224' MUOSP:
227 ;*****
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232 000001
233 000002
234 000004
235 000010
236 000020
237 000040
238 000100
239 000200
240 000400
241 001000
242 002000
243 004000
244 010000
245 020000
246 040000
247 100000
248 000000
249 000001
250 000002
251 000003
252 000004
253 000005
254 000006
255 000007
256 000006
257 000007
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259
260 000224' 000000
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USER EQUATES AND CONSTANTS

BIT0 = 000001
BIT1 = 000002
BIT2 = 000004
BIT3 = 000010
BIT4 = 000020
BIT5 = 000040
BIT6 = 000100
BIT7 = 000200
BIT8 = 000400
BIT9 = 001000
BIT10 = 002000
BIT11 = 004000
BIT12 = 010000
BIT13 = 020000
BIT14 = 040000
BIT15 = 100000
R0 = 0
R1 = 1
R2 = 2
R3 = 3
R4 = 4
R5 = 5
R6 = 6
R7 = 7
SP = 6
PC = 7

TIMES: 0 ;WILL KEEP TRACK OF RAM MEMORY TEST ITERATIONS

262
263
264 000226' 016701 177554
265 000232' 005011
266 000234' 005067 177604
267 000240' 012767 000004 177756
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272 000246' 005011
273 000250' 012700 002000
274 000254' 005061 000002
275 000260' 005061 000004
276 000264' 005211
277 000266' 005300
278 000270' 001371
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307 000272' 012702 177777
308 000276' 005003
309 000300' 005004
310 000302' 012705 000001
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312 000306' 012700 002000
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314 000312' 010411
315 000314' 020361 000002
316 000320' 001003
317 000322' 120361 000004

START:
RESTRT: MOV ADDR,R1 ;GET THE BASE ADDRESS (CSR)
CLR (R1) ;MAKE SURE THAT ENABLE (CSR BIT12) IS CLEAR
CLR HRDCNT ;SET RAM MEMORY TEST ERROR COUNT TO ZERO
MOV #4,TIMES ;SET ITERATION COUNT TO 10 OCTAL

CLEAR THE KUV11-AA RAM MEMORY

CLR RAM: CLR (R1) ;SET UP CSR FOR Q-BUS ACCESS, RAM ADDRESS 0
MOV #2000,R0 ;SET UP LOOP COUNTER
1\$: CLR 2(R1) ;WRITE ZEROES...
CLR 4(R1) ;...TO RAM WORD
INC (R1) ;INCREMENT RAM ADDRESS
DEC R0 ;DUNE CLEARING THE WCS RAM?
BNE 1\$;BRANCH BACK AND CONTINUE IF NOT

; * TEST RAM MEMORY USING 'MOV1' MEMORY TEST
; *
; * AFTER THE KUV11-AA RAM HAS BEEN CLEARED, THE ALGORITHM PROCEEDS AS FOLLOWS:
; *
; * (1) READ RAM LOCATION 0 AND VERIFY THAT IT CONTAINS ZEROES,
; * WRITE ALL ONES TO RAM LOCATION 0,
; * READ LOCATION 0 AND VERIFY THAT IT CONTAINS ALL ONES;
; * (2) REPEAT THE ABOVE FOR LOCATIONS 1,2,....,1777 (I.E., ALL OF THE RAM).
; * AT THIS POINT THE RAM WILL BE FULL OF ONES.
; * (3) REPEAT THE ABOVE, WRITING ZEROES THIS TIME. AFTER THIS THE RAM WILL
; * AGAIN BE FULL OF ZEROES.
; * (4) REPEAT ALL OF THE ABOVE, EXCEPT START AT THE TOP OF THE RAM AND WORK
; * DOWNWARDS (RAM LOCATIONS 1777-0).
; * AT THIS POINT THE TEST HAS SEQUENCED THROUGH THE RAM FOUR TIMES.
; * (5) REPEAT THE ABOVE, EXCEPT USE BIT 1 AS THE LEAST SIGNIFICANT BIT WHEN
; * FORMING THE RAM ADDRESSES. THE RAM ADDRESS WILL OVERFLOW AFTER CHECKING
; * ALL EVEN RAM ADDRESSES, AND THE OVERFLOW BIT IS SIMPLY WRAPPED AROUND
; * TO BIT 0 OF THE RAM ADDRESS. TESTING CONTINUES UNTIL ALL RAM ADDRESSES
; * HAVE BEEN TESTED.
; * REPEAT AGAIN, THIS TIME WITH BIT 2 AS THE LSB IN FORMING THE RAM
; * ADDRESSES; AND AGAIN WITH BIT 3 AS THE LSB; AND SO ON UNTIL ALL BITS
; * OF THE RAM ADDRESS HAVE SERVED AS THE LSB. SINCE THERE ARE 10 BITS
; * IN THE RAM ADDRESS (TO COVER THE RANGE 0-1777), THIS MEANS THAT STEP (5)
; * WILL BE DONE 10 TIMES IN ALL.

RAMTEST: MOV #177777,R2 ;R2 WILL CONTAIN TEST DATA
CLR R3 ;R3 WILL CONTAIN TEST DATA
CLR R4 ;R4 WILL CONTAIN THE RAM ADDRESS
MOV #1,R5 ;R5 WILL HAVE THE LEAST SIGNIFICANT BIT FOR
;GENERATING THE RAM ADDRESS
MOV #2000,R0 ;SET UP LOOP COUNTER
;BEGINNING OF TEST LOOP
1\$: MOV R4,(R1) ;PUT ADDRESS INTO THE CSR
CMP R3,2(R1) ;CORRECT DATA?
BNE 2\$;BRANCH TO ERROR IF NOT
CMPB R3,4(R1) ;CORRECT DATA?


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318 000326' 001404      BEQ      36      ;BRANCH OVER ERROR IF OK
319 000330' 010367      MOV      R3,ASH
320 000334' 004767      JSR      PC,MEERR      ;GO TO ERROR REPORTING SUBROUTINE
321 000340' 010261      MOV      R2,2(R1)      ;WRITE TEST DATA TO RAM BITS 0-15
322 000344' 010261      MOV      R2,4(R1)      ;WRITE TEST DATA TO RAM BITS 16-23
323 000350' 020261      CMP      R2,2(R1)      ;CORRECT DATA?
324 000354' 001003      BNE      45      ;BRANCH TO ERROR IF NOT
325 000356' 120261      CMPB     R2,4(R1)      ;CORRECT DATA?
326 000362' 001404      BEQ      55      ;BRANCH OVER ERROR IF YES
327 000364' 010267      MOV      R2,ASH
328 000370' 004767      JSR      PC,MEERR      ;GO TO ERROR REPORTING SUBROUTINE
329 000374' 060504      ADD      R5,R4      ;GENERATE A RAM ADDRESS
330 000376' 032704      BIT      #BIT10,R4      ;ADDRESS OVERFLOW?
331 000402' 001403      BEQ      68      ;BRANCH IF NOT
332 000404' 042704      BIC      #BIT10,R4      ;ADD ADDRESS OVERFLOW...
333 000410' 005204      INC      R4      ;...TO BOTTOM OF ADDRESS
334 000412' 005300      DEC      R0      ;LOOP DONE?
335 000414' 001336      BNE      18      ;CONTINUE TEST LOOP IF NOT
336 000416' 005102      CUM      R2      ;R2 GETS ONES COMPLEMENT OF ITSELF
337 000420' 005103      COM      R3      ;R3 GETS ONES COMPLEMENT OF ITSELF
338 000422' 012700      MOV      #2000,R0      ;SET UP LOOP COUNTER
339 000426' 005004      CLR      R4      ;INITIALIZE THE RAM ADDRESS
340 000430' 005702      TST      R2      ;HAVE WE GONE THROUGH TWICE?
341 000432' 001727      BEQ      18      ;NO, GO BACK AND DO IT AGAIN WITH COMPLEMENTED DATA
342      ;DO THE SAME AS ABOVE EXCEPT TEST RAM MEMORY FROM TOP TO BOTTOM (1777-0)
343 000434' 012704      MOV      #1777,R4      ;SET UP RAM ADDRESS
344 000440' 010411      MOV      R4,(R1)      ;PUT ADDRESS INTO THE CSR
345 000442' 020361      CMP      R3,2(R1)      ;CORRECT DATA?
346 000446' 001003      BNE      85      ;BRANCH TO ERROR IF NOT
347 000450' 120361      CMPB     R3,4(R1)      ;CORRECT DATA?
348 000454' 001404      BEQ      96      ;BRANCH OVER ERROR IF OK
349 000456' 010367      MOV      R3,ASH
350 000462' 004767      JSR      PC,MEERR      ;GO TO MEMORY ERROR REPORTING SUBROUTINE
351 000466' 010261      MOV      R2,2(R1)      ;WRITE TEST DATA TO RAM BITS 0-15
352 000472' 010261      MOV      R2,4(R1)      ;WRITE TEST DATA TO RAM BITS 16-23
353 000476' 020261      CMP      R2,2(R1)      ;CORRECT DATA?
354 000502' 001003      BNE      105      ;BRANCH TO ERROR IF NOT
355 000504' 120261      CMPB     R2,4(R1)      ;CORRECT DATA?
356 000510' 001404      BEQ      115      ;BRANCH OVER ERROR IF YES
357 000512' 010267      MOV      R2,ASH
358 000516' 004767      JSR      PC,MEERR      ;GO TO MEMORY ERROR REPORTING SUBROUTINE
359 000522' 160504      SUB      R5,R4      ;GENERATE A RAM ADDRESS
360 000524' 100002      BFL      128      ;BRANCH IF NO ADDRESS UNDERFLOW OCCURED
361 000526' 062704      ADD      #1777,R4      ;CORRECT THE ADDRESS UNDERFLOW
362 000532' 005300      DEC      R0      ;LOOP DONE?
363 000534' 001341      BNE      78      ;CONTINUE TEST LOOP IF NOT
364 000536' 005102      COM      R2      ;R2 GETS ONES COMPLEMENT OF ITSELF
365 000540' 005103      CUM      R3      ;R3 GETS ONES COMPLEMENT OF ITSELF
366 000542' 012700      MOV      #2000,R0      ;SET UP LOOP COUNTER
367 000546' 012704      MOV      #1777,R4      ;INITIALIZE THE RAM ADDRESS
368 000552' 005702      TST      R2      ;HAVE WE GONE THROUGH TWICE?
369 000554' 001731      BEQ      78      ;GO BACK AND DO IT AGAIN WITH COMPLEMENTED DATA IF NOT
370
371      ;GENERATE A NEW LEAST SIGNIFICANT BIT FOR RAM ADDRESS GENERATION AND,
372      ;IF NOT DONE WITH THE TEST, GO BACK AND RUN SOME MORE
373 000556' 006305      ASL      R5      ;SHIFT THE LSB FOR SUBSEQUENT RAM ADDRESS GENERATION

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374 000560' 005004      CLR      R4      ;INITIALIZE THE RAM ADDRESS
375 000562' 032705      BIT      #BIT10,R5      ;SEE IF WE ARE DONE WITH THE RAM MEMORY TEST
376 000566' 001002      BNE      135      ;BRANCH IF YES
377 000570' 000167      JMP      18      ;CONTINUE TESTING IF NOT
378 000574' 005367      DEC      TIMES      ;ALL ITERATIONS DONE?
379 000600' 001402      BEQ      145      ;BRANCH IF YES
380 000602' 000167      JMP      CLRMM      ;NO, RUN RAM MEMORY TEST AGAIN
381
382 000606' 005767      TST      HRDCNT      ;ANY RAM MEMORY ERRORS?
383 000612' 001410      BEQ      155      ;BRANCH IF NOT
384      ;*****
385      ;*****
386      ;*****
387 000614' 104420      OTOAS,BEGIN,HRDCNT,COUNT      ;CONVERT HRDCNT TO ASCII AND
388 000622' 001614'      ;STORE AT COUNT
389
390 000624' 104403      MSGNS,BEGIN,ERR2      ;ASCII MESSAGE CALL WITH COMMON HEADER
391 000632' 000403      BR      165      ;SKIP PRINTOUT
392 000634' 104403      MSGNS,BEGIN,RAMOK      ;ASCII MESSAGE CALL WITH COMMON HEADER
393 000634' 104403      ;*****
394      ;*****
395      ;*****
396      ;*****
397      ;* THE NEXT ROUTINE LOADS MICRO-CODE FROM THE TABLES AT 'UCODE' & 'UCODE1' INTO
398      ;* THE KUV11-AA RAM AND CHECKS THAT IT LOADED CORRECTLY.
399      ;*
400      ;*****
401
402      ;LOAD THE MICRO-CODE
403 000642' 036727      BIT      SP1,#BIT0      ;LOAD MICRO-CODE EVEN IF RAM MEMORY TEST HAD ERRORS?
404 000650' 001403      BEQ      178      ;BRANCH IF YES
405 000652' 005767      TST      HRDCNT      ;SEE IF ANY RAM MEMORY TEST ERRORS OCCURED
406 000656' 001103      BNE      ABORT      ;YES, BRANCH TO LOADER ABORT
407 000660' 005011      CLR      (R1)      ;SET RAM ADDRESS TO 0
408 000662' 012702      MOV      #UCODE,R2      ;GET FIRST MICRO-CODE TABLE ADDRESS
409 000666' 012261      MOV      (R2)+,2(R1)      ;LOAD A KUV11-AA RAM WORD, BITS 15-0
410 000672' 021127      CMP      (R1),#777      ;DONE?
411 000676' 001402      BEQ      198      ;BRANCH IF YES
412 000700' 005211      INC      (R1)      ;INCREMENT THE RAM ADDRESS
413 000702' 000771      BR      185      ;BRANCH BACK AND CONTINUE LOADING MICRO-CODE
414 000704' 012702      MOV      #UCODE1,R2      ;GET SECOND MICRO-CODE TABLE ADDRESS
415 000710' 012200      MOV      (R2)+,R0      ;GET A TABLE ENTRY
416 000712' 001414      BEQ      225      ;BRANCH OUT OF MICRO-CODE LOADER IF A ZERO TABLE
417      ;ENTRY IS ENCOUNTERED
418 000714' 012703      MOV      #BIT0,R3      ;PRE-LOAD R3 WITH RAM DATA
419 000720' 005700      TST      R0      ;SEE IF BIT15 IS SET
420 000722' 100004      BPL      215      ;BRANCH IF NOT
421 000724' 012703      MOV      #BIT1,R3      ;PRE-LOAD R3 WITH RAM DATA
422 000730' 042700      BIC      #BIT15,R0      ;STRIP OFF BIT 15
423 000734' 010011      MOV      R0,(R1)      ;LOAD THE RAM ADDRESS
424 000736' 010361      MOV      R3,4(R1)      ;LOAD THE RAM WORD, BITS 23-16
425 000742' 000762      BR      205
426
427 000744' 005011      ;CHECK FOR CORRECT MICRO-CODE LOAD
428 000746' 012702      CLR      (R1)      ;SET RAM ADDRESS TO 0
429 000752' 022261      MOV      #UCODE,R2      ;GET FIRST MICRO-CODE TABLE ADDRESS
430      CMP      (R2)+,2(R1)      ;CORRECT DATA IN RAM, BITS 15-0?

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430 000756' 001026      BNE 278      ;BRANCH TO ERROR IF NOT
431 000760' 021127 000777 CMP (R1),#777 ;DONE?
432 000764' 001402      BEQ 248      ;BRANCH IF YES
433 000766' 005211      INC (R1)      ;INCREMENT RAM ADDRESS
434 000770' 000770      BR 248      ;BRANCH BACK AND CONTINUE CHECKING MICRO-CODE
435 000772' 012702 004070' 248: MOV #UCODE1,R2 ;GET SECOND MICRO-CODE TABLE ADDRESS
436 000776' 012200 258: MOV (R2)+,R0 ;GET A TABLE ENTRY
437 001000' 001423      BEQ 288      ;BRANCH OUT OF MICRO-CODE CHECKER IF A ZERO TABLE
438                                ;ENTRY IS ENCOUNTERED
439 001002' 012703 000001 MOV #BIT0,R3 ;PRE-LOAD R3 WITH EXPECTED RAM DATA
440 001006' 005700      IST R0      ;SEE IF #BIT15 IS SET
441 001010' 100004      BPL 268      ;BRANCH IF NOT
442 001012' 012703 000002 MOV #BIT1,R3 ;PRE-LOAD R3 WITH EXPECTED RAM DATA
443 001016' 042700 100000 BIC #BIT15,R0 ;STRIP OFF BIT 15
444 001022' 010011 268: MOV R0,(R1) ;LOAD THE RAM ADDRESS
445 001024' 020361 000004 CMP R3,4(R1) ;CHECK FOR CORRECT DATA IN RAM BITS 23-16
446 001030' 001001      BNE 278      ;BRANCH TO ERROR IF BAD
447 001032' 000761      BR 258      ;CONTINUE CHECKING
448
449 001034' 278:
450 001034' 104403 000000' 001256' MSGNS,BEGIN,LOADAD ;ASCII MESSAGE CALL WITH COMMON HEADER
451 001042' 005267 176776 INC RRD CNT ;ADD THIS LOAD ERROR TO THE ERROR COUNT
452 001046' 000405      BR END
453
454 001050' 288:
455 001050' 104403 000000' 001252' MSGNS,BEGIN,LOADOK ;ASCII MESSAGE CALL WITH COMMON HEADER
456 001056' 052711 010000 HIS #BIT12,(R1) ;SET KUV11-AA ENABLE BIT, TO ALLOW MIB ACCESS TO THE RAM
457 001062'      END:
458 001062' 104413 000000'      ENDITS,BEGIN ;SIGNAL END OF ITERATION.
459                                ;MONITOR SHALL TEST END OF PASS
460
461 001066'      ABORT:
462 001066' 104403 000000' 001262' MSGNS,BEGIN,LDABRT ;ASCII MESSAGE CALL WITH COMMON HEADER
463 001074' 000772      BR END
464
465 ;*****
466 ;*
467 ;*SUBROUTINE MEMERR
468 ;*
469 ;*THIS SUBROUTINE REPORTS KUV11-AA RAM MEMORY ERRORS. EXPECTED DATA IS PASSED
470 ;*VIA 'ASB'; THE BAD DATA AND FAILING RAM ADDRESS ARE OBTAINED THROUGH THE KUV11-AA
471 ;*DEVICE REGISTERS. THE BASE REGISTER ADDRESS OF THE KUV11-AA IS PASSED VIA
472 ;*'ADDR'. THE STANDARD MONITOR CALLS 'MSGN' AND 'OACNV' ARE MADE IN THE SUBROUTINE,
473 ;*AND USE GENERAL REGISTER 5. NO OTHER GENERAL PURPOSE REGISTERS ARE USED.
474 ;*THIS SUBROUTINE IS CALLED AS FOLLOWS: JSR PC,MEMERR
475 ;*
476 ;*****
477
478 001076' 005267 176742 MEMERR: INC RRD CNT ;COUNT THE NUMBER OF RAM MEMORY ERRORS
479 001102' 036727 176710 BIT SRI,#BIT1 ;REPORT EACH ERROR?
480 001110' 001050      BNE 15 ;BRANCH OVER ERROR REPORTING IF NO
481 ;*****
482 ;CONVERT ASB TO ASCII AND
483 ;STORE AT GOODLO
484 001112' 104420 000000' 000106' OTOAS,BEGIN,ASB,GOODLO
485 001120' 001462'

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486 ;*****
487 001122' 042767 177400 176756 BIC #177400,ASB ;STRIP OFF HIGH BYTE
488 ;*****
489 ;CONVERT ASB TO ASCII AND
490 ;STORE AT GOODHI
491 001130' 104420 000000' 000106' OTOAS,BEGIN,ASB,GOODHI
492 001136' 001453'
493 ;*****
494 001140' 011167 000070 MOV (R1),GUNCH ;CONVERT FAILING RAM ADDRESS TO ASCII
495 ;*****
496 ;CONVERT GUNCH TO ASCII AND
497 ;STORE AT ADDRES
498 001144' 104420 000000' 001234' OTOAS,BEGIN,GUNCH,ADDRES
499 001152' 001442'
500 ;*****
501 001154' 112767 000040 000260 MOV #40,ADDRES ;PUT TWO ASCII SPACES INTO ADDRES
502 001162' 112767 000040 000253 MOV #40,ADDRES+1 ;
503 001170' 016167 000002 000036 MOV 2(R1),GUNCH ;CONVERT BAD DATA TO ASCII
504 ;*****
505 ;CONVERT GUNCH TO ASCII AND
506 ;STORE AT BADLO
507 001176' 104420 000000' 001234' OTOAS,BEGIN,GUNCH,BADLO
508 001204' 001502'
509 ;*****
510 001206' 016167 000004 000020 MOV 4(R1),GUNCH ;CONVERT BAD DATA TO ASCII
511 ;*****
512 ;CONVERT GUNCH TO ASCII AND
513 ;STORE AT BADHI
514 001214' 104420 000000' 001234' OTOAS,BEGIN,GUNCH,BADHI
515 001222' 001473'
516 ;*****
517 ;TYPE OUT THE ERROR MESSAGE AND DATA
518 001224' 104403 000000' 001236' MSGNS,BEGIN,ERR1 ;ASCII MESSAGE CALL WITH COMMON HEADER
519 001232' 000207 15: RTS PC ;RETURN FROM THE SUBROUTINE
520

```

```

521
522
523
524
525 001234' 000000 GUNCH: .WORD 0 ;TEMP LOC. FOR OCTAL TO ASCII CONV.
526
527 001236' 001266' ERR1: ERROR1
528 001240' 177777 177777
529
530 001242' 001511' ERR2: ERROR2
531 001244' 177777 177777
532
533 001246' 001625' RAMOK: MEMOK
534 001250' 177777 177777
535
536 001252' 001667' LOADOK: LOK
537 001254' 177777 177777
538
539 001256' 001731' LDBAD: LBAD
540 001260' 177777 177777
541
542 001262' 001775' LDABRT: LABORT
543 001264' 177777 177777
544
545
546 001266' 041045 042101 042040 ERROR1: .ASCII '%BAD DATA IN A KUV11-AA RAM WORD.'
547 001274' 052101 020101 047111
548 001302' 040440 045440 053125
549 001310' 030461 040455 020101
550 001316' 040522 020115 047527
551 001324' 042122 056
552 001327' 045 020040 040522 .ASCII '% RAM GOOD DATA BAD DATA'
553 001334' 020115 020040 020040
554 001342' 043440 047517 020104
555 001350' 040504 040524 020040
556 001356' 020040 020040 041040
557 001364' 042101 042040 052101
558 001372' 101
559 001373' 045 042101 051104 .ASCII '%ADDRESS CSR+4 CSR+2 CSR+4 CSR+2%'
560 001400' 051505 020123 041440
561 001406' 051123 032053 020040
562 001414' 051503 025522 020062
563 001422' 020040 041440 051123
564 001430' 032053 020040 051503
565 001436' 025522 022462
566
567 001442' 000006 ADDRES: .BLKB 6 ;BAD RAM ADDRESS
568 001450' 040 040 .BYTE 40,40,40 ;ASCII SPACES
569 001453' 000006 GOODHI: .BLKB 6 ;EXPECTED DATA, BITS 23-16
570 001461' 040 .BYTE 40 ;ASCII SPACE
571 001462' 000006 GOODLO: .BLKB 6 ;EXPECTED DATA, BITS 15-0
572 001470' 040 040 .BYTE 40,40,40 ;ASCII SPACES
573 001473' 000006 BADHI: .BLKB 6 ;ACTUAL DATA, BITS 23-16
574 001501' 040 .BYTE 40 ;ASCII SPACE
575 001502' 000006 BADLO: .BLKB 6 ;ACTUAL DATA, BITS 15-0
576 001510' 000 .BYTE 0 ;ASCII MESSAGE TERMINATOR

```

```

577
578
579
580
581 001511' 045 040522 020115 ERROR2: .ASCII '%RAM MEMORY TEST UNSUCCESSFUL; TOTAL NUMBER OF ERRORS (IN OCTAL) = '
582 001516' 042515 047515 054522
583 001524' 052040 051505 020124
584 001532' 047125 052523 041503
585 001540' 051505 043123 046125
586 001546' 020073 047524 040524
587 001554' 020114 052516 041115
588 001562' 051105 047440 020106
589 001570' 051105 047522 051522
590 001576' 024040 047111 047440
591 001604' 052103 046101 020051
592 001612' 020075
593 001614' 000006 COUNT: .BLKB 6
594 001622' 015 .BYTE 15
595 001623' 012 .BYTE 12
596 001624' 000 .BYTE 0 ;ASCII MESSAGE TERMINATOR
597
598 001625' 045 040522 020115 MEMOK: .ASCIZ '%RAM MEMORY TEST WAS SUCCESSFUL.%'
599 001632' 042515 047515 054522
600 001640' 052040 051505 020124
601 001646' 040527 020123 052523
602 001654' 041503 051505 043123
603 001662' 046125 022456 000
604
605
606 001667' 045 044515 051103 LOK: .ASCIZ '%MICRO-CODE LOAD WAS SUCCESSFUL.%'
607 001674' 026517 047503 042504
608 001702' 046040 040517 020104
609 001710' 040527 020123 052523
610 001716' 041503 051505 043123
611 001724' 046125 022456 000
612
613 001731' 045 051105 047522 LOAD: .ASCIZ '%ERROR OCCURED IN MICRO-CODE LOAD.%'
614 001736' 020122 041517 052503
615 001744' 042522 020104 047111
616 001752' 046440 041511 047522
617 001760' 041455 042117 020105
618 001766' 047514 042101 022456
619 001774' 000
620 001775' 045 044515 051103 LABORT: .ASCIZ '%MICRO-CODE LOAD IS ABORTED SINCE RAM MEMORY TEST FAILED.%'
621 002002' 026517 047503 042504
622 002010' 046040 040517 020104
623 002016' 051511 040440 047502
624 002024' 052122 042105 051440
625 002032' 047111 042503 051040
626 002040' 046501 046440 046505
627 002046' 051117 020131 042524
628 002054' 052123 043040 044501
629 002062' 042514 027104 000045
630
631 .EVEN
632

```

```

633
634
635      002070' 072411      UC0DE: 072411
636      ;*
637      ;*
638
639
640      004070' 000123      UC0DE1: 000123
641      ;*
642      ;*
643      004134' 000000      000000
644
645
646      000001      .END

```

[illegible]

HRDPAS	000050H	195#																		
ICONT	000036H	190#																		
ICOUNT	000040H	191#																		
IDNUM	000122H	220#																		
INIT	000030H	187#																		
INTR	000120H	219#																		
LABORT	001775H	542	620#																	
LBAD	001731H	539	613#																	
LDABRT	001262H	462	542#																	
LDBAD	001256H	450	539#																	
LUADOK	001252H	455	536#																	
LOK	001667H	536	606#																	
MAP226=	104416	228#																		
MEMERR	001076H	320	328	350	35#	478#														
MEMOK	001625H	533	598#																	
MODNAM	000000H	174#																		
MODSV	000224H	188	226#																	
MSG#	= 104403	228#	390	393	450	455	462	518												
MSG5	= 104402	228#																		
MSG6	= 104401	228#																		
NULL	= 000000	228#																		
OPEN	= 000000	175	181	182	183	184	201	202	203	204	205	206	207	208						
		210	212	214	215	217	218	219	228#											
OTOAS	= 104420	228#	387	484	491	498	507	514												
PASCNT	000034H	189#																		
PIRG#	= 000004	228#																		
POPSP	= 005726	228#																		
POPSP2	= 022626	228#																		
PRIY	= 000000	228#																		
PRIY0	= 000000	178	179	228#																
PRIY1	= 000040	228#																		
PRIY2	= 000100	228#																		
PRIY3	= 000140	228#																		
PRIY4	= 000200	228#																		
PRIY5	= 000240	228#																		
PRIY6	= 000300	228#																		
PRIY7	= 000340	228#																		
PS	= 177776	228#																		
PS#	= 177776	228#																		
PUSH	= 005746	228#																		
PUSH2	= 024646	228#																		
RAMOK	001246H	393	533#																	
RAMTSI	000272H	307#																		
RAND#	= 104417	228#																		
RANNUM	000054H	197#																		
RESTR	000226H	216	265#																	
RES1	000056H	199#																		
RES2	000060H	200#																		
RSIRT	000112H	216#																		
SBADR	000102H	209#																		
SUFCNT	000042H	192#																		
SOFER6	= 104406	228#																		
SOFPAS	000046H	194#																		
SPOINT	000032H	188#																		
SPSIZ	= 000040	1#	221																	
SRI	000016H	181#	403	479																

SR2	000020H	182#																		
SR3	000022H	183#																		
SR4	000024H	184#																		
START	000226H	187	264#																	
STAT	000026H	186#																		
SVR0	000062H	201#																		
SVR1	000064H	202#																		
SVR2	000066H	203#																		
SVR3	000070H	204#																		
SVR4	000072H	205#																		
SVR5	000074H	206#																		
SVR6	000076H	207#																		
SISCNT	000052H	196#																		
TIMES	000224H	260#	268#	378#																
TRPDFD	= 000022	228#																		
UCODE	002070H	408	428	635#																
UCODE1	004070H	414	435	640#																
VECTOR	000010H	177#																		
WASADR	000104H	211#																		
WDFH	000116H	218#																		
WJIO	000114H	217#																		
XFLAG	000005H	175#																		
.	= 004136H	567#	569#	571#	573#	575#	593#													

. ABS. 000000 000
 004136 001

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

XKUAB0,XKUAB0/SUL/CRF:SYM=DDACUM,XKUAB0
 RUN-TIME: 1 2 .2 SECONDS
 RUN-TIME RATIO: 36/4=7.8
 CORE USED: 7K (13 PAGES)

1

2

3

4

5

6

7

1 .MLIST F11
2 .REF *

IDENTIFICATION

PRODUCT CODE: AC-E974C-MC
PRODUCT NAME: XLPHCO LPA11-XX MODULE
PRODUCT DATE: APRIL 1981
MAINTAINER: DEC/X11 SUPPORT GROUP

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1.0 ABSTRACT

LPH IS AN IOMOD THAT EXERCISES THE LPA11-XX OPTION. NO DATA WRAPAROUND THRU THE DIGITAL I/O OR ANALOG CHANNELS ARE CHECKED. WHEN MULTI-USER MICRO-CODE (DEFAULT) IS LOADED IN THE LPA-11, ALL SELECTED DEVICES ON THE LPA-11'S I/O BUS ARE EXERCISED. WHEN DEDICATED USER MICRO-CODE IS LOADED (SR1 BIT 6 SET) IN THE LPA11, ONLY A/D'S ON THE LPA11'S I/O BUS ARE EXERCISED AND HIGH-SPEED SAMPLE TAKING IS ENABLED. A 150 KILO-HERTZ SAMPLE RATE IS THE DEFAULT USED. SAMPLING AT THIS RATE MAY PRODUCE DATA LATENCY PROBLEMS IN OTHER NPR DEVICES ON THE PDP-11 SYSTEM BUS. IN ADDITION, OTHER DEVICES MAY CAUSE LOSS OF PROGRAM EXECUTION SPEED AS TO CAUSE AN LPA-11 "USER REQUEST" ERROR CODE 241 (HIGH-BYTE OF "CSHC" ERROR TYPEOUT). FOR EXAMPLE, WHEN RUNNING AN RK11 AND DEDICATED LPA-11 MICRO-CODE WITH ONE A TO D, SR2 SHOULD BE SET TO A 1. WHEN RUNNING WITH TWO A TO D'S, SR2 SHOULD BE SET TO A 5. THE VALUE OF "SR2" DETERMINE THE SAMPLE RATE USED FOR THE A/D.

2.0 REQUIREMENTS

HARDWARE: ONE LPA-11XX INCLUDES:
1 KMC-11, 1 M8200-YC, 1 M8254, 1 CLOCK (KW11K, LPSKW OR AR11)
PLUS AT LEAST ONE OF THE FOLLOWING DEVICES:
AD11K, AA11K, DR11K, AR11, LPSDR, LPSAD, LPSVC

STORAGE: LPH REQUIRES:
1. DECIMAL WORDS: 5494
2. OCTAL WORDS: 12566
3. OCTAL BYTES: 25354

3.0 PASS DEFINITION

THE NUMBER OF NPR DATA TRANSFERS FOR A SUCCESSFUL PASS IS COMPLETELY DEPENDANT UPON THE LPA-11 CONFIGURATION.

4.0 EXECUTION TIME

ONE PASS OF LPH WITH MULTI USER (DEFAULT 10KHZ):
1 CLOCK AND 1 A TO D = 60 SECONDS
1 CLOCK AND 2 A TO D = 60 SECONDS
1 CLOCK AND 1 DIGITAL I/O = 90 SECONDS
1 CLOCK AND 1 D TO A = 90 SECONDS
ONE PASS OF LPH WITH DEDICATED A/D MICRO-CODE (150KHZ):
1 CLOCK AND 1 A TO D = 10 SECONDS
1 CLOCK AND 2 A TO D = 10 SECONDS
ONE PASS OF LPH WITH DEDICATED A/D MICRO-CODE (20KHZ):
1 CLOCK AND 1 A TO D = 20 SECONDS
1 CLOCK AND 2 A TO D = 20 SECONDS

```

88      5.0      CONFIGURATION REQUIREMENTS
89      -----
90
91      DEFAULT PARAMETERS:
92
93      DEVDADR: 170460, VECTOR: 300, BR1: 5
94
95      DEVCNT: N/A, SR1: 0, SR2: 0
96
97      REFER TO THE PROGRAM LISTING SECTION FOR THE ADDRESS
98      ASSIGNMENTS OF THE DEVICES ON THE LPA-11 I/O BUS.
99
100     REQUIRED PARAMETERS
101
102     SR1      MUST BE SET UP TO REFLECT THE TRUE LPA11-KX
103     SYSTEM CONFIGURATION (SEE OPERATIONS OPTIONS)
104     NOTE: THIS MODULE WILL ERROR AND WILL BE "DROPPED"
105     UNLESS A CLOCK HAS BEEN SPECIFIED IN SR1!
106     SR2      MAY BE CHANGED TO LOWER THE A/D TRANSFER RATE
107     IF DEDICATED MICRO-CODE HAS BEEN SELECTED.
108
109     6.0      DEVICE/OUTPUT SET-UP
110     -----
111
112     ALL CONNECTIONS TO THE A TO F, D TO A AND CLOCK INPUT
113     OR OUTPUT CONNECTOR SHOULD BE REMOVED.
114
115     7.0      MODULE OPERATION
116     -----
117
118     1. "IST1" LOADS AND VERIFIES EITHER DEDICATED OR MULTI-USER
119     MICRO-CODE INTO THE KMC-11 AS INDICATED BY BIT 6 OF "SR1".
120
121     2. "TST2" CHECKS THAT THE MICRO-CODE CAN BE STARTED.
122
123     3. "TST3" BUILDS A DEVICE ADDRESS TABLE BASED ON THE INFORMATION
124     THE OPERATOR FURNISHED IN SR1. AFTER FOLDING THE
125     TABLE, IT GIVES THE TO THE LPA-11 FOR VERIFICATION.
126
127     4. "TST4" DETERMINES THE LOCATION OF THE LPA-11K BUFFERS IN
128     18 BIT ADDRESSING. THE LIST OF "START DATA TRANSFER" IS
129     CREATED AS "TO DO" QUEUE JOB ENTRIES. THE LPA-11 CLOCK IS
130     STARTED AND THE INITIATION OF THE "TO DO" QUEUE JOBS OCCURS.
131     WHEN MULTI-USER MICRO-CODE IS SELECTED <DEFAULT>, ALL
132     DEVICES ENABLED, VIA SR1, ON THE LPA-11 I/O BUS ARE EXERCISED.
133     IF DEDICATED CODE IS SELECTED, A/D SAMPLES ARE TAKEN AT A RATE
134     CONTROLLED BY THE VALUE IN SR2.
135

```

```

136     8.0      OPERATIONS OPTIONS
137     -----
138
139     SR1: INFORMS THE LPH MODULE AS TO WHAT DEVICES ARE ON THE
140     LPA-11 I/O BUS AND THE MODE THE LPA-11 WILL BE RUNNING.
141     YOU MUST CHANGE THIS LOCATION TO REFLECT THE ACTUAL
142     SYSTEM CONFIGURATION.
143
144     SR 1 BIT=1      OCTAL      DEVICE
145     -----
146     0                000001    1ST A-11K
147     1                000002    1ST K-11K
148     2                000004    1ST L-11K
149     3                000010    1ST A-11K
150     4                000020    2ND A-11K
151     5                000040    2ND L-11K
152     6                000100    DEDICATED USER MICRO-CODE SELECTED
153     7                000200    3RD L-11K
154     8                000400    4TH L-11K
155     9                001000    5TH L-11K
156     10               002000    A-11K
157     11               004000    UNUSED
158     12               010000    LPSAD (LPS A/D)
159     13               020000    LPSK* (LPS REAL TIME CLOCK)
160     14               040000    LPSVC (LPS O/A)
161     15               100000    LPSDR (LPS DIGITAL I/O)
162
163     NOTE: BEFORE RUNNING THIS MODULE, ALL CONNECTIONS TO THE
164     A TO F, D TO A AND CLOCK INPUT OR OUTPUT CONNECTOR
165     SHOULD BE REMOVED. IF NOT, IT IS UNPREDICTABLE WHAT
166     THESE DEVICE WILL DO.
167
168     SR2: INFORMS THE MODULE AS TO THE A/D DATA TRANSFER RATE
169
170           OCTAL VALUE      RATE (KILOHERTZ)
171           -----
172           0                150K
173           1                134K
174           2                118K
175           3                100K
176           4                80K
177           5                60K
178           6                40K
179           7                20K
180
181
182     9.0      NON-STANDARD PRINTOUTS
183     -----
184
185     ALL PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED
186     IN THE DEC/X11 DOCUMENT.
187

```



```
188 .LIST BIN
189 %
190 000000' IOMOD <LPHC >,170460,300,5,0,0,50,0
191 000000' MODULE 140000,LPHC ,170460,300,5,0,0,50,0
192 .TITLE LPHC DEC/X11 SYSTEM EXERCISER MODULE
193 ; DDXCUM VERSION 6 Z3-MAY-78
194 .LIST BIN
195 *****
196 000000' BEGIN:
197 000000' 050114 041510 040 MODNAM: .ASCII /LPHC / ;MODULE NAME.
198 000005' 000 XFLAG: .BYTE UPEN ;USED TO KEEP TRACK OF WBUF USAGE
199 000006' 170460 ADDR: 170460+0 ;1ST DEVICE ADDR.
200 000010' 000300 VECTOR: 300+0 ;1ST DEVICE VECTOR.
201 000012' 240 BK1: .BYTE PRTY5+0 ;1ST BP LEVEL.
202 000013' 000 BK2: .BYTE PRTY0+0 ;2ND BP LEVEL.
203 000014' 000001 DVID1: 0+1 ;DEVICE INDICATOR 1.
204 000016' 000000 SR1: UPEN ;SWITCH REGISTER 1.
205 000020' 000000 SR2: UPEN ;SWITCH REGISTER 2.
206 000022' 000000 SR3: UPEN ;SWITCH REGISTER 3.
207 000024' 000000 SR4: UPEN ;SWITCH REGISTER 4.
208 *****
209 000026' 140000 SIAT: 140000 ;STATUS WORD.
210 000030' 000506' INIT: START ;MODULE START ADDR.
211 000032' 000224' SPOINT: MODSP ;MODULE STACK POINTER.
212 000034' 000000 PASCNT: 0 ;PASS COUNTER.
213 000036' 000050 ICOUNT: 50 ;# OF ITERATIONS PER PASS=50
214 000040' 000000 SOFCNT: 0 ;LOC TO COUNT ITERATIONS
215 000042' 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
216 000044' 000000 SDFPAS: 0 ;LOC TO SAVE TOTAL HARD ERRORS
217 000046' 000000 HRDPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
218 000050' 000000 SYSCNT: 0 ;LOC TO SAVE HARD ERRORS PER PASS
219 000052' 000000 RANNUM: 0 ;# OF SYS ERRORS ACCUMULATED
220 000054' 000000 CONFIG: 0 ;MULDS RANDOM # WHEN RAND MACRO IS CALLED
221 000056' 000000 RES1: 0 ;RESERVED FOR MONITOR USE
222 000058' 000000 RES2: 0 ;RESERVED FOR MONITOR USE
223 000060' 000000 SVR0: UPEN ;LOC TO SAVE R0.
224 000062' 000000 SVR1: UPEN ;LOC TO SAVE R1.
225 000064' 000000 SVR2: UPEN ;LOC TO SAVE R2.
226 000066' 000000 SVR3: UPEN ;LOC TO SAVE R3.
227 000070' 000000 SVR4: UPEN ;LOC TO SAVE R4.
228 000072' 000000 SVR5: UPEN ;LOC TO SAVE R5.
229 000074' 000000 SVR6: UPEN ;LOC TO SAVE R6.
230 000076' 000000 CSRA: UPEN ;ADDR OF CURRENT CSP.
231 000100' 000000 ACSRA: UPEN ;ADDR OF GOOD DATA, OR
232 000102' 000000 WASADH: 0 ;CONTENTS OF CSR.
233 000104' 000000 ASTAT: UPEN ;ADDR OF BAD DATA, OR
234 000106' 000000 ERRTYP: 0 ;STATUS REG CONTENTS.
235 000108' 000000 ASP: UPEN ;TYPE OF ERROR
236 000110' 000000 AWAS: UPEN ;EXPECTED DATA.
237 000112' 000506' RSTPT: RSTPT ;ACTUAL DATA.
238 000114' 000000 WDT0: UPEN ;PRESTART ADDRESS AFTER END OF PASS
239 000116' 000000 WDFR: UPEN ;WORDS TO MEMORY PER ITERATION
240 000120' 000000 INTR: UPEN ;WORDS FROM MEMORY PER ITERATION
241 000122' 000000 IDNUM: 0 ;# OF INTERRUPTS PER ITERATION
242 *****
243 000124' 000000 ;MODULE IDENTIFICATION NUMBER=0
```

```
244 000224' MODSP:
245 *****
246 000224' 000001 MODREV: 1 ;NEW LPHC PA001
247
248 000226' 177763 SK2VAL: -13. ;CLOCK PRESET VALUE FOR SK2 RATE CODE
249 000230' KMCSR:
250 ;
251 ;ADDRESS OF KMC-11 OF LPA-11 THE ADDR FOR KMA00 MAY BE
252 ; CHANGED BY THE USER TO REFLECT
253 ; A DIFFERENT KMC-11 ADDR. THE
254 ; REST OF THE ADDRESSES WILL
255 ; BE CHANGED BY THE PROGRAM.
256 ;
257
258 000230' LPCI:
259 000230' 170460 KMA00: .WORD 170460 ;BASE KMC ADDR. MAY BE PATCHED BY USFR.
260
261 000232' LPMR:
262 000232' 170461 KMA01: .WORD 170460+1 ;DO NOT <KMC-CSR ADDR
263 000234' LPC0: ;
264 000234' 170462 KMA02: .WORD 170460+2 ;PATCH <
265 000236' LPS0: ;
266 000236' 170463 KMA03: .WORD 170460+3 ;THIS AREA <
267 000240' LPA0: ;
268 000240' 170464 KMA04: .WORD 170460+4 ;
269 000242' LPA0H: ;
270 000242' 170465 KMA05: .WORD 170460+5 ;DO NOT <
271 000244' LPM5: ;
272 000244' 170466 KMA06: .WORD 170460+6 ;PATCH <
273 000246' LPM52: ;
274 000246' 170467 KMA07: .WORD 170460+7 ;THIS AREA <
275
276 000250' 000005 VERSN: .WORD 5 ;CURRENT VERSION NUMBER OF MICROCODE.
277
278
279 000252' 170400 AD11K: .WORD 170400 ;AD11K #1 ADDRESS.
280 000254' 170404 KW11K: .WORD 170404 ;KW11K ADDRESS.
281 000256' 167770 DR11K1: .WORD 167770 ;DR11K #1 ADDR.
282 000260' 170416 AA11K: .WORD 170416 ;AA11K ADDRESS.
283 000262' 170440 AD11K2: .WORD 170440 ;AD11K #2 ADDRESS.
284 000264' 167760 DR11K2: .WORD 167760 ;DR11K #2 ADDRESS.
285 000266' 167750 DR11K3: .WORD 167750 ;DR11K #3 ADDRESS.
286 000270' 167740 DR11K4: .WORD 167740 ;DR11K #4 ADDRESS.
287 000272' 167730 DR11K5: .WORD 167730 ;DR11K #5 ADDRESS.
288 000274' 170400 AR11K: .WORD 170400 ;AR11K ADDRESS.
289 000276' 170400 LPS11: .WORD 170400 ;LPS11 BASE ADDRESS.
```

```

290
291 000300' 000115      UCODE: .ASCIIZ /M/      ;TYPE OF U CODE SELECTED
292 000302' 015346'      UCUES: .WORD MAST      ;POINTER TO SELECTED U CODE
293 000304' 000000'      SDDAT: .WORD 0      ;TEMP STORAGE.
294 000306' 000000'      SGDDAT: .WORD 0      ;TEMP STORAGE.
295
296 000310' 007326'      GETADR: .WORD BUFFER      ;THIS LOCATION USED TO GET PA HITS.
297 000312' 000000'      GETPAR: .WORD 0      ;CONTAINS PA HITS (FROM MONITOR)
298 000314' 000000'      GETEAR: .WORD 0      ;CONTAINS EA BITS SHIFTED.
299 000316' 007326'      MBUFF0: .WORD BUFF0      ;THIS LOC USED TO GET THE PA OF THE BUFFER
300 000320' 000000'      GBUFF0: .WORD 0      ;CONTAINS PA BITS (FROM MONITOR)
301 000322' 000000'      EBUFF0: .WORD 0      ;CONTAINS EA BITS SHIFTED
302 000324' 010330'      MBUFF1: .WORD BUFF1      ;THIS LOC USED TO GET THE PA OF BUFFER1
303 000326' 000000'      GBUFF1: .WORD 0      ;CONTAINS PA (FROM MONITOR)
304 000330' 000000'      EBUFF1: .WORD 0      ;CONTAINS EA BITS SHIFTED
305 000332' 012334'      MBUFF2: .WORD BUFF2      ;THIS LOC USED TO GET PA ADDS OF BUFFER2
306 000334' 000000'      GBUFF2: .WORD 0      ;CONTAINS PA (FROM MONITOR)
307 000336' 000000'      EBUFF2: .WORD 0      ;CONTAINS EA BITS
308 000340' 013336'      MBUFF3: .WORD BUFF3      ;THIS LOC USED TO GET PA OF BUFFER3
309 000342' 000000'      GBUFF3: .WORD 0      ;CONTAINS PA (FROM MONITOR)
310 000344' 000000'      EBUFF3: .WORD 0      ;CONTAINS EA BITS
311 000346' 005240'      MJOB0: .WORD JOB0      ;THIS LOC USED TO GET PA OF JOB0
312 000350' 000000'      GJOB0: .WORD 0      ;CONTAINS PA (FROM MONITOR)
313 000352' 000000'      EJOB0: .WORD 0      ;CONTAINS EA BITS
314 000354' 005334'      MJOB1: .WORD JOB1      ;THIS LOC USED TO GET PA OF JOB1
315 000356' 000000'      GJOB1: .WORD 0      ;CONTAINS PA (FROM MONITOR)
316 000360' 000000'      EJOB1: .WORD 0      ;CONTAINS EA BITS
317 000362' 005430'      MJOB2: .WORD JOB2      ;THIS LOC USED TO GET PA OF JOB2
318 000364' 000000'      GJOB2: .WORD 0      ;CONTAINS PA (FROM MONITOR)
319 000366' 000000'      EJOB2: .WORD 0      ;CONTAINS EA BITS
320 000370' 005524'      MJOB3: .WORD JOB3      ;THIS LOC USED TO GET PA OF JOB3
321 000372' 000000'      GJOB3: .WORD 0      ;CONTAINS PA (FROM MONITOR)
322 000374' 000000'      EJOB3: .WORD 0      ;CONTAINS EA BITS
323 000376' 005620'      MJOB4: .WORD JOB4      ;THIS LOC USED TO GET PA OF JOB4
324 000400' 000000'      GJOB4: .WORD 0      ;CONTAINS PA (FROM MONITOR)
325 000402' 000000'      EJOB4: .WORD 0      ;CONTAINS EA BITS
326 000404' 005714'      MJOB5: .WORD JOB5      ;THIS LOC USED TO GET PA OF JOB5
327 000406' 000000'      GJOB5: .WORD 0      ;CONTAINS PA (FROM MONITOR)
328 000410' 000000'      EJOB5: .WORD 0      ;CONTAINS EA BITS
329 000412' 006010'      MJOB6: .WORD JOB6      ;THIS LOC USED TO GET PA OF JOB6
330 000414' 000000'      GJOB6: .WORD 0      ;CONTAINS PA (FROM MONITOR)
331 000416' 000000'      EJOB6: .WORD 0      ;CONTAINS EA BITS
332 000420' 006104'      MJOB7: .WORD JOB7      ;THIS LOC USED TO GET PA OF JOB7
333 000422' 000000'      GJOB7: .WORD 0      ;CONTAINS PA (FROM MONITOR)
334 000424' 000000'      EJOB7: .WORD 0      ;CONTAINS EA BITS
335 000426' 005332'      MJOB00: .WORD JOB00      ;THIS LOC USED TO GET PA OF JOB00
336 000430' 000000'      GJOB00: .WORD 0      ;CONTAINS PA (FROM MONITOR)
337 000432' 000000'      EJOB00: .WORD 0      ;CONTAINS EA BITS
338 000434' 005426'      MJOB10: .WORD JOB10      ;THIS LOC USED TO GET PA OF JOB10
339 000436' 000000'      GJOB10: .WORD 0      ;CONTAINS PA (FROM MONITOR)
340 000440' 000000'      EJOB10: .WORD 0      ;CONTAINS EA BITS
341 000442' 005522'      MJOB20: .WORD JOB20      ;THIS LOC USED TO GET PA OF JOB20
342 000444' 000000'      GJOB20: .WORD 0      ;CONTAINS PA (FROM MONITOR)
343 000446' 000000'      EJOB20: .WORD 0      ;CONTAINS EA BITS
344 000450' 005616'      MJOB30: .WORD JOB30      ;THIS LOC USED TO GET PA OF JOB30
345 000452' 000000'      GJOB30: .WORD 0      ;CONTAINS PA (FROM MONITOR)

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346 000454' 000000'      EJOB30: .WORD 0      ;CONTAINS EA BITS
347 000456' 005712'      MJOB40: .WORD JOB40      ;THIS LOC USED TO GET PA OF JOB40
348 000460' 000000'      GJOB40: .WORD 0      ;CONTAINS PA (FROM MONITOR)
349 000462' 000000'      EJOB40: .WORD 0      ;CONTAINS EA BITS
350 000464' 006006'      MJOB50: .WORD JOB50      ;THIS LOC USED TO GET PA OF JOB50
351 000466' 000000'      GJOB50: .WORD 0      ;CONTAINS PA (FROM MONITOR)
352 000470' 000000'      EJOB50: .WORD 0      ;CONTAINS EA BITS
353 000472' 006102'      MJOB60: .WORD JOB60      ;THIS LOC USED TO GET PA OF JOB60
354 000474' 000000'      GJOB60: .WORD 0      ;CONTAINS PA (FROM MONITOR)
355 000476' 000000'      EJOB60: .WORD 0      ;CONTAINS EA BITS
356 000500' 006176'      MJOB70: .WORD JOB70      ;THIS LOC USED TO GET PA OF JOB70
357 000502' 000000'      GJOB70: .WORD 0      ;CONTAINS PA (FROM MONITOR)
358 000504' 000000'      EJOB70: .WORD 0      ;CONTAINS EA BITS
359
360
361 000506'      START:
362 000506'      RESTRT:
363 000506' 016767 177274 177514      MOV ADDR,KMAD0      ;SET UP BASE ADDR.
364
365      ;THIS SECTION OF CODE HANDLES INITIALIZING LPA-11 FUNCTIONS
366
367
368 000514' 016700 177510      MOV KMAD0,R0      ;GET KMC-11 ADDRESS.
369 000520' 012701 000232'      MOV #KMAD1,R1      ;GET ADDR. OF ADDR. LIST.
370
371 000524' 005200      64$: INC R0      ;UPDATE ADDR.
372 000526' 010021      MOV R0,(R1)+      ;WRITE ADDR.
373 000530' 020127 000250'      CMP R1,#KMAD7+2      ;DONE ALL ADDRESSES?
374 000534' 001373      BNE 64$      ;NO - DO NEXT ADDR.
375
376      ;IF DEDICATED MICRO-CODE WAS SELECTED
377      ;THE ENCODE SR2 VALUE BECOMES THE CLOCK PRESET VALUES
378      ;IF NOT USE THE DEFAULT RATE (ABOUT 10KHZ)
379
380 000536' 016700 177256      MOV SR2,R0      ;GET CURRENT CODE
381 000542' 042700 177770      BIC #177770,R0      ;MASK OFF OTHER BITS
382 000546' 006300      ASL R0      ;CONVERT INTO WORD OFFSET
383 000550' 016067 000560' 177450      MOV 105(R0),SR2VAL      ;INDEX INTO TABLE OF PRESET VALUES
384 000556' 000410      BR 10$      ;BR OVER TABLE OF VALUES
385 000560' 177763      10$: .WORD -13.      ;150K RATE
386 000562' 177761      .WORD -15.      ;134K
387 000564' 177757      .WORD -17.      ;118K
388 000566' 177754      .WORD -20.      ;100K
389 000570' 177747      .WORD -25.      ;80K
390 000572' 177737      .WORD -33.      ;60.6K
391 000574' 177716      .WORD -50.      ;40K
392 000576' 177635      .WORD -99.      ;20K
393
394 000600' 005767 177212      1$: TST SR1      ;CHECK TO BE SURE SR1 HAS BEEN INITIALIZED BY THE OPERATOR
395 000604' 001010      HNE TST1      ;HAS SR1 BEEN MODIFIED ?
396 000606' 012767 000007 177272      MOV #7,ERRTYP      ;YES-BRANCH TO START OF TESTS
397 000614' 104403 000000' 006200'      MSGNS,BEGIN,WAHMSG      ;LOAD "SELECTION" ERROR CODE
398 000622' 104410 000000'      ENDS,BEGIN      ;ASCII MESSAGE CALL WITH COMMON HEADER
399
400      ;ABORT THIS MODULE SINCE WE HAVE NOT
401      ;BEEN INFORMED WHICH DEVICES ARE ON
402      ;THE I/O BUS VIA SR1

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402                                ;DETERMINE THE TYPE OF MICROCODE TO LOAD AND THEN LOAD AND VERIFY THE LOAD
403
404 000626' 032767 000100 177162 TST1: BIT #BIT6,SR1 ;TEST MICRO-CODE TYPE INDICATOR
405 000634' 001010 HNE 15 ;BR IF DEDICATED USER
406 000636' 012700 015346' MOV #MMAS1,R0 ;GET ADDRESS OF MULTI-USER
407 000642' 010067 177434 MOV R0,UCODES ;SAVE STARTING ADDRESS OF U CODE
408 000646' 012767 000115 177424 MOV #M,UCODE ;LOAD TYPE OF UCODE
409 000654' 000407 BR 2S ;BR OVER SETUP
410 000656' 012700 021352' 1S: MOV #DMAS1,R0 ;GET ADDRESS OF DEDICATED USER
411 000662' 010067 177414 MOV R0,UCODES ;SAVE STARTING ADDRESS
412 000666' 012767 000104 177404 MOV #D,UCODE ;LOAD TYPE OF UCODE
413 000674' 005077 177330 2S: CLR #KMA0 ;CLEAR CSR
414 000700' 005077 177334 CLR #KMA04 ;CLEAR SCRAM ADDRESS
415 000704' 052777 002000 177316 3S: BIS #2000,#KMA0 ;SELECT CRAM
416 000712' 012077 177326 MOV (R0)+,#KMA06 ;WRITE DATA
417 000716' 052777 020000 177304 BIS #2000,#KMA0 ;SET CRAM WRITE
418 000724' 005077 177300 CLR #KMA0 ;DISABLE CRAM
419 000730' 005277 177304 INC #KMA04 ;UPDATE CRAM ADDRESS
420 000734' 021027 177777 CMP (R0),#-1 ;ALL DONE
421 000740' 001361 BNE 3S ;NO-LOOP
422 000742' 005077 177272 CLR #KMA04 ;CLEAR CRAM ADDRESS
423 000746' 016700 177330 MOV UCODES,R0 ;GET MICRO-CODE ADDRESS
424 000752' 052777 002000 177250 4S: BIS #2000,#KMA0 ;SELECT CRAM
425 000760' 022077 177260 CMP (R0)+,#KMA06 ;DATA-OK?
426 000764' 001010 HNE 5S ;NO-REPORT ERROR
427 000766' 021027 177777 CMP (R0),#-1 ;ALL DONE
428 000772' 001431 BEQ TST2 ;YES EXIT
429 000774' 005077 177230 CLR #KMA0 ;NO DESELECT CRAM
430 001000' 005277 177234 INC #KMA04 ;UPDATE CRAM ADDRESS
431 001004' 000762 BR 4S ;BR AND CHECK NEXT WORD
432                                ;SETUP CODE FOR ERROR REPORTING
433 001006' 016767 177232 177066 5S: MOV #KMA0,ACSR ;STORE ADDRESS OF KMC-11
434 001014' 017767 177220 177062 MOV #KMA04,#ASADR ;ADDRESS LOCATION OF FAILING MICRO-CODE
435 001022' 017767 177216 177060 MOV #KMA06,#AAS ;ACTUAL MICRO-CODE LOADED INTO THE KMC
436 001030' 014067 177052 MOV -(R0),ASB ;EXPECTED MICRO-CODE
437 001034' 010067 177042 MOV R0,SBADR ;ADDRESS OF EXPECTED MICRO-CODE
438 001040' 012767 000052 177040 MOV #S2,ERRTYP ;LOAD "MICRO-CODE" NOT LOADED ERROR CODE
439                                ;*****
440 001046' 104404 000000' DATERS,BEGIN ;DATA ERROR!!!
441                                ;*****
442                                ;*
443                                ;*THIS IS A FATAL KMC ERROR.
444                                ;*THIS ERROR WAS DETECTED BY COMPARING THE KNOWN
445                                ;*MICRO-CODE AGAINST THAT WHICH WAS LOADED
446                                ;*SHADR" CONTAINS THE ADDRESS OF WHAT THE MICRO-CODE SHOULD HAVE BEEN
447                                ;*WAS ADR" CONTAINS THE ADDRESS OF THE MICRO-CODE THAT WAS IN ERROR
448                                ;*AAS" CONTAINS THE MICRO-CODE IN ERROR
449                                ;*ASB" CONTAINS WHAT THE MICRO-CODE SHOULD HAVE BEEN.
450                                ;*DUE TO THE NATURE OF THIS ERROR, YOU CANNOT RUN "DXLPH"
451                                ;*IT IS SUGGESTED THAT YOU RUN "NO-11-DRLPA".
452 001052' 000167 177430 JMP START ;TRY STARTING AGAIN

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453                                ;TEST #2
454                                ;IN THIS TEST WE WILL TRY TO START
455                                ;THE USER MICRO-CODE PREVIOUSLY LOAD BY "TST1"
456                                ;
457 001056' TST2:
458 001050' 052777 040000 177144 BIS #BIT14,#KMA0 ;SET INIT.
459 001064' 012701 000040 MOV #40,R1 ;LOAD DELAY COUNTER
460 001070' 1S:
461 001070' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
462 001074' 104407 000000' HREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
463 001100' 005301 DEC R1 ;DELAY
464 001102' 001372 BNE 1S ; LOOP
465 001104' 012777 104000 177116 MOV #BIT15|BIT111,#KMA0 ;BIT 15 OF THE KMC CSR SHOULD BE
466                                ;SET AT THIS TIME.
467 001112' 012701 000500 MOV #320,R1 ;LOAD DELAY COUNTER
468 001116' 032777 000200 177104 2S: BIT #BIT7,#LPCI ;BIT 7 OF CONTROL IN REG. (READY IN)
469 001124' 001020 HNE 3S ;SHOULD BE SET AT INIT.
470 001126' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
471 001132' 104407 000000' HREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
472 001136' 005301 DEC R1 ;DELAY
473 001140' 001366 BNE 2S ; LOOP
474 001142' 012767 000034 176736 MOV #34,ERRTYP ;LOAD "DEVICE WILL NOT INIT" ERROR CODE
475 001150' 004767 005220 JSR PC,RESET0 ;GO AND READ LPA-11 REGISTERS
476                                ;*****
477 001154' 104405 000000' 000000 HDEPS,BEGIN,NULL ;LPA-11 ERROR - READY IN NOT SET AT INIT.
478                                ;*****
479 001162' 000167 177320 JMP START ;TRY STARTING AGAIN
480
481 001166' 032777 000200 177040 3S: BIT #BIT7,#LPCU ;CHECK READY OUT, IT SHOULD
482 001174' 001412 BEQ TST3 ;BE CLEAR AT INIT.
483 001176' 012767 000034 176702 MOV #34,ERRTYP ;LOAD "DEVICE WILL NOT INIT" ERROR CODE
484 001204' 004767 005164 JSR PC,RESET0 ;GO AND READ LPA-11 REGISTERS
485                                ;*****
486 001210' 104405 000000' 000000 HDEPS,BEGIN,NULL ;LPA-11 ERROR - READY OUT SET AT INIT.
487                                ;*****
488 001216' 000167 177264 JMP START ;TRY STARTING AGAIN

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489          ;TEST #3
490          ;INIT TEST IN THIS TEST WE MAKE SURE THE KMC MICRO-
491          ;CODE CAN FIND ALL ADDRESSES OF DEVICES TO BE TESTED.
492          ;NOW MAKE SURE A CLOCK IS SELECTED
493          TST3: MOV     #DEVLT+2,R1      ;PRIME POINTER TO THE "INIT" COMMAND
494          BIT     #BIT1,SR1             ;Kw11k CLOCK??
495          BEQ     1S                     ;NO
496          MOV     K11K,(R1)+            ;YES FIX ADDR.
497          BR      TST3A
498          BIT     #BIT10,SR1            ;AR11 CLOCK??
499          BEQ     2S                     ;NO
500          MOV     AR11K,(R1)            ;YES FIX AR11 CLOCK ADDR.
501          ADD     #4,(R1)+
502          BR      TST3A
503          BIT     #BIT13,SR1            ;LPS CLOCK?
504          BEQ     3S                     ;RR IF NO CLOCK WAS SELECTED
505          MOV     LPS11,(R1)            ;YES FIX LPS11 CLOCK ADDR.
506          ADD     #4,(R1)+
507          BR      TST3A
508          MOV     #7,ERRTYP             ;LOAD "SELECTION" ERROR CODE
509          ;*****
510          HIDEKS,BEGIN, NULL            ;NO CLOCK SELECTED FOR TEST...
511          ;*****
512          ;CAN NOT DO ANY MORE TESTS WITHOUT CLOCK PRESENT! YOU MUST SELECT A CLOCK.
513          ENDS,BEGIN
514          ;NOW DETERMINE IF ANOTHER DEVICE WAS SELECTED
515          TST3A: BIT     #BIT10,SR1      ;TEST IF AR11 SELECTED
516          RNE     1S13B                 ;RR IF AR11
517          BIT     #155675,SP1           ;TEST IF OTHER THAN A CLOCK WAS SELECTED
518          RNE     1S13B                 ;RR IF OTHER THAN CLOCK ALSO SELECTED
519          MOV     #7,ERRTYP             ;LOAD "SELECTION" ERROR CODE
520          ;*****
521          HIDEKS,BEGIN, NULL            ;ONLY A CLOCK WAS SELECTED...
522          ;*****
523          ENDS,BEGIN
524          ;NOW DECODE "SK1" FOR SYSTEM CONFIG.
525          ; AND BUILD INITIALIZE COMMAND IN NDA FORMAT
526          TST3B: MOV     #1,(R1)+        ;NO "CLOCK R" WILL BE USED
527          BIT     #BIT0,SR1             ;AD11k FOR TEST??
528          BEQ     2S                     ;NO
529          MOV     AD11K,(R1)+            ;YES FIX AD11K ADDR.
530          BR      3S
531          BIT     #BIT10,SR1            ;AR11 A/D?
532          BEQ     2S
533          MOV     AR11K,(R1)+
534          BR      3S
535          BIT     #BIT12,SR1            ;LPS A/D?
536          BEQ     2S
537          MOV     LPS11,(R1)+
538          BR      3S
539          MOV     #1,(R1)+              ;INDICATE NO "FIRST A TO D"
540          BIT     #BIT4,SR1              ;2ND AD11K?
541          BEQ     3S
542          MOV     AD11K2,(R1)+
543          BR      3S
544          MOV     #1,(R1)+              ;INDICATE NO "SECOND A TO D"

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545          001474' 032767 000010 176314 32S:  BIT     #BIT3,SR1      ;AA11K?
546          001502' 001403                BEQ     3S
547          001504' 016721 176550                MOV     AA11K,(R1)+
548          001510' 000424                BR      4S
549          001512' 032767 002000 176276 33S:  BIT     #BIT10,SR1      ;AR11?
550          001520' 001405                BEQ     3S
551          001522' 016711 176546                MOV     AR11K,(R1)
552          001526' 062721 000016                ADD     #16,(R1)+
553          001532' 000413                BR      4S
554          001534' 032767 040000 176254 34S:  BIT     #BIT14,SP1      ;LPS D/A?
555          001542' 001405                BEQ     3S
556          001544' 016711 176526                MOV     LPS11,(R1)
557          001550' 062721 000016                ADD     #16,(R1)+
558          001554' 000402                BR      4S
559          001556' 012721 000001                MOV     #1,(R1)+      ;INDICATE NO "D TO A"
560          001562' 032767 000004 176226 40S:  BIT     #BIT2,SR1      ;DR11K?
561          001570' 001403                BEQ     4S
562          001572' 016721 176460                MOV     DR11K1,(R1)+
563          001576' 000413                BR      5S
564          001600' 032767 100000 176210 41S:  BIT     #BIT15,SR1      ;LPS I/O?
565          001606' 001405                BEQ     4S
566          001610' 016711 176462                MOV     LPS11,(R1)
567          001614' 062721 000010                ADD     #10,(R1)+    ;FIX LPS11 I/O ADDR.
568          001620' 000402                BR      5S
569          001622' 012721 000001                MOV     #1,(R1)+    ;INDICATE NO "1ST DIGITAL I/O"
570          001626' 032767 000040 176162 50S:  BIT     #BIT5,SR1      ;DR11K #2 ?
571          001634' 001403                BEQ     5S
572          001636' 016721 176422                MOV     DR11K2,(R1)+
573          001642' 000402                BR      5S
574          001644' 012721 000001                MOV     #1,(R1)+    ;INDICATE NO "2ND DIGITAL I/O"
575          001650' 032767 000200 176140 52S:  BIT     #BIT7,SR1      ;DR11K #3 ?
576          001656' 001403                BEQ     5S
577          001660' 016721 176402                MOV     DR11K3,(R1)+
578          001664' 000402                BR      5S
579          001666' 012721 000001                MOV     #1,(R1)+    ;INDICATE NO "3RD DIGITAL I/O"
580          001672' 032767 000400 176116 54S:  BIT     #BIT8,SR1      ;DR11K #4 ?
581          001700' 001403                BEQ     5S
582          001702' 016721 176362                MOV     DR11K4,(R1)+
583          001706' 000402                BR      5S
584          001710' 012721 000001                MOV     #1,(R1)+    ;INDICATE NO "4TH DIGITAL I/O"
585          001714' 032767 001000 176074 56S:  BIT     #BIT9,SR1      ;DR11K #5 ?
586          001722' 001403                BEQ     5S
587          001724' 016721 176342                MOV     DR11K5,(R1)+
588          001730' 000402                BR      6S
589          001732' 012711 000001                MOV     #1,(R1)      ;INDICATE NO "5TH DIGITAL I/O"
590          001736' 116767 176306 004525 60S:  MOV     #1,ERRTYP      ;SAVE THE "VERSION" OF MICRO-CODE USED IN THIS MODULE
591          001744' 105067 004520                CLRB     DEVLT        ;ENSURE "INIT" CODE

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```
592 ;NOW START THE "INITILIZE COMMAND" TO THE LPA-11
593
594 001750' 012761 006470' TST3C: MOV #DEVLIST,P1 ;SET TO GET PA
595 001754' 010167 176330 MOV R1,GETADR ;MOV ADKS SO WE CAN GET THE PA
596 001760' 104415 000000' 000310' GETPAS,BEGIN, GETADR ;GET PHYSICAL ADDRESS FROM 16-BIT GETADR
597 001766' 016701 176320 MOV GETPAS,R1 ;RETURN PHYSICAL ADDR.
598 001772' 010177 176242 MOV R1,MLPADL ;SET INIT POINTER
599 001776' 006267 176312 ASH GETEAB ;SHIFT EA BITS INTO BITS 2 AND 3
600 002002' 006267 176306 ASH GETEAB
601 002006' 142777 000014 176214 RICH #14,MLPC1 ;REMOVE OLD "EA" BITS
602 002014' 156777 176274 RICH GETEAB,MLPC1 ;SET EA BITS
603 002022' 052777 000001 176206 BIS #BIT0,MLPC1 ;SET GO!
604 002030' 005069 CLR R0 ;CLEAR "DELAY VALUE"
605
606 002032' 1S: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
607 002032' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
608 002036' 104407 000000' BIT6 #BIT7,MLPC1 ;DONE?
609 002042' 132777 000200 176160 RNE ZS ;YES!
610 002050' 001020 INCR R0 ;NO-WAIT
611 002052' 105200 RNE ZS ;WAIT
612 002054' 001366 INCR R0 ;NO-WAIT
613 002056' 004767 004312 RNE ZS ;HUT GOT TOO LONG...
614 002062' 122767 000326 176013 JSR PC,RESET0 ;GO AND READ LPA-11 REGISTERS
615 002070' 001425 CMPB #326,ACSR+1 ;TEST IF DEVICE NOT FOUND ERROR CODE
616 002072' 012767 000034 176006 BEQ ZS ;FOR IF THAT ERROR CODE
617 MOV #34,ERRTYF ;LOAD "DEVICE WILL NOT INITILIZE" ERROR CODE
618 002100' 104405 000000' 000000' HDEFS,BEGIN,NULL ;LPA FAILED TO FINISH INIT.
619 ;*****
620 002106' 000167 176374 JMP START ;TRY STARTING AGAIN
621 ;FOR HERE UPON "LPA ERROR FLAG" BEING SET
622 002112' 105777 176116 ZS: TST #LPCU ;DID CONTROL OUT SET WITH ERROR??
623 002116' 100022 BPL TS4 ;FOR IF NO ERROR
624 002120' 004767 004250 JSR PC,RESET0 ;GO AND READ LPA-11 REGISTERS
625 002124' 012767 000044 175754 MOV #44,ERRTYF ;LOAD "FLAG SHOULD NOT BE SET" ERROR CODE
626 ;*****
627 002132' 104405 000000' 000000' HDEFS,BEGIN,NULL ;LPA ERROR CODE - CHECK RETURNED ERROR CODE
628 ;*****
629 ; AGAINST DOCUMENTATION.
630 002140' 000167 176342 JMP START ;TRY STARTING AGAIN
631 ;FOR HERE UPON "DEVICE NOT FOUND ON LPA I/O BUS" LPA ERROR CODE
632 002144' 012767 000006 175734 ZS: MOV #0,ERRTYF ;LOAD "DEVICE NON-EXISTENT" ERROR CODE
633 ;*****
634 002152' 104405 000000' 000000' HDEFS,BEGIN,NULL ;DEVICE DOES NOT EXIST? CHECK VALUE OF SH1
635 ;*****
636 002160' 000167 176322 JMP START ;TRY STARTING AGAIN
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637 ;TEST 4, FIRST GET READY BY FIRST GETTING THE PHYSICAL ADDRESS
638 ; OF ALL THE JOBS AND BUFFERS
639
640 002164' 012767 007326' 176116 TST4: MOV #BUFFER,GETADR ;SET UP TO GET ALL PA'S
641 002172' 104415 000000' 000310' GETPAS,BEGIN, GETADR ;GET PHYSICAL ADDRESS FROM 16-BIT GETADR
642 002200' 104415 000000' 000316' GETPAS,BEGIN, MBUFF0 ;GET PHYSICAL ADDRESS FROM 16-BIT MBUFF0
643 002206' 104415 000000' 000324' GETPAS,BEGIN, MBUFF1 ;GET PHYSICAL ADDRESS FROM 16-BIT MBUFF1
644 002214' 104415 000000' 000332' GETPAS,BEGIN, MBUFF2 ;GET PHYSICAL ADDRESS FROM 16-BIT MBUFF2
645 002222' 104415 000000' 000340' GETPAS,BEGIN, MBUFF3 ;GET PHYSICAL ADDRESS FROM 16-BIT MBUFF3
646 002230' 104415 000000' 000346' GETPAS,BEGIN, MJOB0 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB0
647 002236' 104415 000000' 000354' GETPAS,BEGIN, MJOB1 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB1
648 002244' 104415 000000' 000362' GETPAS,BEGIN, MJOB2 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB2
649 002252' 104415 000000' 000370' GETPAS,BEGIN, MJOB3 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB3
650 002260' 104415 000000' 000376' GETPAS,BEGIN, MJOB4 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB4
651 002266' 104415 000000' 000404' GETPAS,BEGIN, MJOB5 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB5
652 002274' 104415 000000' 000412' GETPAS,BEGIN, MJOB6 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB6
653 002302' 104415 000000' 000420' GETPAS,BEGIN, MJOB7 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB7
654 002310' 104415 000000' 000426' GETPAS,BEGIN, MJOB8 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB8
655 002316' 104415 000000' 000434' GETPAS,BEGIN, MJOB9 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB9
656 002324' 104415 000000' 000442' GETPAS,BEGIN, MJOB10 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB10
657 002332' 104415 000000' 000450' GETPAS,BEGIN, MJOB20 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB20
658 002340' 104415 000000' 000456' GETPAS,BEGIN, MJOB30 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB30
659 002346' 104415 000000' 000464' GETPAS,BEGIN, MJOB40 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB40
660 002354' 104415 000000' 000472' GETPAS,BEGIN, MJOB50 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB50
661 002362' 104415 000000' 000500' GETPAS,BEGIN, MJOB60 ;GET PHYSICAL ADDRESS FROM 16-BIT MJOB60
662 002370' 012700 002422' MOV #TABLE0,P0 ;LOAD POINTER TO TABLE
663 002374' 005710 1S: TST (R0) ;TEST IF AT END OF TABLE
664 002376' 001426 BEQ TST4A ;FOR IF DONE ALL ENTRIES
665 002400' 011002 MOV (R0),R2 ;GET AN ADDRESS VALUE
666 002402' 011201 MOV (R2),P1 ;GET THE VALUE
667 002404' 006201 ASH R1 ;MOVE THE DATA
668 002406' 006201 ASH R1 ;MOVE THE DATA
669 002410' 006201 ASH R1 ;MOVE THE DATA
670 002412' 006201 ASH R1 ;MOVE THE DATA
671 002414' 010112 MOV R1,(R2) ;RELOAD THE NEW VALUE
672 002416' 005720 TST (R0)+ ;HUMP THE POINTER
673 002420' 000765 BR 1S ;FOR AND DO ANOTHER TABLE ENTRY
674
675 002422' 000322' TABLE: MBUFF0 ;EA BITS FOR EACH BUFFER ADDRESS
676 002424' 000330' MBUFF1
677 002426' 000336' MBUFF2
678 002430' 000344' MBUFF3
679 002432' 000432' EJUB00 ;EA BITS FOR EACH JOB "JOB"
680 002434' 000440' EJUB10
681 002436' 000446' EJUB20
682 002440' 000454' EJUB30
683 002442' 000462' EJUB40
684 002444' 000470' EJUB50
685 002446' 000476' EJUB60
686 002450' 000504' EJUB70
687 002452' 000000' U
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688
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695
696 002454' 012767 177600 003710 TST4A: MOV #177600,K*TPRE ;SET AN INITIAL CLOCK RATE PRESET.
697 002462' 012700 005240' MOV #JOB0,R0 ;PRIME THE POINTER TO BUFFER
698 002466' 005020 18: CLR (R0)+ ;CLEAR JOB ENTRY
699 002470' 020027 006200' CMP R0,#WAHNMMSG ;TEST IF DONE CLEARING
700 002474' 001374 BNE JS ;BR IF NOT
701 002476' 005067 175604 CLR $GDDAT ;CLEAR "# OF JOBS STARTED" COUNTER
702
703
704 ;DETERMINE THE TYPE OF "JOBS" AND LOAD THE "DATA TRANSFER START RUA FORMAT"
705
706 ;DETERMINE IF JOB 0 (FIRST A TO D) IS TO BE RUN
707 002502' 032767 012001 175306 DJOB0: BIT #B110:B1110:BIT12,SR1 ;ANY 1ST A/D'S?
708 002510' 001510 BEQ DJOB1 ;NO-EXIT 1ST A/D'S.
709 002512' 012700 005240' MOV #JOB0,R0 ;UPDATE JOB POINTER
710 002516' 012710 000012 MOV #12,(R0) ;MODE WORD-START A/D.
711 002522' 052710 000400 BIS #BIT8,(R0) ;SINGLE CHAN.
712 002526' 012760 000512 000002 MOV #512,2(R0) ;TAKE 512 SAMPLES
713 002534' 005067 002572 CLR JOB0U ;CLEAR USW
714 002540' 016760 175664 000004 MOV GJOB0U,4(R0) ;LOAD PA OF USW
715 002546' 012760 000202 000007 MOVH #BIT7:BIT11,7(U) ;BUFFER OVERRUN NOT FATAL, THREE BUFFERS.
716 002554' 016760 175540 000010 MOV GBUFF0,10(R0) ;SET BUFFER ADDR.
717 002562' 016760 175532 000014 MOV GBUFF0,14(R0)
718 002570' 016760 175524 000020 MOV GBUFF0,20(R0)
719 002576' 012760 000010 000054 MOV #10,54(R0) ;SET DELAY BEFORE START COUNT.
720 002604' 012760 000400 000056 MOV #400,56(R0) ;START CH0, INC=1
721 002612' 012760 000016 000060 MOV #16,60(R0) ;SAMPLE 14 CHANNELS
722 002620' 012760 000200 000062 MOV #200,62(R0) ;SET DWELL VALUE
723
724 002626' 016760 175600 000006 MOVH EJUR0U,6(R0) ;NOW WE MUST SET ALL "EA" BITS
725 002634' 016760 175462 000012 MOV EBUFF0,12(R0)
726 002642' 016760 175454 000016 MOV EBUFF0,16(R0)
727 002650' 016760 175446 000022 MOV EBUFF0,22(R0)
728 002656' 005267 175424 INC $GDDAT ;UPDATE "# OF JOBS STARTED" COUNTER
729
730 ;DETERMINE IF OPERATOR SELECTED "DEDICATED" FIRMWARE
731
732 002662' 026727 175412 000104 CMPB UC00E,#'D ;DEDICATED MICRO CODE SELECTED ?
733 002670' 001020 BNE DJOB1 ;BR IF NOT (MUST BE MULTI USER)
734
735 002672' 042710 000010 BIC #BIT3,(R0) ;CLEAR MULTIUSER BIT.
736 002676' 032767 000020 175112 BIT #BIT4,SR1 ;SECOND A/D?
737 002704' 001402 BEQ ZS ;NO CONTINUE.
738 002706' 012710 004442 MOV #4442,(R0) ;YES=MAKE OPERATE IN DUAL A/D MODE.
739 002712' 012760 000150 000002 2S: MOV #1520,2(R0) ;LARGER BUFFER.
740 002720' 016767 175302 003444 MOV SR2VAL,K*TPRE ;SET A HIGHER CLOCK RATE.
741 ;WE WILL TAKE SAMPLES AT (SR2) RATE
742 002726' 000167 001416 JMP ELOOP ;BYPASS OTHER JOB SETUP
743
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744
745
746 ;DETERMINE IF JOB 1 (SECOND A/D (MULTI USER MICRO-CODE SELECTED)) IS TO BE RUN
747 002732' 032767 000020 175056 DJOB1: BIT #BIT4,SR1 ;SECOND A/D?
748 002740' 001502 BEQ DJOB2 ;BR IF NO SECOND A/D
749 002742' 012700 005334' MOV #JOB1,R0 ;UPDATE JOB STORAGE AND POINTER.
750 002746' 012710 000012 MOV #12,(R0) ;MODE WORD-START A/D.
751 002752' 052710 000400 BIS #BIT8,(R0) ;SINGLE CHANNEL
752 002756' 012760 000512 000002 MOV #512,2(R0) ;TAKE 512 SAMPLES
753
754 002764' 005067 002436 CLR JOB1U ;CLEAR USW
755 002770' 016760 175442 000004 MOV GJOB1U,4(R0) ;LOAD THE USW PA
756 002776' 012760 000202 000007 MOVH #BIT7:BIT11,7(U) ;BUFFER OVERRUN NOT FATAL, ONE BUFFER.
757 003004' 016760 175316 000010 MOV GBUFF1,10(R0) ;SET BUFFER ADDR.
758 003012' 016760 175310 000014 MOV GBUFF1,14(R0)
759 003020' 016760 175302 000020 MOV GBUFF1,20(R0)
760 003026' 012760 000010 000054 MOV #10,54(R0) ;SET DELAY BEFORE START.
761 003034' 012760 000500 000056 MOV #500,56(R0) ;START CHANNEL = 64, INC = 1
762 003042' 012760 000016 000060 MOV #16,60(R0) ;SAMPLE 14 CHANNELS.
763 003050' 012760 000200 000062 MOV #200,62(R0) ;LOAD THE DWELL VALUE
764 003056' 005267 175224 INC $GDDAT ;UPDATE "# OF JOBS STARTED COUNTER"
765 ;NOW LOAD THE "EA" BITS FOR THE BUFFER ADDRESSES
766 003062' 016760 175352 000006 MOVH EJUB1U,6(R0)
767 003070' 016760 175234 000012 MOV EBUFF1,12(R0)
768 003076' 016760 175226 000016 MOV EBUFF1,16(R0)
769 003104' 016760 175220 000022 MOV EBUFF1,22(R0)
770
771 ;DETERMINE IF OPERATOR SELECTED "DEDICATED" FIRMWARE
772
773 003112' 026727 175162 000104 CMPB UC00E,#'D ;DEDICATED MICRO CODE SELECTED ?
774 003120' 001012 BNE DJOB2 ;BR IF NOT (MUST BE MULTI USER)
775
776 003122' 042710 000010 BIC #BIT3,(R0) ;CLEAR MULTI USER BIT
777 003126' 012760 000150 000002 MOV #1520,2(R0) ;ENABLE A LARGER BUFFER
778 003134' 016767 175066 003230 MOV SR2VAL,K*TPRE ;SET THE SELECTED CLOCK RATE
779 003142' 000167 001202 JMP ELOOP ;BYPASS OTHER JOB SETUP
780
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781                                     ;DETERMINE IF JOB 2 (D TO A) IS TO BE RUN
782
783 003146' 032767 042010 174642 DJOB2: HIT #B17:B1110:B1114,SR1 ;D/A OUTPUT SELECTED?
784 003154' 001457 BEQ B4S ;NO-NEXT SET-UP
785 003156' 012700 005430' MOV #JOB2,R0 ;UPDATE JOB STORAGE AREA POINTER
786 003162' 012710 001212 MOV #B11:B173:B117:H179,(R0) ;START, D/A, SEW. CHAN.
787 003166' 012760 000256 000002 MOV #256,2(R0) ;SET XFERR COUNT
788
789 003174' 005067 002322 CLR JOB2U ;CLEAR USW
790 003200' 016760 175240 MOV GJOB2U,4(R0) ;LOAD THE USW PA INTO THE RDA
791 003206' 112760 000202 000007 MOVH #B17:B111,7(R0) ;3 BUFFERS.
792 003214' 016760 175127 000010 MOV GBUFF3,10(R0) ;SET BUFFER ADDR.
793 003222' 016760 175114 000014 MOV GBUFF3,14(R0)
794 003230' 016760 175106 000020 MOV GBUFF3,20(R0)
795 003236' 012760 000100 000054 MOV #100,54(R0) ;DELAY BEFORE START COUNTER.
796 003244' 012760 000001 000060 MOV #1,60(R0)
797 003252' 012760 000040 000062 MOV #40,62(R0) ;LOAD DWELL VALUE
798 003260' 005267 175072 INC SDDAT ;UPDATE "# OF JOBS STARTED" COUNTER
799
800 003264' 116760 175156 000006 ;NOW LOAD THE "EA" BITS FOR THE BUFFER ADDRESSES
801 003272' 016760 175046 000012 MOVH EJOB2U,6(R0) ;SET EA BITS
802 003300' 016760 175040 000016 MOV EBUFF3,12(R0)
803 003306' 016760 175032 000022 MOV EBUFF3,16(R0)
804
805 003314'                                     ;DETERMINE IF JOB 3 (DR11K #1 OR LPSDR) IS TO BE RUN
806 DJOB3:
807 003314' 032767 100004 174474 BIT #B12:B115,SR1 ;THIS DR11K #1 OR LPS I/O SEL?
808 003322' 001462 BEQ B4S
809 003324' 012700 005524' MOV #JOB3,R0 ;UPDAT JOB STORAGE AREA POINTER.
810 003330' 012710 000432 MOV #432,(R0) ;SET I/O START, MULTI-USER, DIGITAL INPUT, SINGLE CHAN.
811 003334' 012760 000256 000002 MOV #256,2(R0) ;WORD COUNT
812
813 003342' 005067 002250 CLR JOB3U ;CLEAR USW
814 003346' 016760 175100 000004 MOV GJOB3U,4(R0) ;LOAD PA OF USW INTO RDA
815 003354' 112760 000202 000007 MOVH #B17:B111,7(R0) ;3 BUFFERS.
816 003362' 016760 174746 000010 MOV GBUFF2,10(R0) ;SET BUFFER ADDR.
817 003370' 016760 174740 000014 MOV GBUFF2,14(R0)
818 003376' 016760 174732 000020 MOV GBUFF2,20(R0)
819 003404' 005267 174676 INC SDDAT ;UPDATE "# OF JOBS STARTED" COUNTER
820 003410' 012760 000100 000054 MOV #100,54(R0) ;DELAY BEFORE START.
821 003416' 012760 000009 000056 MOV #1-1,56(R0) ;SELECT CHAN #.
822 003424' 012760 000001 000060 MOV #1,60(R0) ;SAMPLING ONLY ONE CHAN.
823 003432' 012760 000040 000062 MOV #40,62(R0) ;SET THE DWELL VALUE
824
825 003440' 116760 175010 000006 ;NOW LOAD THE "EA" BITS FOR THE ADDRESSES
826 003446' 016760 174664 000012 MOVH EJOB3U,6(R0)
827 003454' 016760 174656 000016 MOV EBUFF2,12(R0)
828 003462' 016760 174650 000022 MOV EBUFF2,16(R0)
829 003470' 016760 174650 000022 MOV EBUFF2,22(R0)
64S:

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830                                     ;DETERMINE IF JOB 4 (DR11K #2) IS TO BE RUN
831 003470'                                     DJOB4:
832
833 003470' 032767 000040 174320 HIT #B15,SR1 ;THIS DR11K #2 SELECTED?
834 003476' 001462 BEQ B4S
835 003500' 012700 005626' MOV #JOB4,R0 ;UPDAT JOB STORAGE AREA POINTER.
836 003504' 012710 000432 MOV #432,(R0) ;SET I/O START, MULTI-USER, DIGITAL INPUT, SINGLE CHAN.
837 003510' 012760 000256 000002 MOV #256,2(R0) ;WORD COUNT
838
839 003516' 005067 002170 CLR JOB4U ;CLEAR USW
840 003522' 016760 174732 000004 MOV GJOB4U,4(R0) ;LOAD PA OF USW INTO RDA
841 003530' 112760 000202 000007 MOVH #B17:B111,7(R0) ;3 BUFFERS.
842 003536' 016760 174572 000010 MOV GBUFF2,10(R0) ;SET BUFFER ADDR.
843 003544' 016760 174564 000014 MOV GBUFF2,14(R0)
844 003552' 016760 174556 000020 MOV GBUFF2,20(R0)
845 003560' 005267 174522 INC SDDAT ;UPDATE "# OF JOBS STARTED" COUNTER
846 003564' 012760 000100 000054 MOV #100,54(R0) ;DELAY BEFORE START.
847 003572' 012760 000001 000056 MOV #2-1,56(R0) ;SELECT CHAN #.
848 003600' 012760 000001 000060 MOV #1,60(R0) ;SAMPLING ONLY ONE CHAN.
849 003606' 012760 000040 000062 MOV #40,62(R0) ;SET THE DWELL VALUE
850
851 003614' 116760 174642 000006 ;NOW LOAD THE "EA" BITS FOR THE ADDRESSES
852 003622' 016760 174510 000012 MOVH EJOB4U,6(R0)
853 003630' 016760 174502 000016 MOV EBUFF2,12(R0)
854 003636' 016760 174474 000022 MOV EBUFF2,16(R0)
855 003644' 016760 174474 000022 MOV EBUFF2,22(R0)
64S:
856
857 003644'                                     ;DETERMINE IF JOB 5 (DR11K #3) IS TO BE RUN
858 DJOB5:
859 003644' 032767 000200 174144 HIT #B17,SR1 ;THIS DR11K #3 SELECTED?
860 003652' 001462 BEQ B4S
861 003654' 012700 005714' MOV #JOB5,R0 ;UPDAT JOB STORAGE AREA POINTER.
862 003660' 012710 000432 MOV #432,(R0) ;SET I/O START, MULTI-USER, DIGITAL INPUT, SINGLE CHAN.
863 003664' 012760 000256 000002 MOV #256,2(R0) ;WORD COUNT
864
865 003672' 005067 002110 CLR JOB5U ;CLEAR USW
866 003676' 016760 174564 000004 MOV GJOB5U,4(R0) ;LOAD PA OF USW INTO RDA
867 003704' 112760 000202 000007 MOVH #B17:B111,7(R0) ;3 BUFFERS.
868 003712' 016760 174416 000010 MOV GBUFF2,10(R0) ;SET BUFFER ADDR.
869 003720' 016760 174410 000014 MOV GBUFF2,14(R0)
870 003726' 016760 174402 000020 MOV GBUFF2,20(R0)
871 003734' 005267 174346 INC SDDAT ;UPDATE "# OF JOBS STARTED" COUNTER
872 003740' 012760 000100 000054 MOV #100,54(R0) ;DELAY BEFORE START.
873 003746' 012760 000002 000056 MOV #3-1,56(R0) ;SELECT CHAN #.
874 003754' 012760 000001 000060 MOV #1,60(R0) ;SAMPLING ONLY ONE CHAN.
875 003762' 012760 000040 000062 MOV #40,62(R0) ;SET THE DWELL VALUE
876
877 003770' 116760 174474 000006 ;NOW LOAD THE "EA" BITS FOR THE ADDRESSES
878 003776' 016760 174334 000012 MOVH EJOB5U,6(R0)
879 004004' 016760 174326 000016 MOV EBUFF2,12(R0)
880 004012' 016760 174320 000022 MOV EBUFF2,16(R0)
881 004020' 016760 174320 000022 MOV EBUFF2,22(R0)
64S:

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882                                     ;DETERMINE IF JOB 6 (DR11K #4) IS TO BE RUN
883 DJOB6:
884
885 004020' 032767 000400 173770      BIT    #R11H,SR1    ;//THIS DR11K #4 SELECTED?
886 004026' 001462                      BEQ     64S
887 004030' 012700 006010'           MOV     #J0H6,R0    ;//UPDAT JOB STORAGE AREA POINTER.
888 004034' 012710 000432           MOV     #432,(R0)    ;//SET I/O START, MULTI-USER, DIGITAL INPUT, SINGLE CHAN.
889 004040' 012760 000256 000002     MOV     #256,2(R0)    ;//WORD COUNT
890
891 004046' 005067 002030             CLR     J0H6U      ;//CLEAR USW
892 004052' 016760 174416 000004     MOV     GJ0H6U,4(R0) ;//LOAD PA OF USW INTO RDA
893 004060' 112760 000202 000007     MOV     #R17:R17,7(R0) ;//3 BUFFERS.
894 004066' 016760 174242 000010     MOV     GBUFF2,10(R0) ;//SET BUFFER ADDR.
895 004074' 016760 174234 000014     MOV     GBUFF2,14(R0)
896 004102' 016760 174226 000020     MOV     GBUFF2,20(R0)
897 004110' 005267 174172             INC     $GDDAT      ;//UPDATE "# OF JOBS STARTED" COUNTER
898 004114' 012760 000100 000054     MOV     #100,54(R0) ;//DELAY BEFORE START.
899 004122' 012760 000003 000056     MOV     #4-1,56(R0) ;//SELECT CHAN #.
900 004130' 012760 000001 000060     MOV     #1,60(R0)   ;//SAMPLING ONLY ONE CHAN.
901 004136' 012760 000040 000062     MOV     #40,62(R0)   ;//SET THE DWELL VALUE
902
903                                     ;NOW LOAD THE "EA" BITS FOR THE ADDRESSES
904 004144' 116760 174326 000006     MOV     GJ0H6U,6(R0)
905 004152' 016760 174160 000012     MOV     EBUFF2,12(R0)
906 004160' 016760 174152 000016     MOV     EBUFF2,16(R0)
907 004174' 016760 174144 000022     MOV     EBUFF2,22(R0)
908
909                                     64S:
910 ;DETERMINE IF JOB 7 (DR11K #5) IS TO BE RUN
911 DJOB7:
912
913 004174' 032767 001000 173614      BIT     #R19,SR1    ;//THIS DR11K #5 SELECTED?
914 004202' 001462                      BEQ     64S
915 004204' 012760 006104'           MOV     #J0H7,R0    ;//UPDAT JOB STORAGE AREA POINTER.
916 004210' 012710 000432           MOV     #432,(R0)    ;//SET I/O START, MULTI-USER, DIGITAL INPUT, SINGLE CHAN.
917 004214' 012760 000256 000002     MOV     #256,2(R0)    ;//WORD COUNT
918
919 004222' 005067 001750             CLR     J0H7U      ;//CLEAR USW
920 004226' 016760 174250 000004     MOV     GJ0H7U,4(R0) ;//LOAD PA OF USW INTO RDA
921 004234' 112760 000202 000007     MOV     #R17:R17,7(R0) ;//3 BUFFERS.
922 004242' 016760 174066 000010     MOV     GBUFF2,10(R0) ;//SET BUFFER ADDR.
923 004250' 016760 174060 000014     MOV     GBUFF2,14(R0)
924 004256' 016760 174052 000020     MOV     GBUFF2,20(R0)
925 004264' 005267 174016             INC     $GDDAT      ;//UPDATE "# OF JOBS STARTED" COUNTER
926 004270' 012760 000100 000054     MOV     #100,54(R0) ;//DELAY BEFORE START.
927 004276' 012760 000004 000056     MOV     #5-1,56(R0) ;//SELECT CHAN #.
928 004304' 012760 000001 000060     MOV     #1,60(R0)   ;//SAMPLING ONLY ONE CHAN.
929 004312' 012760 000040 000062     MOV     #40,62(R0)   ;//SET THE DWELL VALUE
930
931                                     ;NOW LOAD THE "EA" BITS FOR THE ADDRESSES
932 004320' 116760 174160 000006     MOV     GJ0H7U,6(R0)
933 004326' 016760 174004 000012     MOV     EBUFF2,12(R0)
934 004334' 016760 173776 000016     MOV     EBUFF2,16(R0)
935 004342' 016760 173770 000022     MOV     EBUFF2,22(R0)
936
937                                     64S:

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934                                     ;NOW THAT THE "JOBS" HAVE BEEN BUILT,
935 ; BUILD THE "TO DO" QUEUE
936
937                                     ;*
938                                     ;* J0H0-J0H7 PRE SET UP
939                                     ;* $GDDAT= NO. OF JOBS TO EXERCISE
940                                     ;*
941
942 004350' 012700 007060'           FLOOP: MOV     #J0H0,R0
943 004354' 005020                      CLP     (R0)+      ;//CLEAR "TO DO" QUEUE AREA.
944 004356' 020027 007324'           1S:  CMP     R0,#D0WEC
945 004362' 001374                      BNE     1S
946 004364' 012701 007060'           MOV     #J0H0,R1      ;//GET QUES AREA
947 004370' 012762 005240'           MOV     #J0H0,R2      ;//PICK UP 1ST JOB ADDRESS
948 004374' 012703 005334'           MOV     #J0H1,R3      ;//CALCULATE ADDR DIF.
949 004400' 162703 005240'           SUB     #J0H0,R3      ;// BETWEEN THE JOBS
950 004404' 016767 173676 002566     MOV     $GDDAT,J0H0C ;//REMEMBER HOW MANY JOBS ARE QUEUED
951 004412' 012760 000010             MOV     #8,R0        ;//LOAD MAX JOB COUNT
952 004416' 005712                      2S:  FSI     (R2)    ;//ANYTHING IN THIS JOB??
953 004420' 001401                      BEQ     3S
954 004422' 010221                      MOV     R2,(R1)+    ;//STORE JOB ADDRESS INTO "TO DO" QUEUE
955 004424' 060302                      ADD     #1,R2      ;//POINT TO NEXT JOB
956 004426' 005300                      DEC     R0         ;//DONE ALL JOBS?
957 004430' 001372                      BNE     2S          ;//NO-LOOP.
958
959                                     ;NOW LOAD THE CLOCK START RDA
960 004432' 012701 006366'           MOV     #RAT,R1      ;//SET UP TO GET PA
961 004436' 010167 173646             MOV     R1,GETADR    ;//MOV ADRS SO WE CAN GET THE PA
962 004442' 104415 000009' 000310'   GETPAS,BEGIN, GETADR ;//GET PHYSICAL ADDRESS FROM 16-BIT GETADR
963 004450' 016701 173636             MOV     GETPAS,R1    ;//RETURN PHYSICAL ADRS.
964 004454' 010177 173560             MOV     R1,#LPAUL    ;//START CLOCK
965
966 004460' 006267 173630             ASR     GETEAB      ;//MOVE EA BITS
967 004464' 006267 173624             ASR     GETEAB      ;//OVER TO BIT 2-3
968 004470' 042777 000014 173532     BIC     #14,@LPC1    ;//REMOVE OLD "EA" BITS
969 004476' 056777 173612 173524     HIS     GETEAB,@LPC1 ;//SET NEW "EA" BITS.
970 004504' 005067 173574             CLR     $BDDAT      ;//CLEAR "JOB STARTED" COUNTER
971 004510' 052777 000001 173512     BIS     #R110,@LPC1 ;//ENABLE THE LPA-11 TO START
972
973                                     ;NOW PRIME THE LPA-11 INTERRUPT VECTORS
974 004516' 016700 173266             MOV     VECTOR,R0
975 004522' 012720 004704'           MOV     #OUPSRV,(R0)+
976 004526' 016720 173260             MOV     #R1,(R0)+
977 004532' 012720 004562'           MOV     #INPSRV,(R0)+
978 004536' 016720 173250             MOV     #R1,(R0)+
979 004542' 052777 000100 173460     BIS     #R16,@LPC1 ;//ENABLE LPA-11 "INPUT" INTERRUPTS
980 004550' 052777 000100 173456     BIS     #R16,@LPC1 ;//ENABLE LPA-11 "OUTPUT" INTERRUPTS
981 004556' 104400 000000'           EXITS,BEGIN ;//EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

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979      ;RETURN HERE UPON AN "INPUT" INTERRUPT FROM THE LPA-11
980      INPSRV: TST      IODUC      ;FINISHED ALL "TO DO" JOB REQUESTS
981      004562' 005767 002412      BEQ      IEA      ;IF DONE
982      ;-----
983      004570' 000004 000000' 004576' PIRUS,BEGIN,1S      ; QUEUE UP TO CONTINUE AT 1S AND RTI
984      ;-----
985      004576' 012700 007000' 1S:    MOV      #IODUC,R0      ;LOAD POINTER TO "TO DO" QUEUE
986      004602' 005720      TST      (R0)+      ;FIND AN ENTRY
987      004604' 001776      BEQ      ZS      ;
988      004606' 014067 173476      MOV      -(R0),GETADR      ;SET TO GET PA
989      004612' 104415 000000' 000310' GETPAS,BEGIN, GETADR      ;GET PHYSICAL ADDRESS FROM 16-BIT GETADR
990      004620' 016777 173466 173412      MOV      GETPAR,RLPADL      ;LOAD RDA OF THIS JOB INTO LPA11
991      004626' 006267 173467      ASK      GETEAB
992      004632' 006267 173456      ASK      GETEAB
993      004636' 042777 000014 173364      BIC      #14,RLPCI      ;REMOVE OLD "EA" BITS
994      004644' 056777 173444 173356      BIS      GETEAB,RLPCI      ;SET EXTION BITS
995      004652' 005010      CLR      (R0)      ;CLEAR THIS JOB OUT OF THE QUEUE
996      004654' 005367 002320      DEC      IODUC      ;REDUCE "TO DO" JOB COUNTER
997      004660' 001003      BNE      ZS      ;IF MORE REMAIN
998      004662' 042777 000100 173340      BIC      #BIT6,RLPCI      ;DON'T ALLOW ANOTHER INTERRUPT.
999      004670' 052777 000001 173332 3S:  HIS      #BIT0,RLPCI      ;SET GO!
1000     004676' 104400 000000'      EXITS,BEGIN      ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
1001     004702' 000002      IEX:      RTI      ;EXIT IF UNEXPECTED "INPUT" INTERRUPT
1002
1003     ;RETURN HERE UPON AN "OUTPUT" INTERRUPT FROM THE LPA-11
1004     OUPSRV:
1005     ;-----
1006     004704' 000004 000000' 004712' PIRUS,BEGIN,1S      ; QUEUE UP TO CONTINUE AT 1S AND RTI
1007     ;-----
1008     004712' 017700 173316 1S:    MOV      #LPCU,R0      ;NOW LETS GET USER NUMBER.
1009     004716' 042700 177770      BIC      #C<7>,R0      ;RID OF REST OF JUNK.
1010     004722' 006300      ASL      R0      ;MAKE A WORD INDEX
1011     004724' 017701 173310      MOV      #LPADL,R1      ;READ THE "NOA" ADDRESS FROM THE LPA
1012     004730' 105777 173302      TSTH      #LPSH      ;SEE IF BIT15 SET SHOWING ERROR
1013     004734' 106032      BPL      NEM      ;IF NO ERROR, BR AND PROCEED.
1014     004736' 122777 000250 173272      C-PR      #ZS,RLPSU      ;JOB DONE <REQUEST TERMINATED> ERROR?
1015     004744' 001014      BNE      ZS      ;NO-OK AND REPORT ERROR
1016     004746' 005267 173332      INC      $HODAT      ;UPDATE COUNTER OF "JOB DONE" ERRORS
1017     004752' 026767 173330 173324      CMP      $HODAT,$HODAT      ;COMPARE COUNT OF JOB DONE <REQUEST TERMINATED>
1018     ; TO THE NUMBER OF JOBS STARTED
1019     004760' 001122      BNE      OUICON      ;IF NOT EQUAL
1020     004762' 004767 001406      JSR      PC,RESET0      ;STOP THE LPA-11K
1021     004766' 104413 000000'      ENDDITS,BEGIN      ;SIGNAL END OF ITERATION.
1022     ;MONITOR SHALL TEST END OF PASS
1023     004772' 000167 173510      JMP      START      ;RETURN TO TESTING
1024     ;BR HERE UPON UNEXPECTED ERROR FLAG CONDITIONS
1025     ZS:
1026     004776' 004767 001372      JSR      PC,RESET0      ;GO AND READ LPA-11 REGISTERS
1027     005002' 012767 000000 173076      MOV      #0,ERRTYP      ;LOAD "NOT DEFINED" ERROR CODE
1028     ;*****
1029     005010' 104405 000000' 000000      HRDEPS,BEGIN,NULL      ;LPA-11 MICRO CODE REPORTED AN ERROR
1030     ;*****
1031     005016' 000167 173464      JMP      START      ;TRY STARTING AGAIN

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1032     ;COME HERE IF "OUT" INTERRUPT "ERROR" WAS NOT SET
1033     NEM:      BNE      ZS      ;IF STATUS IS 1 OR 2 OK. IF =0 THEN
1034     005024' 017712 173210      MOV      #LPADL,R2      ;GET THE PA OF THE RDA FROM THE LPA
1035     005030' 012703 000350' 1S:    MOV      #GJDBU,R3      ;AND GO THRU THE TABLES TO FIND THE VA
1036     005034' 020213      CMP      #2,(R3)      ;DOES THE PA MATCH ?
1037     005036' 001417      BEQ      ZS      ;YES GET THE VA FROM THE TABLE
1038     005040' 062703 000006      ADD      #6,R3      ;NO-UPDATE POINTER AND TRY AGAIN
1039     005044' 022703 000436'      CMP      #GJDBU,R3      ;TEST IF AT END OF LIST
1040     005050' 001371      BNE      ZS      ;IF NOT
1041     005052' 004767 001316      JSR      PC,RESET0      ;GO AND READ LPA-11 REGISTERS
1042     005056' 012767 000000 173022      MOV      #0,ERRTYP      ;LOAD "NOT DEFINED" ERROR CODE
1043     ;*****
1044     005064' 104405 000000' 000000      HRDEPS,BEGIN,NULL      ;LPA-11 RETURNED INCORRECT RDA ADDRESS
1045     ;*****
1046     005072' 000167 173410      JMP      START      ;TRY STARTING AGAIN
1047     005076' 016360 177776 007202' 2S:  MOV      -2(R3),TABLE(R0)      ;STORE THE VA OF THE RDA IN A TABLE
1048     ; INDEXING OFF THE UID CODE RETURNED BY THE LPA MICRO-CODE.
1049
1050     ;BR TO HERE UPON CODE 1 (BUFFER FULL/EMPTY) OR 2 (NONFATAL BUFFER OVERRUN)
1051     ;FALL THROUGH IF CODE 0
1052
1053     005104' 016001 007202' 3S:    MOV      TABLE(R0),R1      ;REFETCH RDA VA ADDR
1054     005110' 016103 000004      MOV      #4(R1),R3      ;GET THE PA OF THE USW
1055     005114' 012704 000430'      MOV      #GJDBU,R4      ;SET TO FIND PA IN TABLE
1056     005120' 020314      CMP      #1,(R4)      ;DOES THE PA MATCH
1057     005122' 001420      BEQ      ZS      ;YES-OK AND GET VA FROM TABLE
1058     005124' 062704 000006      ADD      #6,R4      ;NO-TRY NEXT ENTRY
1059     005130' 022704 000510'      CMP      #GJDBU+6,R4      ;TEST IF END OF BUFFER
1060     005134' 001371      BNE      ZS      ;IF NOT
1061     005136' 000240      NOP
1062     005140' 004767 001230      JSR      PC,RESET0      ;GO AND READ LPA-11 REGISTERS
1063     005144' 012767 000000 172734      MOV      #0,ERRTYP      ;LOAD "NOT DEFINED" ERROR CODE
1064     ;*****
1065     005152' 104405 000000' 000000      HRDEPS,BEGIN,NULL      ;LPA RETURNED INCORRECT USER NUMBER
1066     ;*****
1067     005160' 000167 173322      JMP      START      ;TRY STARTING AGAIN
1068
1069     ;COME HERE UPON FINDING AN ADDRESS MATCH
1070
1071     005164' 016403 177776 5S:    MOV      -2(R4),R3      ;GET VA OF THE USW
1072     005170' 116302 000001      MOVH      1(R3),R2      ;GET THE LAST BUFFER USED
1073     005174' 005202      INC      R2      ;UPDATE FOR NEXT BUFFER
1074     005176' 042702 177770      BIC      #177770,R2      ;LEAVE LSD
1075     005202' 116104 000007      MOVH      1(R1),R4      ;GET LAST BUFFER MASK.
1076     005206' 042704 177770      BIC      #177770,R4      ;LEAVE LSD
1077     005212' 110263 000001      MOVH      #2,1(R3)      ;UPDATE THE USER STATUS WORK
1078     005216' 120204      C-PR      #2,R4      ;LAST BUFFER ?
1079     005220' 003402      BLE      OUICON      ;NO-BRANCH
1080     005222' 052713 040000      BIS      #40000,(R3)      ;SET STOP JOB HIT
1081     005226' 042777 000200 173000      BIC      #BIT7,RLPCU      ;CLEAR "READY OUT" - WHICH REENABLES THE LPA-11
1082
1083     005234'      OUPX:
1084     005234' 104400 000000'      EXITS,BEGIN      ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
1085

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1086      *** REQUEST DESCRIPTOR ARRAY FOR JOB #0
1087      ***
1088      *** THIS TABLE WILL BE ALTERED BY THIS PROGRAM
1089      *** LOCATION JOB00 WILL BE ALTERED BY THE LPA-11
1090
1091 005240' 000000      JOB0: .WORD 0      //MODE INFORMATION.
1092 005242' 000000      .WORD 0      //WORD 2 //WORD COUNT
1093 005244' 005332'    .WORD 0+JOB00 .WORD 4 //USW (USER STATUS WORD)
1094 005246' 000      .BYTE 0      //BYTE 6 //USW (EXTENDED ADDR. BITS)
1095 005247' 000      .BYTE 0      //BYTE 7 //VMM (VALID BUFFER MASK)
1096 005250' 000000      .WORD 0      //WORD10 //BUFFER ADDRESS #0
1097 005252' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1098 005253' 000      .BYTE 0      //UNUSED
1099 005254' 000000      .WORD 0      //WORD14 //BUFFER ADDRESS #1
1100 005256' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1101 005257' 000      .BYTE 0      //UNUSED
1102 005260' 000000      .WORD 0      //WORD20 //BUFFER ADDRESS #2
1103 005262' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1104 005263' 000      .BYTE 0      //UNUSED
1105 005264' 000000      .WORD 0      //WORD24 //BUFFER ADDRESS #3
1106 005266' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1107 005267' 000      .BYTE 0      //UNUSED
1108 005270' 000000      .WORD 0      //WORD30 //BUFFER ADDRESS #4
1109 005272' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1110 005273' 000      .BYTE 0      //UNUSED
1111 005274' 000000      .WORD 0      //WORD34 //BUFFER ADDRESS #5
1112 005276' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1113 005277' 000      .BYTE 0      //UNUSED
1114 005300' 000000      .WORD 0      //WORD40 //BUFFER ADDRESS #6
1115 005302' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1116 005303' 000      .BYTE 0      //UNUSED
1117 005304' 000000      .WORD 0      //WORD44 //BUFFER ADDRESS #7
1118 005306' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1119 005307' 000      .BYTE 0      //UNUSED
1120 005310' 000000      .WORD 0      //WORD50 //RCLR (ADDR OF LIST OF RANDOM CHAN)
1121 005312' 000      .BYTE 0      //RCL EXTENDED ADR. BITS
1122 005313' 000      .BYTE 0      //UNUSED
1123 005314' 000000      .WORD 0      //WORD54 //DELAY WORD (COUNTER AFTER "START COMMAND")
1124 005316' 000      .BYTE 0      //BYTES6 //START CHANNEL NUMBER
1125 005317' 000      .BYTE 0      //BYTES7 //CHANNEL NUMBER INCREMENT
1126 005320' 000000      .WORD 0      //WORD60 //NUMBER OF SAMPLES IN A SAMPLE SEQUENCE
1127 005322' 000000      .WORD 0      //WORD62 //SAMPLE RATE (DWELL VALUE)
1128 005324' 000      .BYTE 0      //STWD
1129 005325' 000      .BYTE 0      //EMWD EVENT MARK DIGITAL INT WD#
1130 005326' 000000      .WORD 0      //ST MSK
1131 005330' 000000      .WORD 0      //EM MSK EVENT MARK DIGITAL INPUT MASK.
1132      END REG. TABLE
1133
1134 005332' 000000      JOB00: .WORD 0      //USW USER STATUS WORD
1135

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1136      *** REQUEST DESCRIPTOR ARRAY FOR JOB #1
1137      ***
1138      *** THIS TABLE WILL BE ALTERED BY THIS PROGRAM
1139      *** LOCATION JOB10 WILL BE ALTERED BY THE LPA-11
1140
1141 005334' 000000      JOB1: .WORD 0      //MODE INFORMATION.
1142 005336' 000000      .WORD 0      //WORD 2 //WORD COUNT
1143 005340' 005426'    .WORD 0+JOB10 .WORD 4 //USW (USER STATUS WORD)
1144 005342' 000      .BYTE 0      //BYTE 6 //USW (EXTENDED ADDR. BITS)
1145 005343' 000      .BYTE 0      //BYTE 7 //VMM (VALID BUFFER MASK)
1146 005344' 000000      .WORD 0      //WORD10 //BUFFER ADDRESS #0
1147 005346' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1148 005347' 000      .BYTE 0      //UNUSED
1149 005350' 000000      .WORD 0      //WORD14 //BUFFER ADDRESS #1
1150 005352' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1151 005353' 000      .BYTE 0      //UNUSED
1152 005354' 000000      .WORD 0      //WORD20 //BUFFER ADDRESS #2
1153 005356' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1154 005357' 000      .BYTE 0      //UNUSED
1155 005360' 000000      .WORD 0      //WORD24 //BUFFER ADDRESS #3
1156 005362' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1157 005363' 000      .BYTE 0      //UNUSED
1158 005364' 000000      .WORD 0      //WORD30 //BUFFER ADDRESS #4
1159 005366' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1160 005367' 000      .BYTE 0      //UNUSED
1161 005370' 000000      .WORD 0      //WORD34 //BUFFER ADDRESS #5
1162 005372' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1163 005373' 000      .BYTE 0      //UNUSED
1164 005374' 000000      .WORD 0      //WORD40 //BUFFER ADDRESS #6
1165 005376' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1166 005377' 000      .BYTE 0      //UNUSED
1167 005400' 000000      .WORD 0      //WORD44 //BUFFER ADDRESS #7
1168 005402' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1169 005403' 000      .BYTE 0      //UNUSED
1170 005404' 000000      .WORD 0      //WORD50 //RCLR (ADDR OF LIST OF RANDOM CHAN)
1171 005406' 000      .BYTE 0      //RCL EXTENDED ADR. BITS
1172 005407' 000      .BYTE 0      //UNUSED
1173 005410' 000000      .WORD 0      //WORD54 //DELAY WORD (COUNTER AFTER "START COMMAND")
1174 005412' 000      .BYTE 0      //BYTES6 //START CHANNEL NUMBER
1175 005413' 000      .BYTE 0      //BYTES7 //CHANNEL NUMBER INCREMENT
1176 005414' 000000      .WORD 0      //WORD60 //NUMBER OF SAMPLES IN A SAMPLE SEQUENCE
1177 005416' 000000      .WORD 0      //WORD62 //SAMPLE RATE (DWELL VALUE)
1178 005420' 000      .BYTE 0      //STWD
1179 005421' 000      .BYTE 0      //EMWD EVENT MARK DIGITAL INT WD#
1180 005422' 000000      .WORD 0      //ST MSK
1181 005424' 000000      .WORD 0      //EM MSK EVENT MARK DIGITAL INPUT MASK.
1182      END REG. TABLE
1183
1184 005426' 000000      JOB10: .WORD 0      //USW USER STATUS WORD
1185

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1186      *** REQUEST DESCRIPTOR ARRAY FOR JOB #2
1187      ***
1188      *** THIS TABLE WILL BE ALTERED BY THIS PROGRAM
1189      *** LOCATION JOB20 WILL BE ALTERED BY THE LPA-11
1190
1191 005430' 000000      JOB2: .WORD 0      //MODE INFORMATION.
1192 005432' 000000      .WORD 0      //WORD 2 //WORD COUNT
1193 005434' 005522'    .WORD 0+JOB20 //WORD 4 //USW (USER STATUS WORD)
1194 005436' 000        .BYTE 0      //BYTE 6 //USW (EXTENDED ADDR. BITS)
1195 005437' 000        .BYTE 0      //BYTE 7 //VBM (VALID BUFFER MASK)
1196 005440' 000000      .WORD 0      //WORD10 //BUFFER ADDRESS #0
1197 005442' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1198 005443' 000        .BYTE 0      //UNUSED
1199 005444' 000000      .WORD 0      //WORD14 //BUFFER ADDRESS #1
1200 005446' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1201 005447' 000        .BYTE 0      //UNUSED
1202 005450' 000000      .WORD 0      //WORD20 //BUFFER ADDRESS #2
1203 005452' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1204 005453' 000        .BYTE 0      //UNUSED
1205 005454' 000000      .WORD 0      //WORD24 //BUFFER ADDRESS #3
1206 005456' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1207 005457' 000        .BYTE 0      //UNUSED
1208 005460' 000000      .WORD 0      //WORD30 //BUFFER ADDRESS #4
1209 005462' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1210 005463' 000        .BYTE 0      //UNUSED
1211 005464' 000000      .WORD 0      //WORD34 //BUFFER ADDRESS #5
1212 005466' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1213 005467' 000        .BYTE 0      //UNUSED
1214 005470' 000000      .WORD 0      //WORD40 //BUFFER ADDRESS #6
1215 005472' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1216 005473' 000        .BYTE 0      //UNUSED
1217 005474' 000000      .WORD 0      //WORD44 //BUFFER ADDRESS #7
1218 005476' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1219 005477' 000        .BYTE 0      //UNUSED
1220 005500' 000000      .WORD 0      //WORD50 //RCLP (ADDR OF LIST OF RANDOM CHAN)
1221 005502' 000        .BYTE 0      //RCL EXTENDED ADDR. BITS
1222 005503' 000        .BYTE 0      //UNUSED
1223 005504' 000000      .WORD 0      //WORD54 //DELAY WORD (COUNTER AFTER "START COMMAND")
1224 005506' 000        .BYTE 0      //BYTE56 //START CHANNEL NUMBER
1225 005507' 000        .BYTE 0      //BYTE57 //CHANNEL NUMBER INCREMENT
1226 005510' 000000      .WORD 0      //WORD60 //NUMBER OF SAMPLES IN A SAMPLE SEQUENCE
1227 005512' 000000      .WORD 0      //WORD62 //SAMPLE RATE (DWEIL VALUE)
1228 005514' 000        .BYTE 0      //STWD
1229 005515' 000        .BYTE 0      //EMWD EVENT MARK DIGITAL INT WDM
1230 005516' 000000      .WORD 0      //ST MSK
1231 005520' 000000      .WORD 0      //EM MSK EVENT MARK DIGITAL INPUT MASK.
1232      END REG. TABLE
1233
1234 005522' 000000      JOB20: .WORD 0      //USW USER STATUS WORD
1235

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1236      *** REQUEST DESCRIPTOR ARRAY FOR JOB #3
1237      ***
1238      *** THIS TABLE WILL BE ALTERED BY THIS PROGRAM
1239      *** LOCATION JOB30 WILL BE ALTERED BY THE LPA-11
1240
1241 005524' 000000      JOB3: .WORD 0      //MODE INFORMATION.
1242 005526' 000000      .WORD 0      //WORD 2 //WORD COUNT
1243 005530' 005616'    .WORD 0+JOB30 //WORD 4 //USW (USER STATUS WORD)
1244 005532' 000        .BYTE 0      //BYTE 6 //USW (EXTENDED ADDR. BITS)
1245 005533' 000        .BYTE 0      //BYTE 7 //VBM (VALID BUFFER MASK)
1246 005534' 000000      .WORD 0      //WORD10 //BUFFER ADDRESS #0
1247 005536' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1248 005537' 000        .BYTE 0      //UNUSED
1249 005540' 000000      .WORD 0      //WORD14 //BUFFER ADDRESS #1
1250 005542' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1251 005543' 000        .BYTE 0      //UNUSED
1252 005544' 000000      .WORD 0      //WORD20 //BUFFER ADDRESS #2
1253 005546' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1254 005547' 000        .BYTE 0      //UNUSED
1255 005550' 000000      .WORD 0      //WORD24 //BUFFER ADDRESS #3
1256 005552' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1257 005553' 000        .BYTE 0      //UNUSED
1258 005554' 000000      .WORD 0      //WORD30 //BUFFER ADDRESS #4
1259 005556' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1260 005557' 000        .BYTE 0      //UNUSED
1261 005560' 000000      .WORD 0      //WORD34 //BUFFER ADDRESS #5
1262 005562' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1263 005563' 000        .BYTE 0      //UNUSED
1264 005564' 000000      .WORD 0      //WORD40 //BUFFER ADDRESS #6
1265 005566' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1266 005567' 000        .BYTE 0      //UNUSED
1267 005570' 000000      .WORD 0      //WORD44 //BUFFER ADDRESS #7
1268 005572' 000        .BYTE 0      //EXTENDED ADDRESS BITS
1269 005573' 000        .BYTE 0      //UNUSED
1270 005574' 000000      .WORD 0      //WORD50 //RCLP (ADDR OF LIST OF RANDOM CHAN)
1271 005576' 000        .BYTE 0      //RCL EXTENDED ADDR. BITS
1272 005577' 000        .BYTE 0      //UNUSED
1273 005600' 000000      .WORD 0      //WORD54 //DELAY WORD (COUNTER AFTER "START COMMAND")
1274 005602' 000        .BYTE 0      //BYTE56 //START CHANNEL NUMBER
1275 005603' 000        .BYTE 0      //BYTE57 //CHANNEL NUMBER INCREMENT
1276 005604' 000000      .WORD 0      //WORD60 //NUMBER OF SAMPLES IN A SAMPLE SEQUENCE
1277 005606' 000000      .WORD 0      //WORD62 //SAMPLE RATE (DWEIL VALUE)
1278 005610' 000        .BYTE 0      //STWD
1279 005611' 000        .BYTE 0      //EMWD EVENT MARK DIGITAL INT WDM
1280 005612' 000000      .WORD 0      //ST MSK
1281 005614' 000000      .WORD 0      //EM MSK EVENT MARK DIGITAL INPUT MASK.
1282      END REG. TABLE
1283
1284 005616' 000000      JOB30: .WORD 0      //USW USER STATUS WORD
1285

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1286      *** REQUEST DESCRIPTOR ARRAY FOR JOB #4
1287      ***
1288      *** THIS TABLE WILL BE ALTERED BY THIS PROGRAM
1289      *** LOCATION JOB4U WILL BE ALTERED BY THE LPA-11
1290
1291 005620' 000000 JOB4: .WORD 0 //MODE INFORMATION.
1292 005622' 000000 .WORD 0 //WORD 2 //WORD COUNT
1293 005624' 005712' .WORD 0+JOB4U //WORD 4 //USW (USER STATUS WORD)
1294 005626' 000 .BYTE 0 //BYTE 6 //USW (EXTENDED ADDR. BITS)
1295 005627' 000 .BYTE 0 //BYTE 7 //VBM (VALID BUFFER MASK)
1296 005630' 000000 .WORD 0 //WORD10 //BUFFER ADDRESS #0
1297 005632' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1298 005633' 000 .BYTE 0 //UNUSED
1299 005634' 000000 .WORD 0 //WORD14 //BUFFER ADDRESS #1
1300 005636' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1301 005637' 000 .BYTE 0 //UNUSED
1302 005640' 000000 .WORD 0 //WORD20 //BUFFER ADDRESS #2
1303 005642' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1304 005643' 000 .BYTE 0 //UNUSED
1305 005644' 000000 .WORD 0 //WORD24 //BUFFER ADDRESS #3
1306 005646' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1307 005647' 000 .BYTE 0 //UNUSED
1308 005650' 000000 .WORD 0 //WORD30 //BUFFER ADDRESS #4
1309 005652' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1310 005653' 000 .BYTE 0 //UNUSED
1311 005654' 000000 .WORD 0 //WORD34 //BUFFER ADDRESS #5
1312 005656' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1313 005657' 000 .BYTE 0 //UNUSED
1314 005660' 000000 .WORD 0 //WORD40 //BUFFER ADDRESS #6
1315 005662' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1316 005663' 000 .BYTE 0 //UNUSED
1317 005664' 000000 .WORD 0 //WORD44 //BUFFER ADDRESS #7
1318 005666' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1319 005667' 000 .BYTE 0 //UNUSED
1320 005670' 000000 .WORD 0 //WORD50 //RCLR (ADDR OF LIST OF RANDOM CHAN)
1321 005672' 000 .BYTE 0 //RCL EXTENDED ADDR. BITS
1322 005673' 000 .BYTE 0 //UNUSED
1323 005674' 000000 .WORD 0 //WORD54 //DELAY WORD (COUNTER AFTER "START COMMAND")
1324 005676' 000 .BYTE 0 //BYTE56 //START CHANNEL NUMBER
1325 005677' 000 .BYTE 0 //BYTE57 //CHANNEL NUMBER INCREMENT
1326 005700' 000000 .WORD 0 //WORD60 //NUMBER OF SAMPLES IN A SAMPLE SEQUENCE
1327 005702' 000000 .WORD 0 //WORD62 //SAMPLE RATE (DWELL VALUE)
1328 005704' 000 .BYTE 0 //STWD
1329 005705' 000 .BYTE 0 //EMWD EVENT MARK DIGITAL INT WD#
1330 005706' 000000 .WORD 0 //SI MSK
1331 005710' 000000 .WORD 0 //EM MSK EVENT MARK DIGITAL INPUT MASK.
1332      END REG. TABLE
1333
1334 005712' 000000 JOB4U: .WORD 0 //USW USER STATUS WORD
1335

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1336      *** REQUEST DESCRIPTOR ARRAY FOR JOB #5
1337      ***
1338      *** THIS TABLE WILL BE ALTERED BY THIS PROGRAM
1339      *** LOCATION JOB5U WILL BE ALTERED BY THE LPA-11
1340
1341 005714' 000000 JOB5: .WORD 0 //MODE INFORMATION.
1342 005716' 000000 .WORD 0 //WORD 2 //WORD COUNT
1343 005720' 006006' .WORD 0+JOB5U //WORD 4 //USW (USER STATUS WORD)
1344 005722' 000 .BYTE 0 //BYTE 6 //USW (EXTENDED ADDR. BITS)
1345 005723' 000 .BYTE 0 //BYTE 7 //VBM (VALID BUFFER MASK)
1346 005724' 000000 .WORD 0 //WORD10 //BUFFER ADDRESS #0
1347 005726' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1348 005727' 000 .BYTE 0 //UNUSED
1349 005730' 000000 .WORD 0 //WORD14 //BUFFER ADDRESS #1
1350 005732' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1351 005733' 000 .BYTE 0 //UNUSED
1352 005734' 000000 .WORD 0 //WORD20 //BUFFER ADDRESS #2
1353 005736' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1354 005737' 000 .BYTE 0 //UNUSED
1355 005740' 000000 .WORD 0 //WORD24 //BUFFER ADDRESS #3
1356 005742' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1357 005743' 000 .BYTE 0 //UNUSED
1358 005744' 000000 .WORD 0 //WORD30 //BUFFER ADDRESS #4
1359 005746' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1360 005747' 000 .BYTE 0 //UNUSED
1361 005750' 000000 .WORD 0 //WORD34 //BUFFER ADDRESS #5
1362 005752' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1363 005753' 000 .BYTE 0 //UNUSED
1364 005754' 000000 .WORD 0 //WORD40 //BUFFER ADDRESS #6
1365 005756' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1366 005757' 000 .BYTE 0 //UNUSED
1367 005760' 000000 .WORD 0 //WORD44 //BUFFER ADDRESS #7
1368 005762' 000 .BYTE 0 //EXTENDED ADDRESS BITS
1369 005763' 000 .BYTE 0 //UNUSED
1370 005764' 000000 .WORD 0 //WORD50 //RCLR (ADDR OF LIST OF RANDOM CHAN)
1371 005766' 000 .BYTE 0 //RCL EXTENDED ADDR. BITS
1372 005767' 000 .BYTE 0 //UNUSED
1373 005770' 000000 .WORD 0 //WORD54 //DELAY WORD (COUNTER AFTER "START COMMAND")
1374 005772' 000 .BYTE 0 //BYTE56 //START CHANNEL NUMBER
1375 005773' 000 .BYTE 0 //BYTE57 //CHANNEL NUMBER INCREMENT
1376 005774' 000000 .WORD 0 //WORD60 //NUMBER OF SAMPLES IN A SAMPLE SEQUENCE
1377 005776' 000000 .WORD 0 //WORD62 //SAMPLE RATE (DWELL VALUE)
1378 006000' 000 .BYTE 0 //STWD
1379 006001' 000 .BYTE 0 //EMWD EVENT MARK DIGITAL INT WD#
1380 006002' 000000 .WORD 0 //SI MSK
1381 006004' 000000 .WORD 0 //EM MSK EVENT MARK DIGITAL INPUT MASK.
1382      END REG. TABLE
1383
1384 006006' 000000 JOB5U: .WORD 0 //USW USER STATUS WORD
1385

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1386      *** REQUEST DESCRIPTOR ARRAY FOR JOB #6
1387      ***
1388      *** THIS TABLE WILL BE ALTERED BY THIS PROGRAM
1389      *** LOCATION JOB60 WILL BE ALTERED BY THE LPA-11
1390
1391 006010' 000000      JOB6: .WORD 0      //MODE INFORMATION.
1392 006012' 000000      .WORD 0      .WORD 2 //WORD COUNT
1393 006014' 006102'      .WORD 0+JOB60 .WORD 4 //USW (USER STATUS WORD)
1394 006016' 000      .BYTE 0      .BYTE 6 //USW (EXTENDED ADDR. BITS)
1395 006017' 000      .BYTE 0      .BYTE 7 //VBM (VALID BUFFER MASK)
1396 006020' 000000      .WORD 0      .WORD10 //BUFFER ADDRESS #0
1397 006022' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1398 006023' 000      .BYTE 0      //UNUSED
1399 006024' 000000      .WORD 0      .WORD14 //BUFFER ADDRESS #1
1400 006026' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1401 006027' 000      .BYTE 0      //UNUSED
1402 006030' 000000      .WORD 0      .WORD20 //BUFFER ADDRESS #2
1403 006032' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1404 006033' 000      .BYTE 0      //UNUSED
1405 006034' 000000      .WORD 0      .WORD24 //BUFFER ADDRESS #3
1406 006036' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1407 006037' 000      .BYTE 0      //UNUSED
1408 006040' 000000      .WORD 0      .WORD30 //BUFFER ADDRESS #4
1409 006042' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1410 006043' 000      .BYTE 0      //UNUSED
1411 006044' 000000      .WORD 0      .WORD34 //BUFFER ADDRESS #5
1412 006046' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1413 006047' 000      .BYTE 0      //UNUSED
1414 006050' 000000      .WORD 0      .WORD40 //BUFFER ADDRESS #6
1415 006052' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1416 006053' 000      .BYTE 0      //UNUSED
1417 006054' 000000      .WORD 0      .WORD44 //BUFFER ADDRESS #7
1418 006056' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1419 006057' 000      .BYTE 0      //UNUSED
1420 006060' 000000      .WORD 0      .WORD50 //RCLR (ADDR OF LIST OF RANDOM CHAN)
1421 006062' 000      .BYTE 0      //RCL EXTENDED ADDR. BITS
1422 006063' 000      .BYTE 0      //UNUSED
1423 006064' 000000      .WORD 0      .WORD54 //DELAY WORD (COUNTER AFTER "START COMMAND")
1424 006066' 000      .BYTE 0      .BYTE56 //START CHANNEL NUMBER
1425 006067' 000      .BYTE 0      .BYTE57 //CHANNEL NUMBER INCREMENT
1426 006070' 000000      .WORD 0      .WORD60 //NUMBER OF SAMPLES IN A SAMPLE SEQUENCE
1427 006072' 000000      .WORD 0      .WORD62 //SAMPLE RATE (DWELL VALUE)
1428 006074' 000      .BYTE 0      //STW
1429 006075' 000      .BYTE 0      //EMW EVENT MARK DIGITAL INT WDA
1430 006076' 000000      .WORD 0      //ST MSK
1431 006100' 000000      .WORD 0      //EM MSK EVENT MARK DIGITAL INPUT MASK.
1432      .END REG. TABLE
1433
1434 006102' 000000      JOB60: .WORD 0      //USW USER STATUS WORD
1435

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1436      *** REQUEST DESCRIPTOR ARRAY FOR JOB #7
1437      ***
1438      *** THIS TABLE WILL BE ALTERED BY THIS PROGRAM
1439      *** LOCATION JOB70 WILL BE ALTERED BY THE LPA-11
1440
1441 006104' 000000      JOB7: .WORD 0      //MODE INFORMATION.
1442 006106' 000000      .WORD 0      .WORD 2 //WORD COUNT
1443 006110' 006176'      .WORD 0+JOB70 .WORD 4 //USW (USER STATUS WORD)
1444 006112' 000      .BYTE 0      .BYTE 6 //USW (EXTENDED ADDR. BITS)
1445 006113' 000      .BYTE 0      .BYTE 7 //VBM (VALID BUFFER MASK)
1446 006114' 000000      .WORD 0      .WORD10 //BUFFER ADDRESS #0
1447 006116' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1448 006117' 000      .BYTE 0      //UNUSED
1449 006120' 000000      .WORD 0      .WORD14 //BUFFER ADDRESS #1
1450 006122' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1451 006123' 000      .BYTE 0      //UNUSED
1452 006124' 000000      .WORD 0      .WORD20 //BUFFER ADDRESS #2
1453 006126' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1454 006127' 000      .BYTE 0      //UNUSED
1455 006130' 000000      .WORD 0      .WORD24 //BUFFER ADDRESS #3
1456 006132' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1457 006133' 000      .BYTE 0      //UNUSED
1458 006144' 000000      .WORD 0      .WORD30 //BUFFER ADDRESS #4
1459 006136' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1460 006137' 000      .BYTE 0      //UNUSED
1461 006140' 000000      .WORD 0      .WORD34 //BUFFER ADDRESS #5
1462 006142' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1463 006143' 000      .BYTE 0      //UNUSED
1464 006144' 000000      .WORD 0      .WORD40 //BUFFER ADDRESS #6
1465 006146' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1466 006147' 000      .BYTE 0      //UNUSED
1467 006150' 000000      .WORD 0      .WORD44 //BUFFER ADDRESS #7
1468 006152' 000      .BYTE 0      //EXTENDED ADDRESS BITS
1469 006153' 000      .BYTE 0      //UNUSED
1470 006154' 000000      .WORD 0      .WORD50 //RCLR (ADDR OF LIST OF RANDOM CHAN)
1471 006156' 000      .BYTE 0      //RCL EXTENDED ADDR. BITS
1472 006157' 000      .BYTE 0      //UNUSED
1473 006160' 000000      .WORD 0      .WORD54 //DELAY WORD (COUNTER AFTER "START COMMAND")
1474 006162' 000      .BYTE 0      .BYTE56 //START CHANNEL NUMBER
1475 006163' 000      .BYTE 0      .BYTE57 //CHANNEL NUMBER INCREMENT
1476 006164' 000000      .WORD 0      .WORD60 //NUMBER OF SAMPLES IN A SAMPLE SEQUENCE
1477 006166' 000000      .WORD 0      .WORD62 //SAMPLE RATE (DWELL VALUE)
1478 006170' 000      .BYTE 0      //STW
1479 006171' 000      .BYTE 0      //EMW EVENT MARK DIGITAL INT WDA
1480 006172' 000000      .WORD 0      //ST MSK
1481 006174' 000000      .WORD 0      //EM MSK EVENT MARK DIGITAL INPUT MASK.
1482      .END REG. TABLE
1483
1484 006176' 000000      JOB70: .WORD 0      //USW USER STATUS WORD
1485

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1486
1487 006200' 006204'          WARNMSG:      WRNMSG
1488 006202' 177777          -1
1489 006204' 006412 020040 020040  WRNMSG: .ASCII <12><15># WARNING: #
1490 006212' 040527 047122 047111
1491 006220' 035107          040
1492 006223' 012 052015 044510          .ASCII <12><15>#THIS MODULE CAN'T RUN UNLESS SRI HAS BEEN SETUP#
1493 006230' 020123 047515 052504
1494 006236' 042514 041440 047101
1495 006244' 052047 051040 047125
1496 006252' 052440 046116 051505
1497 006260' 020123 051123 020061
1498 006266' 040510 020173 042502
1499 006274' 047105 051440 052105
1500 006302' 050125
1501 006304' 006412 047524 051040          .ASCIZ <12><15>#TO REFLECT THE TRUE LPA11 I/O BUS CONFIGURATION#
1502 006312' 043105 042514 052103
1503 006320' 052040 042510 052040
1504 006326' 052522 020105 050114
1505 006334' 030501 020061 027511
1506 006342' 020117 052502 070123
1507 006350' 047503 043116 043511
1508 006356' 051125 052101 047511
1509 006364' 000116
1510          .EVEN
1511
1512          ;**
1513          ;** REQUEST DESCRIPTION ARRAY FOR THE LPA-11 CLOCK
1514          ;** LOCATION "KWTPRE" WILL BE ALTERED, IF "DEDICATED
1515          ;** MICRO-CODE" IS SELECTED, WITH RESPECT TO THE VALUE OF "SR2".
1516
1517 006366' 000011          KWT: 11          ;START CLOCK CODE
1518 006370' 000503          503          ;CLOCK STATUS AND MODE VALUE
1519 006372' 000000          KWTPRE: 0          ;CLOCK PRESET BUFFER VALUE
1520
1521          ;SUBROUTINE TO STOP THE LPA-11# "CLEAN AND NICE"
1522 RESETO: MOV LPCU,CSHA          ;LOAD LPA CONTROL ADDRESS
1523          MOV WPCU,ACSP          ;GET CONTENTS OF OUT REGISTER
1524          MOV WPCI,ASTAT          ;GET CONTENTS OF IN REGISTER
1525          BIC #BIT6,WPCU          ;REMOVE INTR. ENABLE
1526          BIC #BIT6,WPCI          ; FROM THE LPA-11#
1527          HIS #BIT14,WKMD0          ;SET "LPA-11 RESET" BIT
1528          MOV #2,R0          ;LOAD DELAY COUNTER
1529
1530          IS:
1531          BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR....
1532          BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
1533          DEC R0          ;DELAY A PERIOD
1534          BNE IS          ; OF TIME
1535          BIC #BIT14,WKMD0          ;REMOVE "RESET" BIT
1536          RTS PC          ;EXIT SUBROUTINE

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1536 006470' 000000          DEVLST: .AWORD 0          ;MODE WORD FOR START.
1537 006472' 000012          .BLKW 10.          ;TEN ADDRESSES ON START.
1538 006516'
1539          DMDT: .SHLTL DMDT -- DEDICATED MODE DISPATCH TABLE
1540          .IDENT /LPA.03/
1541
1542
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1556
1557
1558          ; CHARLES A. SAMUELSON
1559          ; FEBRUARY 4, 1977
1560
1561          ; +
1562          ; DMDT -- DEDICATED MODE DISPATCH TABLE
1563          ; TABLE FOR LPA11 MICRO PROCESSOR DEDICATED MODE SAMPLING
1564          ; -
1565          ; DEFINED VALUES
1566
1567          ;
1568          DMDSZ=18.          ;LENGTH OF DMDT BUFFER IN BYTES
1569          SDT=100          ;SLAVE DISPATCH TABLE START ADDRESS
1570          CALNC=0          ;CHANNEL ADDRESS INCREMENT VALUE
1571          AD1SR=0          ;ADC #1 STATUS REGISTER ADDRESS LOW BYTE
1572          SEX=100          ;SELECT EXTERNAL CLOCK START AND INTERRUPT ENABLE
1573          RONPR=35          ;REQUEST OUTPUT NPR IN MICRO-PROCESSOR
1574          CLR=0          ;CLEAR AD STATUS REGISTER
1575          RONPRL=235          ;REQUEST OUTPUT NPR LOW BYTE IN MICRO-PROCESSOR
1576          AD1DRL=2          ;ADC #1 DATA REGISTER ADDRESS LOW BYTE
1577          RINPR=15          ;REQUEST INPUT NPR
1578          SCS=40          ;SELECT CLOCK OVERFLOW START FOR ADC'S
1579          AD2SR=40          ;ADC #2 STATUS REGISTER ADDRESS LOW BYTE
1580          AD2DRL=42          ;ADC #2 DATA REGISTER ADDRESS LOW BYTE
1581          AD1SRH=1          ;ADC #1 STATUS REGISTER HIGH BYTE ADDRESS
1582          AD2SRH=41          ;ADC #2 STATUS REGISTER HIGH BYTE ADDRESS
1583          SEN=20          ;SELECT EXTERNAL START, NO INTERRUPT ENABLE
1584
1585          ;
1586          D.OES:          ;ONE ADC, EXTERNAL TRIGGER, SINGLE CHANNEL
1587          .BYTE DMDSZ+3*40,AD1SR,SEX,RONPR,AD1DRL,RINPR,CLR,RONPR,SDT
1588
1589          ;
1590          ; D.OES+23
1591          D.OEG:          ;ONE ADC, EXTERNAL TRIGGER, SEQUENTIAL CHANNEL

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1592	006541'	162	000	000	.BYTE	DMSIZ+<3*40>,CAINC,AD1SRL,SEX,RONPR,AD1DRL,RINPR,CLR,RONPRL,SDT
1593	006544'	100	035	002		
1594	006547'	015	000	235		
1595	006552'	100				
1596	006553'	000				
1597	006564'				.BYTE	CAINC
1598	006564'				.D.OCQ+23	
1599	006564'	162	000	040	.D.OCQ:	:ONE ADC, CLOCK TRIGGER, SINGLE CHANNEL
1600	006567'	035	002	015	.BYTE	DMSIZ+<3*40>,AD1SRL,SCS,RONPR,AD1DRL,RINPR,CLR,RONPRL,SDT
1601	006572'	000	235	100		
1602	006607'				.D.OCQ+23	
1603	006607'				.D.OCQ:	:ONE ADC, CLOCK TRIGGER, SEQUENTIAL CHANNEL
1604	006607'	162	000	000	.BYTE	DMSIZ+<3*40>,CAINC,AD1SRL,SCS,RONPR,AD1DRL,RINPR,CLR,RONPRL,SDT
1605	006612'	040	035	002		
1606	006615'	015	000	235		
1607	006620'	100				
1608	006621'	000			.BYTE	CAINC
1609	006632'				.D.OCQ+23	
1610	006632'				.D.TES:	:TWO ADC, EXTERNAL TRIGGER, SINGLE CHANNEL
1611	006632'	162	000	100	.BYTE	DMSIZ+<3*40>,AD1SRL,SEX,RONPR,AD1DRL,RINPR,CLR,RONPRL,SDT+10
1612	006635'	035	002	015		
1613	006640'	000	235	110		
1614	006643'	040	100	035	.BYTE	AD2SRL,SEX,RONPR,AD2DRL,RINPR,CLR,RONPRL,SDT
1615	006646'	042	015	000		
1616	006651'	235	100			
1617	006655'				.D.TES+23	
1618	006655'				.D.TEQ:	:TWO ADC, EXTERNAL TRIGGER, SEQUENTIAL CHANNEL
1619	006655'	162	000	000	.BYTE	DMSIZ+<3*40>,CAINC,AD1SRL,SEX,RONPR,AD1DRL,RINPR,CLR,RONPRL
1620	006660'	100	035	002		
1621	006663'	015	000	235		
1622	006666'	111	000	040	.BYTE	SDT+11,CAINC,AD2SRL,SEX,RONPR,AD2DRL,RINPR,CLR,RONPRL,SDT
1623	006671'	100	035	042		
1624	006674'	015	000	235		
1625	006677'	100				
1626	006700'				.D.TEQ+23	
1627	006700'				.D.TCS:	:TWO ADC, CLOCK TRIGGER, SINGLE CHANNEL
1628	006700'	262	000	040	.BYTE	DMSIZ+<5*40>,AD1SRL,SCS,RONPR,AD1DRL,RINPR,CLR,RONPRL
1629	006703'	035	002	015		
1630	006706'	000	235			
1631	006710'	110	040	040	.BYTE	SDT+10,AD2SRL,SCS,RONPR,AD2DRL,RINPR,CLR,RONPRL,SDT
1632	006713'	035	042	015		
1633	006716'	000	235	100		
1634	006723'				.D.TCS+23	
1635	006723'				.D.TCQ:	:TWO ADC, CLOCK TRIGGER, SEQUENTIAL CHANNEL
1636	006723'	262	000	000	.BYTE	DMSIZ+<5*40>,CAINC,AD1SRL,SCS,RONPR,AD1DRL,RINPR,CLR,RONPRL
1637	006726'	040	035	002		
1638	006731'	015	000	235		
1639	006734'	111	000	040	.BYTE	SDT+11,CAINC,AD2SRL,SCS,RONPR,AD2DRL,RINPR,CLR,RONPRL,SDT
1640	006737'	040	035	042		
1641	006742'	015	000	235		
1642	006745'	100				
1643	006746'				.D.TCQ+23	
1644					:	
1645					: PARALLEL MODE TABLE	
1646					:	
1647	006746'				.D.TESP:	:TWO ADC, EXTERNAL TRIGGER, SINGLE, PARALLELS

1648	006746'	022	100	000	.BYTE	DMSIZ+<0*40>,SEX,AD1SRL,RONPR,AD2SRL,SEX,RONPR,AD1DRL,RINPR
1649	006751'	035	040	020		
1650	006754'	035	002	015		
1651	006757'	042	015	001	.BYTE	AD2DRL,RINPR,AD1SRH,RONPRL,AD2SRH,RONPRL,SDT+6
1652	006762'	235	041	235		
1653	006765'	106				
1654	006771'				.D.TESP+23	
1655	006771'				.D.TEQP:	:TWO ADC, EXTERNAL TRIGGER, SEQUENTIAL, PARALLEL
1656	006771'	022	100	000	.BYTE	DMSIZ+<0*40>,SEX,AD1SRL,RONPR,AD2SRL,SEX,RONPR,CAINC,AD1DRL
1657	006774'	035	040	020		
1658	006777'	035	000	002		
1659	007002'	015	042	015	.BYTE	RINPR,AD2DRL,RINPR,AD1SRH,RONPRL,AD2SRH,RONPRL,SDT+6
1660	007005'	001	235	041		
1661	007010'	235	106			
1662	007014'				.D.TEQP+23	
1663	007014'				.D.TCSP:	:TWO ADC, CLOCK TRIGGER, SINGLE, PARALLEL
1664	007017'	222	040	000	.BYTE	DMSIZ+<4*40>,SCS,AD1SRL,RONPR,AD2SRL,SCS,RONPR,AD1DRL,RINPR
1665	007017'	035	040	040		
1666	007022'	035	002	015		
1667	007025'	042	015	001	.BYTE	AD2DRL,RINPR,AD1SRH,RONPRL,AD2SRH,RONPRL,SDT+6
1668	007030'	235	041	235		
1669	007033'	106				
1670	007037'				.D.TCSP+23	
1671	007037'				.D.TCQP:	:TWO ADC, CLOCK TRIGGER, SEQUENTIAL, PARALLEL
1672	007037'	222	040	000	.BYTE	DMSIZ+<4*40>,SCS,AD1SRL,RONPR,AD2SRL,SCS,RONPR,CAINC,AD1DRL
1673	007042'	035	040	040		
1674	007045'	035	000	002		
1675	007050'	015	042	015	.BYTE	RINPR,AD2DRL,RINPR,AD1SRH,RONPRL,AD2SRH,RONPRL,SDT+6
1676	007053'	001	235	041		
1677	007056'	235	106			
1678						

1679
1680
1681
1682 007060' 000050
1683 007200' 000000
1684
1685
1686
1687
1688
1689
1690
1691 007202' 000000
1692 007204' 000000
1693 007206' 000000
1694 007210' 000000
1695 007212' 000000
1696 007214' 000000
1697 007216' 000000
1698 007220' 000000
1699 007222' 000000
1700
1701
1702 007224' 000040
1703 007324' 000000
1704
1705 007326'
1706 007326' 000401
1707 010340' 000401
1708 011332' 000401
1709 012344' 000401
1710 013336' 000401
1711 014340' 000401
1712
1713
1714
1715
1716
1717 015342' 015346'
1718
1719 015344' 021352'

THIS IS THE "TO DO" QUEUE FOR THE "INPUT" INTERRUPTS

TODQ: .BLKW 40.
TODUC: .WORD 0

THE FOLLOWING TABLE IS USED BY "ELUOP" TO INDEX THE JOB BASED
ON THE INFO THE LPA RETURNS AS ASSIGNED JOB NUMBERS.
"ELUOP" INDEXES THIS TABLE WITH THE JOB NUMBER TO STORE THE RDA
ADDR. WHEN CODE=0, ANY OTHER TIME TABLE IS INDEXED TO GET
THE RDA ADDR. OF A PARTICULAR JOB.

TABLE: .WORD 0
.WORD 0
.WORD 0
.WORD 0
.WORD 0
.WORD 0
.WORD 0
.WORD 0
.WORD 0
.WORD 0
.WORD 0

NOTE THIS AREA FROM TODQ: TO DONEC: IS
CLEARED EACH TIME "ELUOP" IS ENTERED.

DONEC: .BLKW 32.
.WORD 0

BUFFER:
BUFF0: .BLKW 257. 1ST A TO D'S BUFFER <MULTI-USER MICRO-CODE>
BUFF1: .BLKW 257. 2ND A TO D'S BUFFER <MULTI-USER MICRO-CODE>
.BLKW 257.
BUFF2: .BLKW 257. DIGITAL I/O BUFFER <MULTI-USER MICRO-CODE>
BUFF3: .BLKW 257. D TO A BUFFER <MULTI-USER MICRO-CODE>
.BLKW 257.
.EVEN

THE 2 DIFFERENT MICRO-CODE'S THAT ARE USED BY THIS MODULE RESIDE
HERE TO LOCATION "ALDUNE"
MULTI-USER MICRO-CODE STARTS AT LOCATION
MMAST
DEDICATED A TO D MICRO-CODE STARTS AT LOCATION
UMAST

1720
1721 015346'
1722 015346' 000400 071220 065222
1723 015354' 063224 063226 000777
1724 015362' 063223 063230
1725 015366' 063231 000407 063220
1726 015374' 016777 063160 101013
1727 015402' 000600 063222
1728 015406' 020640 061620 103024
1729 015414' 100431 063170 101020
1730 015422' 063171 101020
1731 015426' 104606 114717 104607
1732 015434' 000452 023240 060360
1733 015442' 115660 104610
1734 015446' 070605 063224 024360
1735 015454' 053221 101451 064201
1736 015462' 043637 106000
1737 015466' 113472 063165 105373
1738 015474' 000407 063225 023250
1739 015502' 063530 107612
1740 015506' 103062 104613 060530
1741 015514' 107211 060603 101467
1742 015522' 104565 120400
1743 015526' 102163 123451 107773
1744 015534' 120620 107773 060522
1745 015542' 103550 123013
1746 015546' 060602 103506 102143
1747 015554' 060613 103533 104773
1748 015562' 000466 062224
1749 015566' 000761 062225 000417
1750 015574' 061230 120600 102114
1751 015602' 123014 000600
1752 015606' 060714 061220 000414
1753 015614' 063220 000417 061230
1754 015622' 061210 000577
1755 015626' 063262 120620 106202
1756 015634' 060533 103536 100545
1757 015642' 000700 061231
1758 015646' 000401 063302 104773
1759 015654' 060533 103540 000576
1760 015662' 063262 104773
1761 015666' 060611 103553 100560
1762 015674' 060531 103556 100477
1763 015702' 000600 061231
1764 015706' 000677 063262 100471
1765 015714' 122104 122125 000414
1766 015722' 123000 063260
1767 015726' 000403 060700 061230
1768 015734' 120600 102173 000576
1769 015742' 123011 060671
1770 015746' 061220 000403 060700
1771 015754' 061230 061210 060611
1772 015762' 103610 104545
1773 015766' 020500 106153 120620
1774 015774' 106147 021364 023000
1775 016002' 000407 063260

.SBTTL LISTING OF MULTI-USER MICROCODE
MMAST:
.WORD 400, 71220, 65222, 63224, 63226, 777, 63223, 63230
.WORD 63231, 407, 63220, 16777, 63160, 101013, 600, 63224
.WORD 20640, 61620, 103024, 100431, 63170, 101020, 63171, 101020
.WORD 104606, 114717, 104607, 452, 23240, 60360, 115660, 104610
.WORD 70605, 63224, 24360, 53221, 101451, 64201, 43637, 106000
.WORD 113472, 63165, 105373, 407, 63225, 23250, 63530, 107612
.WORD 103062, 104613, 60530, 107211, 60603, 101467, 104565, 120400
.WORD 102163, 123451, 107773, 120620, 107773, 60522, 103550, 123013
.WORD 60602, 103506, 102143, 60613, 103533, 104773, 466, 62224
.WORD 761, 62225, 417, 61230, 120600, 102114, 123014, 600
.WORD 60714, 61220, 414, 63220, 417, 61230, 61210, 577
.WORD 63262, 120620, 106202, 60533, 103536, 100545, 700, 61231
.WORD 401, 63302, 104773, 60533, 103540, 576, 63262, 104773
.WORD 60611, 103553, 100560, 60531, 103556, 100477, 600, 61231
.WORD 677, 63262, 100471, 122104, 122125, 414, 123000, 63260
.WORD 403, 60700, 61230, 120600, 102173, 576, 123011, 60671
.WORD 61220, 403, 60700, 61230, 61210, 60611, 103610, 104545
.WORD 20500, 106153, 120620, 106147, 27364, 23000, 407, 63260

1776	016006'	000622	160400	104735	.WORD	622,160400,104735,100710,100722,114643,104550,104550
1777	016014'	100710	100722	114643		
1778	016022'	104550	104550			
1779	016026'	104550	104550	063237	.WORD	104550,104550, 63237, 453, 70001, 42224, 43230, 63070
1780	016034'	000453	070001	042224		
1781	016042'	043230	063070			
1782	016046'	076470	101274	056225	.WORD	76470,101274, 56225, 41230, 20640,107476,120600,102246
1783	016054'	041230	020640	107476		
1784	016062'	120600	102246			
1785	016066'	120620	102306	000440	.WORD	120620,102306, 440, 60704, 62230, 20640,102255, 22010
1786	016074'	060704	062230	020640		
1787	016102'	102255	022010			
1788	016106'	020420	103663	170617	.WORD	20420,103663,170617, 450, 70001, 57230, 57231, 57232
1789	016114'	000450	070001	057230		
1790	016122'	057231	057232			
1791	016126'	076610	076611	062472	.WORD	76610, 76611, 62472,170617, 42225, 43230, 76470,101301
1792	016134'	170617	042225	043230		
1793	016142'	076470	101301			
1794	016146'	100643	041230	043230	.WORD	100643, 41230, 43230, 404, 62410,100644, 22270,104503
1795	016154'	000404	062410	100644		
1796	016162'	022270	104503			
1797	016166'	060602	103313	104556	.WORD	60602,103313,104556, 10012, 20400,103317, 10010, 40620
1798	016174'	010012	020400	103317		
1799	016202'	010010	040620			
1800	016206'	106151	100743	060602	.WORD	106151,100743, 60602,103325,104556, 23010, 63530,103331
1801	016214'	103325	104556	023010		
1802	016222'	063530	103331			
1803	016226'	104557	107550	060530	.WORD	104557,107550, 60530,107550, 20420, 61620,102767, 20420
1804	016234'	107550	020420	061620		
1805	016242'	102767	020420			
1806	016246'	102342	100743	106550	.WORD	102342,100743,106550, 10407, 63224, 703, 63221,100752
1807	016254'	010407	063224	000703		
1808	016262'	063221	100752			
1809	016266'	000702	063221	040620	.WORD	702, 63221, 40620,101764, 73164,105562, 700, 63001
1810	016274'	101764	073164	105562		
1811	016302'	000700	063001			
1812	016306'	000403	060364	101750	.WORD	403, 60364,101750,100752, 72601, 64201,104750, 20400
1813	016314'	100752	072601	064201		
1814	016322'	104750	020400			
1815	016326'	103372	104550	020420	.WORD	103372,104550, 20420,102342,104550, 0, 0, 0
1816	016334'	102342	104550	000000		
1817	016342'	000600	000000			
1818	016346'	120440	103451	134620	.WORD	120440,103451,144620,103451, 43626,107432, 60566,105443
1819	016354'	103451	043626	107432		
1820	016362'	060566	105443			
1821	016366'	000401	060366	105422	.WORD	401, 60366,105422, 456, 70001, 43234, 62574,105022
1822	016374'	060456	070001	043234		
1823	016402'	062574	105022			
1824	016406'	003006	104505	070701	.WORD	3006,104505, 70201, 43234, 576, 76674, 22760, 431
1825	016414'	043234	000576	076674		
1826	016422'	022760	000431			
1827	016426'	104646	104772	070204	.WORD	104646,104772, 70204, 4000, 2777, 64201, 63537,107443
1828	016434'	004000	002777	064201		
1829	016442'	063537	107443			
1830	016446'	060537	107450	104427	.WORD	60537,107450,104427, 600, 63302, 60537,107366,104422
1831	016454'	000600	063302	060537		

1832	016462'	107366	104422			
1833	016466'	000653	060366	105457	.WORD	653, 60366,105457, 654, 60366,105457,104427, 650
1834	016474'	000654	060366	105457		
1835	016502'	104427	000650			
1836	016506'	063226	104427	063066	.WORD	63226,104427, 63066, 63066, 63066, 724, 63006, 70201
1837	016514'	063066	063066	000724		
1838	016522'	063066	070201			
1839	016526'	064201	016501	062606	.WORD	64201, 16501, 62606,104766, 63066, 63066, 63066, 63066
1840	016534'	104766	063066	063066		
1841	016542'	063066	063066			
1842	016546'	063066	063066	063066	.WORD	63066, 63066, 63066, 63066, 63066, 640, 63306, 70201
1843	016554'	063066	063066	000640		
1844	016562'	063306	070201			
1845	016566'	054620	107366	106114	.WORD	54620,107366,106114,104516, 40620,107766, 62606, 20640
1846	016574'	104516	040620	107766		
1847	016602'	062606	020640			
1848	016606'	107117	000420	060704	.WORD	107117, 420, 60704, 62231, 70201, 43230, 420, 62710
1849	016614'	062231	070201	043230		
1850	016622'	000420	062710			
1851	016626'	104766	063066	063066	.WORD	104766, 63066, 63066, 70201, 33631,106143,107372, 401
1852	016634'	070201	043631	106143		
1853	016642'	107372	000401			
1854	016646'	076711	062606	104772	.WORD	76711, 62606,104772, 23366,104504, 63066,104562, 63066
1855	016654'	023366	104504	063066		
1856	016662'	104562	063066			
1857	016666'	063066	063066	063066	.WORD	63066, 63066, 63066, 63066, 63066, 63066, 63066, 63066
1858	016674'	063066	063066	063066		
1859	016702'	063066	063066			
1860	016706'	063066	063126	000700	.WORD	63066, 63126, 700, 60706, 63223, 63226,120440,107766
1861	016714'	060706	063223	063226		
1862	016722'	120440	107766			
1863	016726'	120620	107766	023263	.WORD	120620,107766, 23263, 575,104646, 60606,106366, 600
1864	016734'	000575	104646	060606		
1865	016742'	106366	000600			
1866	016746'	063302	104766	063066	.WORD	63302,104766, 63066, 63066, 63066, 63066, 63066, 63066
1867	016754'	063066	063066	063066		
1868	016762'	063066	063066			
1869	016766'	063066	063066	063066	.WORD	63066, 63066, 63066, 61226, 61207, 740, 63306, 400
1870	016774'	061226	061207	000740		
1871	017002'	063306	000400			
1872	017006'	063236	120620	107621	.WORD	63236,120620,107621,123451,107633, 60616,105621, 631
1873	017014'	123451	107633	000616		
1874	017022'	105621	000631			
1875	017026'	104646	063176	104621	.WORD	104646, 63176,104621, 60522,107636,104623, 60531,107641
1876	017034'	060522	107636	104623		
1877	017042'	060531	107641			
1878	017046'	104623	000600	061231	.WORD	104623, 600, 61231, 677, 63262,104621, 63237, 60606
1879	017054'	000677	063262	104621		
1880	017062'	063237	060606			
1881	017066'	061223	000466	062224	.WORD	61223, 466, 62224, 761, 62225, 417, 61230,120600
1882	017074'	000761	062225	000417		
1883	017102'	061230	120600			
1884	017106'	106257	123052	000500	.WORD	106257,123052, 500, 63272, 600, 60704, 60712, 61222
1885	017114'	063272	000600	060704		
1886	017122'	060712	061222			
1887	017126'	000414	063220	000417	.WORD	414, 63220, 417, 61230, 61210, 60532,107702, 500

1888	017134	061230	061210	060532					
1889	017142	107702	000500						
1890	017146	063302	164617	000600	WORD	63302,164617,	600, 61231,	677, 63262,164617,	63236
1891	017154	061231	000677	063262					
1892	017162	164617	063236						
1893	017166	000404	070001	042224	WORD	404, 70001, 42224,	43230, 63070,	76470,105324,	56225
1894	017174	043230	063070	076470					
1895	017202	105324	056225						
1896	017206	057220	063060	061210	WORD	57220, 63060, 61210,170616,	42225, 43230,	76470,105331	
1897	017214	170616	042225	043230					
1898	017222	076470	105331						
1899	017226	104720	043220	000404	WORD	104720, 43220,	404, 76400,104721,	60602,107155,	23030
1900	017234	076400	104721	060602					
1901	017242	107155	023030						
1902	017246	000405	060370	105744	WORD	405, 60370,105744,104552,	30360,	2402, 53221,	64201
1903	017254	104552	030360	002402					
1904	017262	053221	064201						
1905	017266	016600	036760	036420	WORD	16600, 36760, 36420,	36400, 36500,	36520,123210,	414
1906	017274	036400	036500	036520					
1907	017302	123210	000414						
1908	017306	076670	002502	000456	WORD	76670, 2502,	456, 70401,	16401,104773,123230,	556
1909	017314	070401	016401	104773					
1910	017322	123230	000556						
1911	017326	060670	061231	023366	WORD	60670, 61231, 23366,	20640,116421,	23604, 63232,	417
1912	017334	020640	116421	023604					
1913	017342	063232	000417						
1914	017346	073264	004000	053221	WORD	73264, 4000, 53221,	64201, 60532,113455,	54620,113061	
1915	017354	064201	060532	113455					
1916	017362	054620	113061						
1917	017366	061620	107373	074612	WORD	61620,107373, 74612,113022,	20640,112414,	60612, 61620	
1918	017374	113022	020640	112414					
1919	017402	060612	061620						
1920	017406	117045	110447	054620	WORD	117045,110447, 54620,112027,112427,	427,100632,	431	
1921	017414	112027	112427	060427					
1922	017422	100632	000431						
1923	017426	104707	020640	107476	WORD	104707, 20640,107476,120600,112033,	600, 60704,	62230	
1924	017434	120600	112033	000600					
1925	017442	060704	062230						
1926	017446	020640	112040	022010	WORD	20640,112040, 22010,	20640,112043,	22030,114471,	40620
1927	017454	020640	112043	022030					
1928	017462	114471	040620						
1929	017466	116045	116445	000454	WORD	116045,116445,	454,100632,114445,	43233,	401, 62713
1930	017474	100632	114445	043233					
1931	017502	000401	062713						
1932	017506	104773	060612	107373	WORD	104773, 60612,107373,	20640,112463,	20600, 20640,112466	
1933	017514	020640	112463	020600					
1934	017522	020640	112466						
1935	017526	070600	104773	000474	WORD	20600,104773,	474,104707,	43230,120600,112075,120620	
1936	017534	104707	043230	120600					
1937	017542	112075	120620						
1938	017546	106065	170610	002524	WORD	106065,170610,	2524,100451,	64201, 60610,113520,	60611
1939	017554	100451	064201	060610					
1940	017562	113520	060611						
1941	017566	112120	112520	000406	WORD	112120,112520,	406, 63236,	516, 63237,	63176,101233
1942	017574	063236	000516	063237					
1943	017602	063176	101233						

1944	017606	000411	070401	000533	WORD	411, 70401,	533,114500,	60602,113131,	16541, 2406
1945	017614	114500	060602	113131					
1946	017622	016541	002406						
1947	017626	110542	016604	000413	WORD	110542, 16604,	413, 60401,	63236, 62476,	70220, 36400
1948	017634	060401	063236	062476					
1949	017642	070220	036400						
1950	017646	110627	074620	043232	WORD	110627, 74620, 43232,	63072, 72472,	4000, 20640,112146	
1951	017654	063072	072472	004000					
1952	017662	020640	112146						
1953	017666	000771	060412	061620	WORD	771, 60412, 61620,	62230, 20640,112154,	22010, 20640	
1954	017674	062230	020640	112154					
1955	017702	022010	020640						
1956	017706	112157	022030	020640	WORD	112157, 22030, 20640,112562,	20600,111601,	36400, 22420	
1957	017714	112562	020600	111601					
1958	017722	036400	022420						
1959	017726	000431	060372	111574	WORD	431, 60372,111574,100451,	4620, 63302,	30360, 22660	
1960	017734	100451	004620	063302					
1961	017742	030360	022660						
1962	017746	100451	021006	021027	WORD	100451, 21006, 21027,104463,	403, 70001,	40620,112231	
1963	017754	104463	000403	070001					
1964	017762	040620	112231						
1965	017766	000413	070401	043220	WORD	413, 70401, 43220,	76560,111217,	43220, 62560,	407
1966	017774	076560	111217	043220					
1967	020002	062560	000407						
1968	020006	070401	016663	043232	WORD	70401, 16663, 43237,	72472, 20400,106064,	76620, 22420	
1969	020014	072472	020400	106064					
1970	020022	076620	022420						
1971	020026	100451	111234	000420	WORD	100451,113234,	420,110635,	421, 63220,	20640,107476
1972	020034	110635	000421	063220					
1973	020042	020640	107476						
1974	020046	062210	000413	070401	WORD	62210, 413, 70401,	600, 63302,	22010, 20640,112246	
1975	020054	000600	063302	022010					
1976	020062	020640	112246						
1977	020066	022030	020640	112251	WORD	22030, 20640,112251,	56230, 20640,112254,	42230, 70204	
1978	020074	056230	020640	112254					
1979	020102	042230	070204						
1980	020106	004000	022660	100451	WORD	4000, 22660,100451,	16621, 43232,	63072, 72472,	23013
1981	020114	016621	043232	063072					
1982	020122	072472	023013						
1983	020126	063133	063133	000414	WORD	63133, 63133,	414, 62673,	451, 60401,	60372,111705
1984	020134	062673	000451	060401					
1985	020142	060372	111705						
1986	020146	000416	060401	060372	WORD	416, 60401, 60372,111714,100451,	407, 70401,	16731	
1987	020154	111714	100451	000407					
1988	020162	070401	016731						
1989	020166	002432	000704	063237	WORD	2432, 704, 63237,100663,	411, 70401,	36760, 23030	
1990	020174	100663	000411	070401					
1991	020202	036760	023030						
1992	020206	000407	062670	070201	WORD	407, 62670, 70201,	43231, 60610,103451,	402, 62711	
1993	020214	043231	060610	103451					
1994	020222	000402	062711						
1995	020226	100451	074620	043232	WORD	100451, 74620, 43232,	63072, 72472,	4000, 36400,	22420
1996	020234	063072	072472	004000					
1997	020242	036400	022420						
1998	020246	000447	060372	111744	WORD	447, 60372,111744,100451,	20640,107476,	500, 60704	
1999	020254	100451	020640	107476					

2000	020262	000500	060704						
2001	020266	062230	000402	070001	..WORD	62230,	402,	70001,	64201, 43231, 56230, 20640,112356
2002	020274	064201	043231	056230					
2003	020402	020640	112356						
2004	020306	043630	062230	020640	..WORD	43630,	62230,	20640,	107476, 402, 63237, 10034, 4000
2005	020314	107476	000402	063237					
2006	020322	010034	004000						
2007	020326	057233	057220	060573	..WORD	57233,	57220,	60573,	111375, 63160, 62230, 20640,112376
2008	020334	111375	063160	062230					
2009	020342	020640	112376						
2010	020346	062210	063577	115407	..WORD	62210,	63577,	115407,	116005,110770, 401,114410, 405
2011	020354	116005	110770	000401					
2012	020362	114410	000405						
2013	020366	063234	020640	116011	..WORD	63234,	20640,	116011,	56230, 63174,115011, 60477,112770
2014	020374	056230	063174	115011					
2015	020402	060477	112770						
2016	020406	110504	061620	103040	..WORD	110504,	61620,	103040,	23620,107605, 417, 60660, 73224
2017	020414	023620	107605	000417					
2018	020422	060660	073224						
2019	020426	004000	053221	105773	..WORD	4000,	53221,	105773,	64201, 60600, 61620, 61620, 61620
2020	020434	064201	060600	061620					
2021	020442	061620	061620						
2022	020446	061620	063226	000407	..WORD	61620,	63226,	407,	63266,1044/4, 22202, 404, 70001
2023	020454	063266	104474	022202					
2024	020462	000404	070001						
2025	020466	042226	043230	063070	..WORD	42226,	43230,	63070,	76470,115177, 56227, 57230, 500
2026	020474	076470	115177	056227					
2027	020502	057230	000500						
2028	020506	123231	060671	060710	..WORD	123231,	60671,	60710,	61231, 20640,116464, 22203, 421
2029	020514	061231	020640	116464					
2030	020522	022203	000421						
2031	020526	061230	043230	076570	..WORD	61230,	43230,	76570,	115210, 43230, 76570,115210, 532
2032	020534	115210	043230	076570					
2033	020542	115210	000532						
2034	020546	063237	057230	057231	..WORD	63237,	57230,	57231,	57232, 57233,120600,116105,123634
2035	020554	057232	057233	120600					
2036	020562	116105	123634						
2037	020566	106101	042224	056226	..WORD	106101,	42224,	56226,	42225, 56227, 43220, 60460, 61230
2038	020574	042225	056227	043220					
2039	020602	060460	061230						
2040	020606	000500	060674	060700	..WORD	500,	60674,	60700,	61231,120600,116124,120620,106100
2041	020614	061231	120600	116124					
2042	020622	120620	106100						
2043	020626	000600	023036	060716	..WORD	600,	23036,	60716,	62223, 60536,117537,115215, 456
2044	020634	062223	060536	117537					
2045	020642	115215	000456						
2046	020646	070401	002401	060610	..WORD	70401,	2401,	60610,	62222, 421, 61230, 60536,117630
2047	020654	062222	000421	061230					
2048	020662	060536	117630						
2049	020666	000407	063676	060751	..WORD	407,	63676,	60751,	107477, 420, 60401, 60416, 60416
2050	020674	107477	000420	060401					
2051	020702	060416	060416						
2052	020706	070416	057230	057231	..WORD	70416,	57230,	57231,	43234, 404, 70401, 76610, 76611
2053	020714	043234	000404	070401					
2054	020722	076610	076611						
2055	020726	076614	076612	076613	..WORD	76614,	76612,	76613,	62616,120600,116174,164617, 42227

2056	020734	062616	120600	116174					
2057	020742	164617	042227						
2058	020746	043230	076470	115204	..WORD	43230,	76470,	115204,	114456, 43230, 404, 76410,114457
2059	020754	114456	043230	000404					
2060	020762	076410	114457						
2061	020766	120600	116210	120620	..WORD	120600,	116210,	120620,	106101,104773, 70201, 40620,106502
2062	020774	106101	104773	070201					
2063	021002	040620	106502						
2064	021006	000531	063237	000600	..WORD	531,	63237,	600,	63236, 62223, 600, 60710,114543
2065	021014	063236	062223	000600					
2066	021022	060710	114543						
2067	021026	000403	070401	040620	..WORD	403,	70401,	40620,	117635,104475, 400, 70401, 43230
2068	021034	117635	104475	000400					
2069	021042	070401	043230						
2070	021046	000440	062710	104773	..WORD	440,	62710,	104773,	60602,117246,104556, 23024, 407
2071	021054	060602	117246	104556					
2072	021062	023024	000407						
2073	021066	073264	053221	105554	..WORD	73264,	53221,	105554,	64201, 510, 43235, 62715,104505
2074	021074	064201	000510	043235					
2075	021102	062715	104505						
2076	021106	000600	062231	023260	..WORD	600,	62231,	23260,	20640,117263, 62211, 20640, 61620
2077	021114	020640	117263	062211					
2078	021122	020640	061620						
2079	021126	117266	020620	060360	..WORD	117266,	20620,	60360,	115675,104603, 63160,115263, 20640
2080	021134	115675	104603	063160					
2081	021142	115263	020640						
2082	021146	116277	062210	020640	..WORD	116277,	62210,	20640,	116702, 20600, 60360,115710,104604
2083	021154	116702	020600	060360					
2084	021162	115710	104604						
2085	021166	063160	115277	000405	..WORD	63160,	115277,	405,	61226, 40620, 61227,100453, 23220
2086	021174	061226	040620	061227					
2087	021202	100453	023220						
2088	021206	010052	062600	000405	..WORD	10052,	62600,	405,	63320, 760, 63260, 400, 60360
2089	021214	063320	000760	063260					
2090	021222	000400	060360						
2091	021226	101433	043220	000405	..WORD	101433,	43220,	405,	100432, 0, 0, 0, 0
2092	021234	100432	000000	000000					
2093	021242	000000	000000						
2094	021246	000000	000000	000000	..WORD	0,	0,	0,	0, 0, 0, 0, 0, 0
2095	021254	000000	000000	000000					
2096	021262	000000	000000						
2097	021266	000000	000000	000000	..WORD	0,	0,	0,	0, 0, 0, 0, 0, 0
2098	021274	000000	000000	000000					
2099	021302	000000	000000						
2100	021306	000000	000000	000000	..WORD	0,	0,	0,	0, 0, 0, 0, 0, 0
2101	021314	000000	000000	000000					
2102	021322	000000	000000						
2103	021326	000000	000000	000000	..WORD	0,	0,	0,	0, 0, 0, 0, 0, 0
2104	021334	000000	000000	000000					
2105	021342	000000	000000						
2106									
2107	021346	177777			..WORD	-1			TERMINATOR FLAG
2108	021350	177777			-1				

		DMAST:		.SMITL LISTING OF DEDICATED MICROCODE	
2109	021352'	000400	071220	065222	
2110	021352'	000400	071220	065222	
2111	021352'	000400	071220	065222	
2112	021360'	063224	063226	777	63223, 63230
2113	021366'	063223	063240		
2114	021372'	063231	000407		
2115	021400'	016777	063120	600	63222
2116	021406'	000600	063222		
2117	021412'	020640	061620	103024	
2118	021420'	100431	063170	101020	
2119	021426'	063171	101020		
2120	021432'	104614	100440	104615	
2121	021440'	000452	023240	060360	
2122	021446'	115732	104616		
2123	021452'	023220	010052	062600	
2124	021460'	000405	063320	006760	
2125	021466'	063260	000400		
2126	021472'	060360	101433	043220	
2127	021500'	000405	100432	033364	
2128	021506'	024360	053221		
2129	021512'	101465	064201	043637	
2130	021520'	106000	113520	023250	
2131	021526'	063530	107620		
2132	021532'	103072	104621	060530	
2133	021540'	107217	060603	101477	
2134	021546'	104573	120400		
2135	021552'	102173	123451	107776	
2136	021560'	120620	107776	060522	
2137	021566'	103560	123013		
2138	021572'	060602	103516	102153	
2139	021600'	060613	103543	104776	
2140	021606'	000466	062224		
2141	021612'	000761	062225	000417	
2142	021620'	061230	120600	102124	
2143	021626'	123014	000600		
2144	021632'	060714	061220	000414	
2145	021640'	063220	000417	061230	
2146	021646'	061210	000577		
2147	021652'	063262	120620	106210	
2148	021660'	060533	103546	100555	
2149	021666'	000700	061231		
2150	021672'	000401	063302	104776	
2151	021700'	060533	103550	000576	
2152	021706'	063262	104776		
2153	021712'	060611	103563	100570	
2154	021720'	060531	103566	100507	
2155	021726'	000600	061231		
2156	021732'	000677	063262	100501	
2157	021740'	122104	122125	000414	
2158	021746'	123000	063260		
2159	021752'	000403	060700	061230	
2160	021760'	120600	102203	000576	
2161	021766'	123011	060671		
2162	021772'	061220	000403	060700	
2163	022000'	061230	061210	060611	
2164	022006'	103620	104553		

2165	022012'	020500	106161	120620	
2166	022020'	106155	027364	023000	
2167	022026'	000407	063260		
2168	022032'	000632	160400	104741	
2169	022040'	100726	100740	114715	
2170	022046'	104556	104556		
2171	022052'	104556	104556	063237	
2172	022060'	060534	103317	120600	
2173	022066'	102245	120620		
2174	022072'	106064	010055	042224	
2175	022100'	043230	063070	076470	
2176	022106'	101303	056225		
2177	022112'	041230	020640	107461	
2178	022120'	120600	102263	120620	
2179	022126'	102315	022010		
2180	022132'	020420	103673	170617	
2181	022140'	010052	057230	057231	
2182	022146'	057232	076610		
2183	022152'	076611	062472	170617	
2184	022160'	042225	043230	076470	
2185	022166'	101310	100660		
2186	022172'	041230	043230	000404	
2187	022200'	062410	100661	022270	
2188	022206'	104466	000442		
2189	022212'	060373	101723	100645	
2190	022220'	063467	102245	170617	
2191	022226'	060602	103331		
2192	022232'	104564	010012	020400	
2193	022240'	103335	010010	040620	
2194	022246'	106157	100767		
2195	022252'	060602	103343	104564	
2196	022260'	023010	060530	107166	
2197	022266'	010014	000402		
2198	022272'	060370	101761	000442	
2199	022300'	060370	101756	104556	
2200	022306'	054620	106157		
2201	022312'	074620	040620	106157	
2202	022320'	020420	102366	100767	
2203	022326'	106556	030360		
2204	022332'	000402	063221	040620	
2205	022340'	101775	104570	072601	
2206	022346'	064201	104754		
2207	022352'	120440	103465	134620	
2208	022360'	103465	043626	107431	
2209	022366'	060566	105440		
2210	022372'	000401	060366	105421	
2211	022400'	010060	043234	062574	
2212	022406'	105021	003006		
2213	022412'	104470	070201	043234	
2214	022420'	000576	076674	022760	
2215	022426'	000430	104654		
2216	022432'	104775	070204	004000	
2217	022440'	002777	064201	063537	
2218	022446'	107440	104426		
2219	022452'	000600	063302	060537	
2220	022460'	107371	104421	063066	

2221	022466'	063066	063066		
2222	022472'	000724	063006	070201	
2223	022500'	064201	016501	062606	.WORD 724, 63006, 70201, 64201, 16501, 62606, 104771, 63066
2224	022506'	104771	063066		
2225	022512'	063066	063066	063066	
2226	022520'	063066	063066	063066	.WORD 63066, 63066, 63066, 63066, 63066, 63066, 63066, 63066
2227	022526'	063066	063066		
2228	022532'	000640	063306	070201	
2229	022540'	054620	107371	106077	.WORD 640, 63306, 70201, 54620, 107371, 106077, 104501, 40620
2230	022546'	104501	040620		
2231	022552'	107771	062606	020640	
2232	022560'	107102	000420	060704	.WORD 107771, 62606, 20640, 107102, 420, 60704, 62231, 70201
2233	022566'	062231	070201		
2234	022572'	043230	022270	020620	.WORD 43230, 22270, 20620, 23277, 20640, 106520, 20600, 104513
2235	022600'	023277	020640	106520	
2236	022606'	020600	104513		
2237	022612'	063177	105114	020640	.WORD 63177, 105114, 20640, 107122, 22271, 20640, 61620, 107125
2238	022620'	107122	022271	020640	
2239	022626'	061620	107125		
2240	022632'	020620	000775	063262	.WORD 20620, 775, 63262, 401, 62710, 104771, 63066, 63066
2241	022640'	000401	062710	104771	
2242	022646'	063066	063066		
2243	022652'	070201	043631	106151	.WORD 70201, 43631, 106151, 107375, 401, 76711, 62606, 23366
2244	022660'	107375	000401	076711	
2245	022666'	062606	023366		
2246	022672'	100455	023366	104467	.WORD 100455, 23366, 104467, 63066, 104570, 63066, 63066, 63066
2247	022700'	063066	104570	063066	
2248	022706'	063066	063066		
2249	022712'	063066	063066	063066	.WORD 63066, 63066, 63066, 63066, 63066, 63066, 63066, 63126
2250	022720'	063066	063066	063066	
2251	022726'	063066	063126		
2252	022732'	000700	060706	063223	.WORD 700, 60706, 63223, 63226, 120440, 107771, 120620, 107771
2253	022740'	063226	120440	107771	
2254	022746'	120620	107771		
2255	022752'	023263	000603	104654	.WORD 23263, 603, 104654, 60606, 106371, 600, 63302, 104771
2256	022760'	060606	106371	000600	
2257	022766'	063302	104771		
2258	022772'	063066	063066	063066	.WORD 63066, 63066, 63066, 63066, 63066, 63066, 63066, 63066
2259	023000'	063066	063066	063066	
2260	023006'	063066	063066	063066	
2261	023012'	063066	061226	061207	.WORD 63066, 61226, 61207, 740, 63306, 400, 63236, 120620
2262	023020'	000740	063306	000400	
2263	023026'	063236	120620		
2264	023032'	107627	123451	107641	.WORD 107627, 123451, 107641, 60616, 105627, 637, 104654, 63176
2265	023040'	060616	105627	000637	
2266	023046'	104654	063176		
2267	023052'	104627	060522	107644	.WORD 104627, 60522, 107644, 104631, 60531, 107647, 104631, 600
2268	023060'	104631	060531	107647	
2269	023066'	104631	000600		
2270	023072'	061231	000677	063262	.WORD 61231, 677, 63262, 104627, 63237, 60606, 61223, 466
2271	023100'	104627	063237	060606	
2272	023106'	061223	000466		
2273	023112'	062224	000761	062225	.WORD 62224, 761, 62225, 417, 61230, 120600, 106265, 123052
2274	023120'	000417	061230	120600	
2275	023126'	106265	123052		
2276	023132'	000500	063272	000600	.WORD 500, 63272, 600, 60712, 61222, 414, 63220, 417

2277	023140'	060712	061222	000414	
2278	023146'	063220	000417		
2279	023152'	061230	061210	060532	.WORD 61230, 61210, 60532, 107707, 500, 63302, 164617, 600
2280	023160'	107707	000500	063302	
2281	023166'	164617	000600		
2282	023172'	061231	000677	063262	.WORD 61231, 677, 63262, 164617, 63236, 10006, 42224, 43230
2283	023200'	164617	063236	010006	
2284	023206'	042224	043230		
2285	023212'	063070	076470	105330	.WORD 63070, 76470, 105330, 56225, 57220, 63060, 61210, 170616
2286	023220'	056225	057220	063060	
2287	023226'	061210	170616		
2288	023232'	042225	043230	076470	.WORD 42225, 43230, 76470, 105335, 104724, 43220, 404, 76400
2289	023240'	105335	104724	043220	
2290	023246'	000404	076400		
2291	023252'	104725	060602	107163	.WORD 104725, 60602, 107163, 23030, 405, 60370, 105750, 104560
2292	023260'	023030	000405	060370	
2293	023266'	105750	104560		
2294	023272'	030360	002402	053221	.WORD 30360, 2402, 53221, 64201, 16600, 36760, 36420, 36400
2295	023300'	064201	016600	036760	
2296	023306'	036420	036400		
2297	023312'	036500	036520	123210	.WORD 36500, 36520, 123210, 414, 76670, 2530, 10060, 16401
2298	023320'	000414	076670	002530	
2299	023326'	010060	016401		
2300	023332'	104776	123230	000556	.WORD 104776, 123230, 556, 60670, 61231, 23366, 60602, 112402
2301	023340'	060670	061231	023366	
2302	023346'	060602	112402		
2303	023352'	020640	114527	004002	.WORD 20640, 114527, 4002, 10004, 57234, 57233, 57235, 56227
2304	023360'	010004	057234	057233	
2305	023366'	057235	056227		
2306	023372'	123230	000500	063270	.WORD 123230, 500, 63270, 40710, 61231, 421, 63220, 10006
2307	023400'	040710	061231	000421	
2308	023406'	063220	010006		
2309	023412'	060614	112026	112426	.WORD 60614, 112026, 112426, 425, 100642, 10006, 23650, 112503
2310	023420'	000425	100642	010006	
2311	023426'	023650	112503		
2312	023432'	120600	112030	022202	.WORD 120600, 112030, 22202, 63075, 42226, 20640, 112515, 22203
2313	023440'	063075	042226	020640	
2314	023446'	112515	022203		
2315	023452'	061210	063075	111057	.WORD 61210, 63075, 111057, 62615, 63165, 111020, 10012, 43230
2316	023460'	062615	063165	111020	
2317	023466'	010012	043230		
2318	023472'	120600	112050	120620	.WORD 120600, 112050, 120620, 106064, 62570, 101055, 114604, 76615
2319	023500'	106064	062570	101055	
2320	023506'	114604	076615		
2321	023512'	120600	112060	043230	.WORD 120600, 112060, 43230, 62470, 111070, 42227, 10006, 110444
2322	023520'	062470	111070	042227	
2323	023526'	010006	110444		
2324	023532'	056227	043230	000404	.WORD 56227, 43230, 404, 63010, 62610, 123231, 501, 60671
2325	023540'	063010	062610	123231	
2326	023546'	000501	060671		
2327	023552'	060710	061231	110466	.WORD 60710, 61231, 110466, 61620, 113106, 114531, 106617, 120620
2328	023560'	061620	113106	114531	
2329	023566'	106617	120620		
2330	023572'	106064	060530	107620	.WORD 106064, 60530, 107620, 113026, 104621, 61620, 113035, 114531
2331	023600'	113026	104621	061620	
2332	023606'	113035	114531		

2333	023612	000522	104714	043230	WORD	522,104714, 43230,120600,112123,120620,106050,170610
2334	023620	120600	112123	120620		
2335	023626	106050	170610			
2336	023632	002550	100465	064201	WORD	2550,100465, 64201, 60611,112145,112545, 23374, 424
2337	023640	060611	112145	112545		
2338	023646	023374	030424			
2339	023652	063236	000543	063237	WORD	63236, 543, 63237, 63176,101251, 402, 63302,114553
2340	023660	063176	101251	000402		
2341	023666	063302	114553			
2342	023672	060602	113155	016564	WORD	60602,113155, 16564, 2406,110565, 16647, 415, 63236
2343	023700	002406	110565	016647		
2344	023706	000415	063236			
2345	023712	062476	070220	036400	WORD	62476, 70220, 36400,110672, 74620, 43232, 63072, 72472
2346	023720	110672	074620	043232		
2347	023726	063072	072472			
2348	023732	004000	020640	112171	WORD	4000, 20640,112171, 771, 60412, 61620, 62230, 20640
2349	023740	000771	060412	061620		
2350	023746	062230	020640			
2351	023752	112177	022010	020640	WORD	112177, 22010, 20640,112202, 22030, 20640,112605, 20600
2352	023760	112202	022030	020640		
2353	023766	112605	020600			
2354	023772	111624	036400	022420	WORD	111624, 36400, 22420, 431, 60372,111617,100465, 10011
2355	024000	000431	060372	111617		
2356	024006	100465	010011			
2357	024012	004002	016627	002776	WORD	4002, 16627, 2776,100465, 21006, 21027,104446, 74620
2358	024020	100465	021006	021027		
2359	024026	104446	074620			
2360	024032	043232	063072	072472	WORD	43232, 63072, 72472, 4001, 36400, 22420, 741, 60372
2361	024040	004001	036400	022420		
2362	024046	000741	060372			
2363	024052	111642	100465	004620	WORD	111642,100465, 4620, 63302, 30360, 22660,100465, 10005
2364	024060	063302	030360	022660		
2365	024066	100465	010005			
2366	024072	040620	112274	010015	WORD	40620,112274, 10015, 43220, 76560,111260, 43220, 62560
2367	024100	043220	076560	111260		
2368	024106	043220	062560			
2369	024112	043220	063160	105445	WORD	43220, 63160,105445, 10011, 16725, 43232, 72472, 20400
2370	024120	010011	016725	043232		
2371	024126	072472	020400			
2372	024132	106047	076620	022420	WORD	106047, 76620, 22420,100465,113277, 420,110700, 421
2373	024140	100465	113277	000420		
2374	024146	110700	000421			
2375	024152	063220	020640	107461	WORD	63220, 20640,107461, 62210, 10015, 600, 63302, 22010
2376	024160	062210	010015	000600		
2377	024166	063302	022010			
2378	024172	020640	112310	022030	WORD	20640,112310, 22030, 20640,112313, 56230, 20640,112316
2379	024200	020640	112313	056230		
2380	024206	020640	112316			
2381	024212	042230	070204	004000	WORD	42230, 70204, 4000, 22660,100465, 16664, 43232, 63072
2382	024220	022660	100465	016664		
2383	024226	043232	063072			
2384	024232	072472	023013	063133	WORD	72472, 23013, 63133, 63133, 414, 62673, 453, 60372
2385	024240	063133	000414	062673		
2386	024246	000453	060372			
2387	024252	111745	000420	060372	WORD	111745, 420, 60372,111753,100465, 10011, 16767, 2432
2388	024260	111753	100465	010011		

2389	024266	016767	002432			
2390	024272	000744	063237	100673	WORD	744, 63237,100673, 10013, 36760, 23030, 407, 62670
2391	024300	010013	036760	023030		
2392	024306	000407	062670			
2393	024312	070201	043231	060610	WORD	70201, 43231, 60610,103465, 402, 62711,100465, 74620
2394	024320	103465	000402	062711		
2395	024326	100465	074620			
2396	024332	043232	063072	072472	WORD	43232, 63072, 72472, 4000, 36400, 22420, 447, 60372
2397	024340	004000	036400	022420		
2398	024346	000447	060372			
2399	024352	115402	100465	000500	WORD	115402,100465, 500, 62230, 10004, 4002, 43231, 56230
2400	024360	062230	010004	004002		
2401	024366	043231	056230			
2402	024372	020640	116010	043630	WORD	20640,116010, 43630, 62230, 23373, 402, 60370,115426
2403	024400	062230	023373	000402		
2404	024406	060370	115426			
2405	024412	000630	063233	060531	WORD	630, 63233, 60531,117026, 514, 63233, 60611, 61620
2406	024420	117026	000514	063233		
2407	024426	060611	061620			
2408	024432	116433	000446	063013	WORD	116433, 446, 63013, 60611,116436,114452, 10037, 4000
2409	024440	060611	116436	114452		
2410	024446	010037	004000			
2411	024452	043232	004001	000423	WORD	43232, 4001, 423, 73013, 74531,117125, 62612, 412
2412	024460	073013	074531	117125		
2413	024466	062612	000412			
2414	024472	070013	062612	070213	WORD	70013, 62612, 70213, 4001, 43235, 57237, 437, 63677
2415	024500	004001	043235	057237		
2416	024506	000437	063677			
2417	024512	062230	020640	116061	WORD	62230, 20640,116061, 56230, 63177,115061, 20640,107461
2418	024520	056236	063177	115061		
2419	024526	020640	107461			
2420	024532	000402	063237	010034	WORD	402, 63237, 10034, 4000, 57233, 57220, 60573,115101
2421	024540	004000	057233	057220		
2422	024546	060573	115101			
2423	024552	063160	062230	020640	WORD	63160, 62230, 20640,116102, 62210, 63577,115513,116111
2424	024560	116102	062210	063577		
2425	024566	115513	116111			
2426	024572	114474	000401	114514	WORD	114474, 401,114514, 405, 63234, 20640,116115, 56230
2427	024600	000405	063234	020640		
2428	024606	116115	056230			
2429	024612	063174	115115	060477	WORD	63174,115115, 60477,116474,110532, 407,114450, 61620
2430	024620	116474	110532	000407		
2431	024626	114450	061620			
2432	024632	103055	120620	106064	WORD	103055,120620,106064, 23620,107613, 33364, 4000, 53221
2433	024640	023620	107613	033364		
2434	024646	004000	053221			
2435	024652	105776	064201	060600	WORD	105776, 64201, 60600, 61620, 61620, 61620, 61620, 63226
2436	024660	061620	061620	061620		
2437	024666	061620	063226			
2438	024672	000407	063266	104457	WORD	407, 63266,104457, 60615, 61620, 61620, 61620, 61620
2439	024700	060615	061620	061620		
2440	024706	061620	061620			
2441	024712	061620	063230	000407	WORD	61620, 63230, 407, 63270, 63170,115572, 23651,116574
2442	024720	063270	063170	115572		
2443	024726	023651	116574			
2444	024732	020600	114564	000540	WORD	20600,114564, 540,114605, 61620,117177,114531,106617

AA11K	000260K	282#	547																
ACSR	000102K	233#	433*	614	1523*														
ADDR	000006P	199#	363																
ADDR22=	001000	246#																	
AD10HL=	000002	1576#	1587	1592	1599	1604	1611	1619	1628	1636	1648	1656	1664	1672					
AD1SRH=	000001	1581#	1651	1659	1667	1675													
AD1SRL=	000000	1571#	1587	1592	1599	1604	1611	1619	1628	1636	1648	1656	1664	1672					
AD11K	000252K	279#	529																
AD11K2	000262R	283#	542																
AU2DHL=	000042	1580#	1614	1622	1631	1639	1651	1659	1667	1675									
AD2SHH=	000041	1582#	1651	1659	1667	1675													
AD2SHL=	000040	1579#	1614	1622	1631	1639	1648	1656	1664	1672									
ALDONE	025356R	2499#																	
AR11K	000274R	288#	500	533	551														
ASH	000106R	237#	436*																
ASTAT	000104R	235#	1524*																
AWAS	000110H	238#	435*																
BEGIN	000000H	196#	397	398	440	461	462	470	471	477	486	510	513	521					
		523	596	607	608	618	627	634	641	642	643	644	645	646					
		647	648	649	650	651	652	653	654	655	656	657	658	659					
		660	661	960	978	983	989	1000	1006	1021	1029	1044	1065	1084					
		1530	1531																
BIT0 =	000001	246#	527	603	707	969	999												
BIT1 =	000002	246#	494	715	756	786	791	815	841	867	893	919							
BIT10 =	002000	246#	498	515	531	549	707	783											
BIT11 =	004000	246#	465																
BIT12 =	010000	246#	535	707															
BIT13 =	020000	246#	503																
BIT14 =	040000	246#	458	554	783	1527	1534												
BIT15 =	100000	246#	465	564	807														
BIT2 =	000004	246#	560	807															
BIT3 =	000010	246#	545	735	776	783	786												
BIT4 =	000020	246#	540	736	747														
BIT5 =	000040	246#	570	833															
BIT6 =	000100	246#	404	976	977	998	1525	1526											
BIT7 =	000200	246#	468	481	575	609	715	756	786	791	815	841	859	867					
		893	919	1081															
R1H =	000400	246#	580	711	751	885													
R1T9 =	001000	246#	585	786	911														
HREAKS=	104407	246#	461	462	470	471	607	608	1530	1531									
RR1	000012H	201#	973	975															
BR2	000013H	202#																	
RTODS =	104421	246#																	
BUFFER	007326R	296	640	1705#															
HUFF0	007326R	299	1706#																
HUFF1	010330R	302	1707#																
HUFF2	012334R	305	1709#																
HUFF3	013336R	308	1710#																
CALNC =	000000	1570#	1592	1596	1604	1608	1619	1622	1636	1639	1656	1672							
CDATAS=	104412	246#																	
CLK =	000000	1574#	1587	1592	1599	1604	1611	1614	1619	1622	1628	1631	1636	1639					
CONFIC	000056R	221#																	
CSKA	000100R	231#	1522*																
DATCKS=	104411	246#																	
DATERS=	104404	246#	440																
DEVLST	006470R	493	590*	591*	594	1536#													

DJ0H0	002502H	707#																	
DJ0B1	002732R	708	733	747#															
DJ0B2	003146R	748	774	783#															
DJ0B3	003314H	784	805#																
DJ0B4	003470R	831#																	
DJ0B5	003644H	857#																	
DJ0B6	004020H	883#																	
DJ0B7	004174H	909#																	
DMAST	021352R	410	1719	2110#															
DMDS1Z=	000022	1568#	1587	1592	1599	1604	1611	1619	1628	1636	1648	1656	1664	1672					
DMDT	006516H	1538#																	
DMNEC	007324H	943	1703#																
DR11K1	000256H	281#	562																
DR11K2	000264R	284#	572																
DR11K3	000266H	285#	577																
DR11K4	000270R	286#	582																
DR11K5	000272H	287#	587																
DVID1	000014R	203#																	
D.OCQ	006607R	1603#	1609																
D.OC5	006564R	1598#	1602																
D.OEQ	006541H	1591#	1597																
D.OES	006516H	1586#	1590																
D.TCQ	006723H	1635#	1643																
D.TCQP	007037H	1671#																	
D.TCS	006700R	1627#	1634																
D.TCSP	007014H	1663#	1670																
D.TEG	006655R	1618#	1626																
D.TEGP	006771H	1655#	1662																
D.TES	006632R	1610#	1617																
D.TESP	006746R	1647#	1654																
EBUFF0	000322R	301#	675	725	726	727													
EBUFF1	000330R	304#	676	767	768	769													
EBUFF2	000336R	307#	677	826	827	828	852	853	854	878	879	880	904	905					
		906	930	931	932														
EBUFF3	000344R	310#	678	801	802	803													
EJ0B0	000352R	313#																	
EJ0R0U	000442R	337#	679	724															
EJ0B1	000360R	316#																	
EJ0B1U	000440H	340#	680	766															
EJ0B2	000366R	319#																	
EJ0B2U	000446R	343#	681	800															
EJ0B3	000374H	322#																	
EJ0B3U	000454R	346#	682	825															
EJ0R4	000402R	325#																	
EJ0B4U	000462R	349#	683	851															
EJ0B5	000410R	328#																	
EJ0B5U	000470R	352#	684	877															
EJ0B6	000416R	331#																	
EJ0B6U	000476R	355#	685	903															
EJ0B7	000424H	334#																	
EJ0B7U	000504H	358#	686	929															
ELUOP	004350H	742	779	941#															
ENDITS=	104413	246#	1021																
ENDS =	104410	246#	398	513	523														
ERRTYP	000106R	236#	396#	438*	474*	483*	508*	519*	616*	625*	632*	1027*	1042*	1063*					
EXITS =	104400	246#	978	1000	1084														

SEQ 0055

LPHC DEC/X11 SYSTEM EXERCISER MODULE MACY11 30(1046) 17-NOV-81 11:52 PAGE 58
XLPHC0,P11 17-NOV-81 11:52 CROSS REFERENCE TABLE -- USER SYMBOLS

SEG 0050

[illegible]

BAHE	1096#	1099	1102	1105	1108	1111	1114	1117	1146#	1149	1152	1155	1158	1161	1164
	1167	1196#	1199	1202	1205	1208	1211	1214	1217	1246#	1249	1252	1255	1258	1261
	1264	1267	1296#	1299	1302	1305	1308	1311	1314	1317	1346#	1349	1352	1355	1358
	1361	1364	1367	1396#	1399	1402	1405	1408	1411	1414	1417	1446#	1449	1452	1455
	1458	1461	1464	1467											
BKMOD	1#														
BREAK	1#	460	470	606	1529										
BTOD	1#														
CKDATA	1#														
DATACK	1#														
DATERR	1#	439													
DFSEVN	1#	246													
DSEVNI	1#	246													
END	1#	398	513	523											
ENDIT	1#	1021													
ENDMOD	1#														
EQUATS	1#	246													
EXIT	1#	97#	1000	1083											
GETPA	1#	596	641	642	643	644	645	646	647	648	649	650	651	652	653
	654	655	656	657	658	659	660	661	960	989					
GTPAE	1#	595	959												
GWBUFF	1#														
HRUER	1#	476	485	509	520	617	626	633	1028	1043	1064				
IOMOD	1#	190													
IOMODP	1#														
IOMODX	1#														
IOMODX	1#														
LOERRU	360#	475	484	613	624	1025	1041	1062							
MAP22	1#														
MODULE	1#	191													
MOVEI	1#														
MOVEM	1#														
MRDA	1086#	1136	1186	1236	1286	1336	1386	1436							
MSG	1#														
MSGN	1#	397													
MSGS	1#														
NBKMOD	1#														
OTOA	1#														
PIRG	1#	982	1004												
RAND	1#														
SBKMOD	1#														
SDRM	744#	805	831	857	883	909									
SOFER	1#														
TUUT	27#														
TRYFX1	941#	958													
SCAL	1#														
SDMAST	1#	2110													
SDMDI	1#	1536													
SDMAST	1#	1721													
.KMAADR	1#	250													
.KSIS	1#	364													
.LOADL	1#														
.LPAIN	1#														
.PUTCS	1#														
.RESET	1#														
.UTK	1#														

.SINLP 1#
.SMAC 1#
.SUUTL 1#
.STLK# 1#
.STUUT 278#

.ABS. 000000 000
025360 001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

.XLPHC0.SEG/SOL/CHK/DUC=DDXCON,XLPHC0
RUN-TIME: 5 8 .7 SECONDS
RUN-TIME RATIO: 42/13=3.0
CORE USED: 21K (41 PAGES)

DOCUMENT PAGES: 58

.REM _

IDENTIFICATION

PRODUCT CODE: AC-E971D-MC
PRODUCT NAME: CXRMAD0 RH11,70/RM03,02 S P M
PRODUCT DATE: FEB 1979
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

RMA IS AN IOMOD THAT EXERCISES RM03/RM02 DISK DRIVES ON AN RH11/RH70 CONTROLLER. IT EXERCISES THE DRIVES BY DOING WRITES, WRITE-CHECKS, READS, AND IN-CORE COMPARISONS. ALL ERRORS DETECTED ARE REPORTED ON THE CONSOLE TTY.

2. REQUIREMENTS

HARDWARE: 1 TO 8 RM03/RM02 WITH AN RH11/RH70 CONTROLLER

STORAGE:: RMA REQUIRES:

1. DECIMAL WORDS: 1825
2. OCTAL WORDS: 03441
3. OCTAL BYTES: 7102

3. PASS DEFINITION

ONE PASS OF THE RMA MODULE CONSISTS OF 1300 CYCLES OF THE BASIC TEST SEQUENCE (WRITE, WRITE-CHECK, READ, DATA-CHECK). THE TEST SEQUENCE WRITES 1024 WORDS, WRITE-CHECKS SAME, READS THE FIRST 256 WORDS, AND DATA-CHECKS SAME.

4. EXECUTION TIME

ONE PASS OF RMA RUNNING ALONE ON A PDP-11/40 TAKES APPROXIMATELY 0.9 MINUTE.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DEVADR: 176700, VECTOR: 254, BR1: 5, DEVCNT: 1

REQUIRED PARAMETERS:

NONE

6. DEVICE/OPTION SETUP

MAKE CERTAIN THAT ALL DRIVES ARE POWERED UP, WRITE ENABLED, AND READY
THIS MODULE ALSO SUPPORTS RP04/5/6 ON THE SAME MASSBUS CONTROLLER.
HOWEVER, THIS MODULE IS NOT USED TO EXERCISE RP04/5/6 ALONE.

7. MODULE OPERATION

TEST SEQUENCE:

- A. SETUP DEVICE REGISTER ADDRESSES AND MODULE VARIABLES
- B. RESET ALL DRIVES ON-LINE AND DROP ALL THAT ARE NOT
- C. GET A STARTING SECTOR ADDRESS
- D. GET A DRIVE ADDRESS
- E. DO A WRITE -- IF ERRORS, REPORT AND RETRY UP TO RETRY LIMIT
- F. DO A WRITE-CHECK -- IF ERRORS, REPORT AND RETRY UP TO RETRY LIMIT
- G. DO A READ -- IF ERRORS, REPORT AND RETRY UP TO RETRY LIMIT
- H. DO A DATA-CHECK -- IF ERRORS, REPORT AND RETRY UP TO RETRY LIMIT
- I. IF END OF PASS, REPORT AND GO TO C
- J. IF END OF DRIVES, GO TO C ELSE GO TO D

8. OPERATION OPTIONS

SR1 BIT0 SET(1):

IF THE RETRY LIMIT IS EXCEEDED ON ANY FUNCTION, A HARD ERROR
IS ASSUMED AND THE DRIVE IS DROPPED

SR1 BIT0 CLEAR(0):

IF THE RETRY LIMIT IS EXCEEDED, THE FUNCTION IS ABORTED AND
THE TESTING CONTINUES

SR1 BIT2 SET(1):

COUNT DATA LATE ERRORS BUT DO NOT TYPE THEM OUT

SR1 BIT2 CLEAR(0):

TYPE OUT DATA LATE ERRORS AND COUNT THEM

SR1 BIT5 CLEAR (0)

;NORMAL FOR RM03 PACKS THAT ARE FORMATED
;FOR 16 BIT MODE (PDP-11)

SR1 BIT5 SET (1)

;FOR RM03 16 BIT FORMATED PACKS

SR1 BIT15 SET (1)

;32 REGISTERS ON RH70

SR1 BIT15 CLEAR (0)

;22 REGISTERS ON RH70

9. NON-STANDARD PRINTOUTS

- A. MOST PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN THE DEC/X11 DOCUMENT
- B. ERROR MESSAGES DUMP THE CONTENTS OF THE 20 RH11/RM03 REGISTERS IN THE FOLLOWING ORDER:

RMCS1 RMWC RMBA RMDA RMCS2 RMDS RMER1 RMAS
RMLA RMDB RMR1 RMDT RMSN RMOF RMDC RMHR
RMMR2 RMER2 RMEC1 RMEC2 RMBAE RMCS3 XFER CNT

10. BAD SPOTS

- A. LOCATION 256 THROUGH 452 CONTAIN ROOM FOR 32. BAD SPOTS.
EACH BAD SPOT TAKES TWO WORDS.
THE FIRST WORD SPECIFIES THE CYLINDER ADDRESS.
THE LOW BYTE OF THE SECOND WORD SPECIFIES THE SECTOR ADDRESS.
THE HIGH BYTE OF THE SECOND WORD SPECIFIES THE TRACK ADDRESS.
- B. THE RMA MODULE DOES NOT ACCESS THESE SPOTS ON ANY OF THE DRIVE UNDER TEST.
- C. THE BAD SPOT FILE IS RETRIEVED FROM ALL DRIVES ASSIGNED IN THE BIT MAP "DVID1".
(CYLINDER 822, TRACK 4, SECTOR 0 - MANUFACTUR BAD SPOT FILE)
(CYLINDER 822, TRACK 4, SECTOR 12 - USER BAD SPOT FILE)
LOCATION 1760 MUST BE SET TO 10 TO SEARCH USER BAD SPOT FILE


```

248 000252* 000254* FILE: .WORD BADSPT ;BAD SECTOR TABLE ADDRESS
000254* BADSPT: ;BAD SPOT ;FIRST WORD = CYLINDER ADDRESS
;SECOND WORD = TRACK + SECTOR ADDRESS
249 000254* 17777777 .WORD 1
250 000254* 17777777 .WORD 1
251 000254* 17777777 .WORD 1
252 000254* 17777777 .WORD 1
253 000254* 17777777 .WORD 1
254 000254* 17777777 .WORD 1
255 000254* 17777777 .WORD 1
256 000254* 17777777 .WORD 1
257 000254* 17777777 .WORD 1
258 000254* 17777777 .WORD 1
259 000254* 17777777 .WORD 1
260 000254* 17777777 .WORD 1
261 000254* 17777777 .WORD 1
262 000254* 17777777 .WORD 1
263 000254* 17777777 .WORD 1
264 000254* 17777777 .WORD 1
265 000254* 17777777 .WORD 1
266 000254* 17777777 .WORD 1
267 000254* 17777777 .WORD 1
268 000254* 17777777 .WORD 1
269 000254* 17777777 .WORD 1
270 000254* 17777777 .WORD 1
271 000254* 17777777 .WORD 1
272 000254* 17777777 .WORD 1
273 000254* 17777777 .WORD 1
274 000254* 17777777 .WORD 1
275 000254* 17777777 .WORD 1
276 000254* 17777777 .WORD 1
277 000254* 17777777 .WORD 1
278 000254* 17777777 .WORD 1
279 000254* 17777777 .WORD 1
280 000254* 17777777 .WORD 1
281 000254* 17777777 .WORD 1
282 000254* 17777777 .WORD 1
283 000254* 17777777 .WORD 1
284 000254* 17777777 .WORD 1
285 000254* 17777777 .WORD 1
286 000254* 17777777 .WORD 1
287 000254* 17777777 .WORD 1
288 000254* 17777777 .WORD 1
289 000254* 17777777 .WORD 1
290 000254* 17777777 .WORD 1
291 000254* 17777777 .WORD 1
292 000254* 17777777 .WORD 1
293 000254* 17777777 .WORD 1
294 000254* 17777777 .WORD 1
295 000254* 17777777 .WORD 1
296 000254* 17777777 .WORD 1
297 000254* 17777777 .WORD 1
298 000254* 17777777 .WORD 1
299 000254* 17777777 .WORD 1
300 000254* 17777777 .WORD 1
301 000254* 17777777 .WORD 1
302 000254* 17777777 .WORD 1
303 000254* 17777777 .WORD 1

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304 000424* 17777777 .WORD 1
305 000424* 17777777 .WORD 1
306 000424* 17777777 .WORD 1
307 000424* 17777777 .WORD 1
308 000424* 17777777 .WORD 1
309 000424* 17777777 .WORD 1
310 000424* 17777777 .WORD 1
311 000424* 17777777 .WORD 1
312 000424* 17777777 .WORD 1
313 000424* 17777777 .WORD 1
314 000424* 17777777 .WORD 1
315 000424* 17777777 .WORD 1
316 000424* 00000000 .WORD 1
317 000424* 00000000 .WORD 1
318 000424* 00000000 .WORD 1
319 000424* 00000000 .WORD 1
320 000424* 00000000 .WORD 1
321 000424* 00000000 .WORD 1
322 000424* 00000000 .WORD 1
323 000424* 00000000 .WORD 1
324 000424* 00000000 .WORD 1
325 000424* 00000000 .WORD 1
326 000424* 00000000 .WORD 1
327 000424* 00000000 .WORD 1
328 000424* 00000000 .WORD 1
329 000424* 00000000 .WORD 1
330 000424* 00000000 .WORD 1
331 000504* 00000000 PA18: 0
332 000504* 00000000 XMEM: 0
333 000504* 00000000 PA22: 0
334 000504* 00000000 EA22: 0
335 000504* 00000000 MBLKRV: 0
336 000504* 00000000 MBLKTR: 0
337 000504* 00000000 MODE: 0
338 000504* 00000000 MLOWCY: 0
339 000504* 00000000 MHTCY: 0
340 000504* 00000000
341
342 000526* 000010 T16: 8
343 000526* 000050 20.
344 000526* 000040 32.
345 000526* 000040 410.*40.
346 000526* 040020 413.*40.-25.
347 000526* 040157
348
349
350
351 000540* 000007 *****
352 000542* 000043 T18: 7
353 000544* 000034 28.
354
355
356 000546* 034016 410.*35.
357 000550* 034136 413.*35.-25.
358
359

```

;DATA LATE ERROR COUNTER
;DISK BLOCK COUNTER
;CALCULATED CYLINDER ADDRESS
;MODIFIED CYLINDER ADDRESS FOR RM03
;CYCLE COUNTER
;CYCLE COUNTER1 - DT03 SUPPORT
;HARDWARE DETECTOR BAD SECTOR
;DO NOT CHANGE THE ORDER OF THE NEXT 15 LOCATIONS
;NEEDED FOR MAP22 ROUTINE
;HOLDS 1024 WD BLKS PER TRACK
;HOLDS BLKS PER CYLINDER
;HOLDS LAST SECTOR # THAT WILL FIT PER TRACK
;BLKS THAT FIT IN FIRST 410 CYLINDERS (0-409)
;BLKS THAT FIT IN LAST 413 CYLINDERS (410-822)
;1 BLOCK = 4 SECTORS = 1024 (256 X 4) WORD.
;THESE 5 LOC. GET MOVED TO ABOVE 5 IF 16 BIT MODE
;1024 WORD BLOCKS/CYLINDER
;SECTORS/TRACK = 4
;410. X 40. (CYLINDER 0 - 409)
;413. X 40. (CYLINDER 410 - 822)
;EXCLUDING THE LAST 25 BLOCKS
;THESE 5 LOC GET MAPPED INTO FIRST 5 ABOVE IF 16 BIT MOD
;1024 WORD (16 BIT) BLOCKS/CYLINDER
;SECTORS/TRACK = 4
;ACTUAL SECTORS ON A TRACK
;FOR 16 BIT WORD FORMAT ARE 30.
;410. X 35. (CYLINDER 0 - 409)
;413. X 35. (CYLINDER 410 - 822)
;EXCLUDING THE LAST 25 BLOCKS

360
 361
 362 000552 000000
 363 000554 000000
 364 000556 000000
 365 000558 000000
 366 000560 000000
 367 000562 000000
 368 000564 000000
 369 000566 000000
 370 000568 000000
 371 000570 000000
 372 000572 000000
 373 000574 000400

;*****

ZERO: 0
 DSKADR: 0
 DVICE: 0
 DRIVE: 0
 BLKSZ: 0
 TBUF: 0
 MCNT1: 0
 MCNT2: 0
 UNITNO: 0
 MIXDV: 0
 MOD1: 0
 SUPIN: .BLKW 256.

;MIXDV=-1, IF NOT A RM03 OR RM02

374 001600 000000
 375 001602 000000
 376 001604 000000
 377 001606 000000
 378 001608 000000
 379 001610 000000
 380 001612 000000
 381 001614 000000
 382 001616 000000
 383 001618 000000
 384 001620 000000
 385 001622 000000
 386 001624 000000
 387 001626 000000
 388 001628 000000
 389 001630 000000
 390 001632 000000
 391 001634 000000
 392 001636 000000
 393 001638 000000
 394 001640 000000
 395 001642 000000
 396 001644 000000
 397 001646 000000
 398 001648 000400
 399 001650 177777

TABLE:
 RMCS1: 0
 RMWC: 0
 RMBA: 0
 RMDA: 0
 RMCS2: 0
 RMDS: 0
 RMER1: 0
 RMAS: 0
 RNLA: 0
 RMDR: 0
 RMHR: 0
 RMDT: 0
 RMSN: 0
 RMOP: 0
 RMDC: 0
 RMNR2: 0
 RMER2: 0
 RMCC: 0
 RMCC2: 0
 RMBAE: 0
 RMCS3: 0
 XFERADR: XFERADR
 XFERCT: XFERCT
 177777

```

400 001662 012767 002000 176226 START: MOV #1024,MOFR 1024. WORDS FROM NEW/ITERATION
401 001670 012767 000000 176214 MOV #1,MOFR 23 WORDS TO NEW/ITERATION
402 001670 012767 000000 176214 MOV #1,INTR 23 INTERRUPTS/ITERATION
403 001704 005067 176560 CLR CMT ZERO END OF PASS TESTER
404 001710 005067 176540 CLR DLT CNT CLEAR DATA LATE ERROR COUNTER
405 001710 012767 000000 176554 MOV #1,MOD1 SETUP FOR 16 BIT MODE (NORMAL)
406 001720 012767 000526 176554 MOV #10,R0 GET TABLE OF VALUES FOR 16 BIT MODE
407 001720 032767 000040 176062 BIT #15,SRI 16 BIT MODE?
408 001730 001404 176634 BEQ MOD1 YES
409 001740 012767 000540 176540 MOV #10,R0 NO 16 BIT MODE. CLEAR FMT BIT
410 001740 012767 000540 176540 MOV #10,R0 GET TABLE OF VALUES FOR 16 BIT MODE
411 001740 012767 000540 176540 MOV #10,R0 BEGIN OF TABLE ENTRIES
412 001750 012767 000005 176552 MOV #2,R2 NO OF ENTRIES
413 001750 012767 000005 176552 MOV #2,R2 STORE IN ENTRY
414 001760 005137 176504 15: DEC R2 COUNT IT
415 001760 005137 176504 15: BNE #0,ONCE SET ONE TIME ONLY FLAGS
416 001760 005137 176504 15: MOV #1,ONCE CLEAR FLAGS
417 001770 012767 000007 176504 15: CLR B DVI01, DVICE GET DRIVE INDICATOR
418 001770 012767 000007 176552 MOV #16,0041 IF RM IS LOAD MEDIUM THEN
419 002004 122737 000016 000041 CMPB #16,0041 BEGIN
420 002010 001000 000040 000001 BNE #16,R0 GET LOAD-DRIVE NUMBER
421 002010 001000 000001 000001 MOV #16,R0 INITIALIZE DRIVE POINTER
422 002020 012767 105700 10$: TSTB R0 WHILE NOT POINTING AT LOAD-DRIVE DO
423 002020 012767 105700 10$: BEQ #16,R0 BEGIN
424 002030 001403 176514 12$: ASL R0 COUNT TO NEXT DRIVE
425 002030 001403 176514 12$: DECR R0 COUNT SHIFTS
426 002030 001403 176514 12$: BR #10, R0 END
427 002030 001403 176514 12$: BTB R0, DVICE IF LOAD-DRIVE SELECTED THEN
428 002030 001403 176514 12$: BNE #16, DVICE BEGIN
429 002040 001403 176506 BICR #16, DVICE DROP THE DEVICE
430 002040 001403 176506 MSGNS,BEGIN,LDRIE ASCII MESSAGE CALL WITH COMMON HEADER
431 002050 104403 006746 13$: END
432 002050 104403 006746 13$: IF NO DRIVES ARE SELECTED THEN
433 002050 104403 006746 13$: BEGIN
434 002050 104403 006746 13$: DROP MODULE
435 002060 000100 000752 DAOST: MOV DVICE,DRIVE ALSO SAVE IT IN DRIVE
436 002060 000100 000752 JMP DAST SP0INT,R6 RESTORE STACK POINTER
437 002070 016767 176462 176462 DAOST: MOV #1,BLK1 INITIALIZE BLOCK COUNTER
438 002070 016767 176462 176462 MOV #1,UNITNO INITIALIZE DEVICE COUNTER
439 002070 016767 176462 176462 MOV #1,UNITNO SET UP CONTROLLER REGISTER
440 002070 016767 176462 176462 MOV #1,UNITNO RESET CONTROLLER AND DRIVES
441 002070 016767 176462 176462 MOV #1,UNITNO FOUND THE FIRST DRIVE AVAILABLE
442 002070 016767 176462 176462 MOV #1,UNITNO NO DRIVE?
443 002070 016767 176462 176462 MOV #1,UNITNO DRIVE AVAILABLE
444 002070 016767 176462 176462 MOV #1,UNITNO DROP THE MODULE
445 002070 016767 176462 176462 MOV #1,UNITNO DEVICE READY?
446 002070 016767 176462 176462 MOV #1,UNITNO YES
447 002070 016767 176462 176462 MOV #1,UNITNO NO,TRY NOT READY ROUTINE
448 002070 016767 176462 176462 MOV #1,UNITNO
449 002070 016767 176462 176462 MOV #1,UNITNO
450 002070 016767 176462 176462 MOV #1,UNITNO
451 002070 016767 176462 176462 MOV #1,UNITNO
452 002070 016767 176462 176462 MOV #1,UNITNO
453 002070 016767 176462 176462 MOV #1,UNITNO
454 002070 016767 176462 176462 MOV #1,UNITNO
455 002070 016767 176462 176462 MOV #1,UNITNO

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456 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS
457 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS
458 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS
459 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS
460 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS
461 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS
462 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS
463 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS
464 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS
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472 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS
473 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS
474 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS
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509 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS
510 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS
511 002177 012701 000254 15: MOV #BADSPTR,R1 32 WORDS TABLE ADDRESS

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512 002502* 105267 176046 RETRX: INCR DSKADR ;ADJUST 2 SECTOR
513 002506* 105267 176042 INCR DSKADR ;ADJUST TWO SECTOR
514 002512* 122767 000036 176034 CMPR #30, DSKADR ;SECTORS 0,2,4,.....30, ALL TRIED
515 002520* 101263 ;BRANCH IF NOT
516 002522* 104403 ;MESSAGE: RETRIEVE FAILS
517 002536* 000167 000306 JMP FIN ;DROP THE MODULES

```

```

518 002534* 005767 175732 RESTR1: TST CNT1 ;+ SUPPORT
519 002540* 001005 175722 BNE RSTR1 ;+ FOR
520 002542* 005767 175722 TST CNT ;+ DT03
521 002546* 001002 BNE RSTR1 ;+ BUS
522 002550* 000167 177106 JMP START ;+ SWITCH
523 002552* 104415 000000* 000124* RSTR1: GETPAS, BEGIN, RBUFVA ;+ GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
524 002554* 016767 175344 176000 MOV RBUFVSZ, WCNT2 ;+ SAVE READ BUFFER SIZE
525 002562* 005467 175774 NEG WCNT2 ;+ GET THE 2'S COMPLEMENT
526 002570* 005467 175774 STRT: MOV BLK1, RLKSAV ;+ INIT BLOCK COUNTER
527 002574* 016767 175660 STRT1:
528 002602* 104414 000000* CWRBVS, BEGIN ;+ GET WRITE BUFFER INFORMATION
529 002604* 016767 175330 MOV WRBVSZ, WCNT1 ;+ SAVE WRITE BUFFER SIZE
530 002606* 005467 175746 NEG WCNT1 ;+ GET THE 2'S COMPLEMENT
531 002614* 004767 175746 JSR PC PDUNIT ;+ FIND UNIT #
532 002620* 004767 175742 NEXT: MOV UNITNO, RMCS2 ;+ GET UNIT # SO BOTH PORTS
533 002624* 016777 175742 TST DVICE ;+ WILL BE LOOKING FOR SAME DRIVE
534 002632* 005767 175720 BNE IS ;+ ANY DRIVES LEFT?
535 002634* 000167 000176 JMP FIN ;+ YES
536 002640* 122767 000010 004226 1$: BITB #BIT3, FLAG ;+ NO DROP MODULE
537 002642* 001404 BEQ ABW ;+ MORE DRIVES ON SYS?
538 002652* 012767 177777 175710 MOV #1, UNITNO ;+ YES
539 002654* 000144 JSR PC, RM03CK ;+ RESET DRIVE NUMBER
540 002662* 004767 002250 JSR PC, READY ;+ YES CORRECT SETUP FOR DUEL PORT RETURN
541 002670* 004567 002606 BR ;+ UPDATE DSK ADDR
542 002674* 004767 002376 1$: CLR TRV1 ;+ IS DRIVE READY?
543 002702* 005067 004156 CLR TRV2 ;+ YES, CONTINUE
544 002704* 005067 004124 CLR TRV3 ;+ NOT READY GO WAIT UNTIL IT IS
545 002706* 005067 004124 CLR TRV4 ;+ ZERO RETRX COUNTERS
546 002708* 005067 004150 CLR TRV5
547 002710* 005067 004146 CLR TRV6
548 002712* 005067 004144 CLR TRV7
549 002714* 005067 004142 CLR TRV8
550 002716* 005067 004140 CLR TRV9
551 002718* 005067 004138 CLR TRV10
552 002720* 005067 004136 CLR TRV11
553 002722* 004567 000466 JSR RS, UPDAT ;+ TO SEE IF RM03 OR RM02
;+ FOR THE SELECTED DRIVE

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```

554 002736- 004567 000272 GOC: JSR R5,WRITE ; WRITE SOME DATA
555 002746- 000167 000004 004124 JMP RET1 ; IF ERRORS, TRY IT AGAIN
556 002746- 000167 000004 004124 BITB RBIT,FLAG ; DISK OVERFLOW ?
557 002754- 001410 BEQ COA ; NO, CONTINUE
558 002754- 001410 BICB RBIT,FLAG ; YES, CLEAR OVERFLOW FLAG
559 002754- 001410 MOV R1,BLK1 ; RESET BLOCK ?
560 002772- 000167 177576 JMP STR1 ; CONTINUE
561 002776- 004567 000326 COA: JSR R5,WRITCK ; WRITE-CHECK THE DATA
562 002776- 000167 000114 JMP RETRY2 ; IF ERRORS, TRY AGAIN
563 002776- 000167 000114 GOB: JSR R5,READ ; READ THE DATA WRITTEN
564 003012- 000167 000136 JMP RETRY3 ; IF ERRORS, TRY AGAIN
565 003016- 004412 000000- 000126- CDATAS,RECI,RRUPPA ; REQUEST FOR MONITOR TO CHECK DATA
566 003024- 003026- *+2 ; IF ERROR, CONTINUE
567 003026- 005267 175440- CVCLE: INC CNT1 ; BUMP INDICATOR FOR DT03
568 003032- 104413 000000- ENDIRS,RECI ; SIGNAL END OF ITERATION
569 003032- 000167 177556 JMP NEXT ; MONITOR SHALL TEST END OF PASS
570 003036- 000167 177556 FIMI: JMP NEXT
571 003042- 000167 177556 ENDS,BEGIN ; DROP THE MODULE
572 003042- 104410 000000-
573 003046- 000167 177546 JMT1: JMT1 ; GET NEXT DRIVE
574 003052- 005767 175422 RETRY1: TST BADSEC ; BAD SECTOR DETECTED ?
575 003056- 001404 BEQ RETRY1 ; YES, RETRY
576 003060- 005967 175414 CLC ; CLEAR THE FLAG
577 003060- 005967 175414 INC TR1 ; COUNT THE RETRY
578 003070- 005967 003770 VNCB TR1 ; LIMIT EXCEEDED
579 003074- 005967 000003 003762 CMPB R3,TR1 ; YES
580 003074- 005967 000003 003762 BEQ RETRY ; NO RETRY
581 003074- 005967 000003 003762 JMT1 ; NO RETRY
582 003110- 004403 000000- 006766- MSGNS,BEGIN,EXCPD1 ; ASCII MESSAGE CALL WITH COMMON HEADER
583 003110- 000167 000064 JMT1 ; GO TO NEXT DRIVE
584 003110- 000167 000064 RETRY2: INC TR2 ; COUNT THE RETRY
585 003110- 000167 000003 003731 CMPB R3,TR2 ; LIMIT EXCEEDED
586 003110- 000167 000003 003731 BEQ RETRY ; YES
587 003110- 000167 177634 JMT1 ; NO RETRY
588 003110- 004403 000000- 006774- MSGNS,BEGIN,EXCPD2 ; ASCII MESSAGE CALL WITH COMMON HEADER
589 003110- 000167 000032 JMT1 ; GO TO NEXT DRIVE
590 003110- 000167 000032 RETRY3: INC TR3 ; COUNT THE RETRY
591 003110- 000167 000003 003700 CMPB R3,TR3 ; LIMIT EXCEEDED
592 003110- 000167 000003 003700 BEQ RETRY ; YES
593 003110- 000167 177612 JMT1 ; NO RETRY
594 003110- 004403 000000- 007002- MSGNS,BEGIN,EXCPD3 ; ASCII MESSAGE CALL WITH COMMON HEADER
595 003110- 000167 000000 JMT1 ; GO TO NEXT DRIVE
596 003202- 000167 000000

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601 003206- 002767 000001 174602 NEXTA: BIT RBIT,SRI ; DROP THE DRIVE
602 003214- 001405 BEQ IS ; NO, SKIP TO NEXT DRIVE
603 003216- 004767 000634 JSR PC,DROP ; YES, DROP OFFENDING DRIVE
604 003222- 004403 000000- 007024- MSGNS,BEGIN,DRP ; ASCII MESSAGE CALL WITH COMMON HEADER
605 003230- 000167 177364 JMT1 ; GO ON TO NEXT DRIVE
606 003230- 000167 177364
607
608
609
610
611 ;
612 ; MACRO LINEUP EABITS ; LINE UP EA BITS FOR RHCS1
613 LINEUP EABITS,RO ; GET EXTENDED MEMORY BITS
614 ASL RO ; SHIFT 4 PLACES TO THE LEFT
615 ASL RO ; TO LINE UP WITH RHCS1
616 ASL RO ;
617 ASL RO ;
618 MOV RO,XMEM ; SAVE THE SHIFTED BITS
619 .ENDM LINEUP

```

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619      003234- 012767 000161 175232 WRITE: MOV #161,FUNC ; LOAD WRITE FUNCTION
620      003242- 016777 175320 176332 MOV MCNT1,ARMWC ; LOAD WORD COUNT
621      003250- 016777 174560 176332 MOV RBUFP1,ARMBA ; LOAD BUFFER ADDRESS
622      003258- 016777 175772 176322 MOV DSKADR,ARMDA ; LOAD DISK ADDRESS
623      003266- 004767 001650 176336 JSR PC,RMO3CK ; CHECK FOR RM03 DRIVE
624      003274- 016777 175168 176336 MOV CYL,ARMDC ; LOAD CYLINDER ADDRESS
625      003276- 016767 175162 175156 MOV CYLSAV,CYL ; RESTORE CALCULATED CYLINDER ADDRESS
626      003304- 016700 174626 ; LINEUP
627      003310- 006300 174626 MOV RBUFEA,RO ; GET EXTENDED MEMORY BITS
628      003314- 006300 174626 ASL RO ; SHIFT 4 PLACES TO THE LEFT
629      003316- 006300 174626 ASL RO ; TO LINE UP WITH RHCSI
630      003318- 006300 174626 ASL RO ;
631      003320- 006300 174626 ASL RO ;
632      003322- 010067 175162 MOV RO,XMEM ; SAVE THE SHIFTED BITS
633      003324- 000167 000300 175136 JMP CODO ; CONTINUE
634      003326- 012767 000124 175136 WRITCK: MOV #151,FUNC ; LOAD WRITE-CHECK FUNCTION
635      003328- 016777 174560 176336 MOV MCNT1,ARMWC ; LOAD WORD COUNT
636      003330- 016777 174564 176336 MOV RBUFP1,ARMBA ; LOAD BUFFER ADDRESS
637      003332- 016777 175176 176226 MOV DSKADR,ARMDA ; LOAD DISK ADDRESS
638      003334- 016777 175176 176226 JSR PC,RMO3CK ; CHECK FOR RM03 DRIVE
639      003336- 016777 175176 176242 MOV CYL,ARMDC ; LOAD CYLINDER ADDRESS
640      003338- 016767 175066 175062 MOV CYLSAV,CYL ; RESTORE CALCULATED CYLINDER ADDRESS
641      003400- 016700 174532 ; LINEUP
642      003404- 006300 174532 MOV RBUFEA,RO ; GET EXTENDED MEMORY BITS
643      003406- 006300 174532 ASL RO ; SHIFT 4 PLACES TO THE LEFT
644      003408- 006300 174532 ASL RO ; TO LINE UP WITH RHCSI
645      003410- 006300 174532 ASL RO ;
646      003412- 006300 174532 ASL RO ;
647      003414- 010067 175066 MOV RO,XMEM ; SAVE THE SHIFTED BITS
648      003416- 000167 000204 JMP CODO ; CONTINUE
649      003418- 004767 001010 UPDAT: JSR PC,BLOCK
650      003420- 000205 RTS
651      003430- 000205
652      003432- 012767 000171 175034 READ: MOV #171,FUNC ; LOAD READ FUNCTION
653      003434- 016777 175124 176134 MOV MCNT1,ARMWC ; LOAD WORD COUNT
654      003436- 016777 174564 176134 MOV RBUFP1,ARMBA ; LOAD BUFFER ADDRESS
655      003438- 016777 174564 176134 MOV DSKADR,ARMDA ; LOAD DISK ADDRESS
656      003440- 004767 001452 176124 JSR PC,RMO3CK ; CHECK FOR RM03 DRIVE
657      003442- 016777 174770 176140 MOV CYL,ARMDC ; LOAD CYLINDER ADDRESS
658      003444- 016767 174764 174760 MOV CYLSAV,CYL ; RESTORE CALCULATED CYLINDER ADDRESS
659      003502- 016700 174422 ; LINEUP
660      003506- 006300 174422 MOV RBUFEA,RO ; GET EXTENDED MEMORY BITS
661      003510- 006300 174422 ASL RO ; SHIFT 4 PLACES TO THE LEFT
662      003514- 006300 174422 ASL RO ; TO LINE UP WITH RHCSI
663      003516- 006300 174422 ASL RO ;
664      003518- 006300 174422 ASL RO ;
665      003520- 010067 174764 MOV RO,XMEM ; SAVE THE SHIFTED BITS
666      003522- 000442 JMP CODO ; CONTINUE

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670      003524- 016777 175042 176056 CLEAR: MOV UNITNO,ARMCS2 ; LOAD UNIT ADDRESS
671      003526- 012777 000011 176040 MOV #11,ARMCS1 ; ISSUE A DRIVE CLEAR
672      003528- 006240 000000- ; WAIT
673      003530- 004407 000000- ; FOR DRIVE CLEAR TO FINISH
674      003532- 004407 000000- BREAKS,BEGIN ; TEMPORARY RETURN TO MONITOR
675      003534- 004407 000000- BREAKS,BEGIN ; THEN CONTINUE AT NEXT INSTRUCTION.
676      003536- 012777 000021 176016 1S: MOV #21,ARMCS1 ; ISSUE A PACK ACK
677      003538- 032777 000200 176010 BIT #17,ARMCS1 ; FUNCTION DONE ?
678      003540- 001005 000000- ; YES, CONTINUE
679      003542- 004407 000000- BREAKS,BEGIN ; TEMPORARY RETURN TO MONITOR
680      003544- 004407 000000- BREAKS,BEGIN ; THEN CONTINUE AT NEXT INSTRUCTION.
681      003546- 000767 000000- BR 1S ; NO, WAIT TILL DONE
682      003548- 012777 177777 176004 2S: MOV #1,ARMAS ; CLEAR AS BIT
683      003550- 012777 040000 175760 MOV #1714,ARMCS1 ; CLEAR AND CONTROLLER ERRORS
684      003552- 016777 174752 176004 MOV MOD1,ARMDF ; SET BIT FOR 11 FORMAT
685      003554- 000205 RTS ; RETURN
686      003630- 016777 174736 175752 GOGO: MOV UNITNO,ARMCS2 ; LOAD UNIT SELECT
687      003632- 012777 003764 174144 #INTRUP,VECTOR ; SET INTERRUPT ENTRY POINTER
688      003634- 012767 001000 174204 BIT #ADDR22,RES1 ; 11/707
689      003636- 004438 175724 174622 MOV #RMBA,PA18 ; GET 18 BIT ADDR
690      003638- 017767 174620 174622 ASR XMEM ; SHIFT EA BITS TO POSITION 4,5
691      003640- 006267 174614 174614 ASR XMEM ;
692      003642- 006267 174604 174604 ASR XMEM ;
693      003644- 006267 174600 174600 ASR XMEM ;
694      003646- 006267 174600 174600 ASR XMEM ;
695      003648- 006267 174600 174600 ASR XMEM ;
696      003650- 004416 000504- MAP22S, BEGIN, PA18 ; GET 22-BIT ADDR FROM 18-BIT ADDR
697      003652- 016777 174574 175856 MOV PA22,ARMBA ; LOAD BA REG
698      003654- 016777 174570 175724 MOV PA22,ARMBAE ; LOAD BAE REG
699      003656- 042767 000034 174560 BIC #A4,PA22 ; CLEAR UNWANTED BITS
700      003658- 000367 174554 174554 SWAB PA22 ; LOAD INTO BITS 8,9
701      003660- 016767 174550 174542 MOV EA22,XMEM ; LOAD XMEM TO SET INTO FUNCTION CODE
702      003662- 056767 174536 174522 BIC #FUNK,FUNC ; LOAD EXTENDED MEMORY BITS
703      003664- 016777 174536 174522 MOV FUNK,ARMCS1 ; EXECUTE THE FUNCTION
704      003666- 104400 000000- EXITS,BEGIN ; EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
705      003764-
706      003764- 000004 000000- 003772- NTRPT:
707      003772- 004567 000116 1S: JSR R5,ERRORS ; GO CHECK FOR ERRORS
708      003774- 000205 RTS ; ERRORS DETECTED, RETURN
709      003776- 005725 TST (R5)+ ; NO ERRORS, SKIP RETRY
710      003778- 000205 BNE (R5)+ ; RETURN OK
711      003780- 000205
712      003782- 000205
713      003784- 000205
714      003786- 000205
715      003788- 000205

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716 004006- 016700 174446 ROOM: MOV BLK1,RO ; SAVE THE CURRENT BLOCK NUMBER
717 004012- 016703 174124 MOV MBUS7,R3 ; GET THE TRANSFER SIZE
718 004016- 132767 000002 003054 BTR BIT1,FLAG ; PLENTY OF ROOM LEFT ?
719 004022- 001406 BEQ ; YES, CONTINUE
720 004026- 142767 000001 003044 BTR BIT0,FLAG ; CLEAR 32K INDICATOR
721 004032- 016701 174464 MOV RMHCY,R1 ; LOAD MAX. NUMBER OF BLOCKS
722 004040- 001402 BNE ; EXHAUST OF ALL BLOCKS
723 004042- 005475 4S: BNE ; YES, MUST BE A REAL ERROR
724 004044- 000205 5S: RTS ; RETURN ERROR
725 004046- 152767 000004 003024 BTR BIT2,FLAG ; SET OVERFLOW FLAG
726 004054- 000205 RTS ; RETURN OK
727 -----
728 004056- 012701 000001 DROP: MOV R1,R1 ; INITIALIZE DROP PICKER
729 004062- 016700 174504 MOV UNITNO,RO ; GET THE DRIVE NUMBER
730 004066- 001403 BNE ; IF DRIVE 0 GO DROP IT
731 004070- 000301 1S: ASL R1 ; POINT TO NEXT DRIVE
732 004072- 005300 DEC R0 ; IS THIS THE ONE ?
733 004074- 001777 BNE ; NO, LOOK AGAIN
734 004076- 040167 174454 2S: BTR BIT,DEVICE ; DROP THE DRIVE
735 -----
736 *****
737 CONVERT UNITNO TO ASCII AND
738 STORE AT ADRI
739 *****
740 004102- 104420 000000- 000572- OTOAS,BEGIN,UNITNO,ADRI
741 004110- 007054- RTS ; RETURN
742 004112- 000207
743

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744 004114- 005067 174360 ERRORS: CLR BADSEC ; CLEAR THE BAD SECTOR FLAG
745 004120- 005777 175454 TST RMCS1 ; ATTENTION OR ERROR ?
746 004124- 100402 BNE ; YES
747 004126- 000197 JNC ; NO GO ON TO NEXT FUNTION
748 004130- 000197 22S: BTR BIT9,RMER1 ; ADDRESS OVERFLOW ?
749 004134- 001403 BNE ; NO, CONTINUE
750 004142- 004567 177640 JSR RMCS1 ; YES, IS IT A REAL ERROR ?
751 004146- 000577 175434 1S: BTR BIT10,RMDS ; DID LBT SET?
752 004150- 001126 BNE RESVNC ; YES
753 004154- 004767 001430 JSR PCERRSUB1 ; LOAD ERROR INFORMATION
754 004158- 005777 175420 TST RMCS2 ; IS THIS A DATA LATE ERROR?
755 004162- 005777 BPL ; NO
756 004166- 005267 INCR DLTCT ; ADD 1 TO DATA LATE COUNTER
757 004170- 032767 000004 173612 BTR BIT2,SRI ; TYPE ERROR AND COUNT IT?
758 004174- 001103 BNE ; NO
759 004178- 004403 MSGNS,BEGIN,DLTERR ; ASCII MESSAGE CALL WITH COMMON HEADER
760 004182- 000450 BR 5S ; CONT
761 004186- 032777 020000 175354 11S: BTR BIT13,RMCS1 ; MASSBUS CONTROL PARITY ERROR ?
762 004190- 001033 BNE ; YES
763 004194- 032767 000400 175354 BTR BIT8,RMCS2 ; MASSBUS DATA PARITY ERROR ?
764 004198- 001033 BNE ; YES
765 004202- 032777 040000 175334 BTR BIT14,RMCS1 ; TRANSFER ERROR ?
766 004206- 001015 BNE ; YES
767 004210- 032777 040000 175336 BTR BIT14,RMDS ; ANY DRIVE ERRORS ?
768 004214- 005777 BNE ; YES
769 004218- 001030 TST RMAS ; ANY ATTENTIONS ACTIVE ?
770 004222- 005777 175334 BNE ; YES, CONTINUE
771 004226- 001025 CLR RMSTYP ; UNKNOWN ERROR
772 004230- 005067 173616 *****
773 *****
774 004270- 104405 000000- 001600- HDRERS,BEGIN,TABLE ; SPECIAL CONDITION SET BUT NO REASON FOUND
775 004276- 000445 BR 8S ; RETURN
776 *****
777 004300- 032777 100000 175334 2S: BTR BIT15,RMER2 ; A BAD SPOT ? *****
778 004304- 001044 BNE ; BRANCH IF SO *****
779 004308- 004403 MSGNS,BEGIN,TRERR ; ASCII MESSAGE CALL WITH COMMON HEADER
780 004312- 000407 BR 5S ; GO DUMP REGISTERS
781 *****
782 004320- 104403 000000- 006756- 3S: MSGNS,BEGIN,MCPERR ; ASCII MESSAGE CALL WITH COMMON HEADER
783 004324- 000403 BR 5S ; GO DUMP REGISTERS
784 *****
785 004330- 104403 000000- 006762- 4S: MSGNS,BEGIN,MDPERR ; ASCII MESSAGE CALL WITH COMMON HEADER
786 004334- 005777 175254 5S: TST RMAS ; ANY ATTENTIONS ACTIVE ?
787 004338- 001402 BNE ; NO, CONTINUE
788 004342- 001060 JSR RMWHO ; YES, FIND OUT WHICH DRIVE IT IS
789 004346- 016700 175246 6S: MOV RMDB,RO ; SAVE ADDRESS OF DATA BUFFER
790 004350- 032777 000200 175226 BTR BIT7,RMCS2 ; CAN DATA BUFFER BE READ ?
791 004354- 001003 BNE ; YES, CONTINUE
792 004358- 012767 000552- 175230 MOV ZERO,RMDB ; NO, LOAD ADDRESS OF ZERO
793 004362- 015767 000001 173506 7S: MOV RMERRTYP ; DATA ERROR
794 *****
795 *****
796 004400- 104406 000000- 001600- SOTERS,BEGIN,TABLE ; DUMP RMH1 AND RM REGISTERS
797 *****
798 004406- 010067 175210 8S: MOV RO,RMDB ; RESTORE DATA BUFFER ADDRESS
799 004412- 004567 177106 JSR RS,CLEAR ; GO CLEAR OUT ERRORS

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800 004416- 000205
801 004420- 004417 177100
802 004424- 012767 177777 174046
803 004432- 000205
          9S:   RTS      R5      ; ERRORS DETECTED, RETURN
          JSR      R5,CLEAR  ; CLEAR THE BAD SPOT ERROR
          MOV      R5,BADSEC ; SET THE BAD SPOT ERROR FLAG
          RTS
    
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804 004434- 005725
805 004436- 000205
806
807
808
809
810
811
812 004440- 005267 174014
813 004444- 177767 000002 002426
814 004445- 005012 174042 173776
815 004445- 026767
816 004462- 101017
817 004464- 005067 173770
818 004470- 177767 000002 002402
819 004476- 006411
820
821 004500- 026767 174020 173752
822 004506- 101005 173744
823 004514- 177767 000002 002355
824 004522- 016700 173732
825 004524- 005767 174042
826 004534- 016777 174032 175046
827 004542- 005067 173714
828 004545- 005067 173712
829 004545- 005067 173716
830 004556- 027777 000620 175050
831 004564- 101414
832 004566- 017767 175042 173666
833
834 004574- 062767 000001 173660
835
836
837 004602- 017767 175000 173744
838 004610- 017767 174772 173736
839
840 004616- 000207
          3S:   RTS      R5      ; THIS ROUTINE DETERMINES IF THERE IS ENOUGH ROOM ON THE DISK TO
          ; DO ANOTHER TRANSFER. IF NOT, PROGRAM GOES TO RESYNC TO
          ; ALLOW BOTH PROCESSORS TO RESYNC AND TO RESTART.
          RESYNC: TST      (R5)+  ; SKIP RETRY
                   RTS      R5      ; RETURN OK
          BLOCK:  INC      BLK1    ; STEP TO NEXT BLOCK
                   BITB     BIT1,FLAG ; BLOCK # IN CYLINDER 410. OR HIGHER ?
                   BNE      1S      ; YES, GO ADJUST BLOCK #
                   CMP      #LOWCY,BLK1 ; BLOCK # IN CYLINDER 410. OR LOWER ?
                   BHI      2S      ; YES, RETURN
                   CLR      BLK1    ; NO, RESET BLOCK #
                   BISR     BIT1,FLAG ; SET HIGH RANGE FLAG
                   BR       2S      ; RETURN
                   1S:  CMP      #HIGHCY,BLK1 ; BLOCK # IN WITHIN RANGE ?
                   BHI      2S      ; YES, RETURN
                   CLR      BLK1    ; NO, RESET BLOCK #
                   BITB     BIT1,FLAG ; SET FLAG TO LOWER RANGE FOR NEW PACK CYCLE
                   MOV      BLK1,R0 ; TRANSFER PARAMETER FOR CONVERT
                   TST      #1,R0   ; IS AN RMO3/RMO2 ?
                   BRN      IF SO   ; BRANCH IF SO
                   MOV      UNITNO,@RMC52 ; LOAD THE DRIVE ADDRESS
                   CLR      CYL     ; RESET CYLINDER ADDRESS
                   CLR      CYLSAV  ; RESET THE SAVED CYLINDER ADDRESS
                   CLR      DSKADR  ; RESET THE TRACK AND SECTOR ADDRESS
                   CMP      #400,@RMDC ; CYL 400 ?
                   BLOS     3S      ; BRANCH IF SO
                   MOV      @RMDC,CYL ;
                   MOV      @RMDC,CYLSAV ;
                   ADD      #1,CYL   ; THE NUMBER CAN MODIFY TO INCREMENT
                   ; THE CYLINDER ADDRESS
                   2S:  MOV      @RMDA,DSKADR ;
                   MOV      @RMDA,DSKADR ;
                   3S:  RTS      R5      ;
    
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R41 004620 005067 173636 CONVRT: CLR CVL ; CLEAR DISK ADDRESSES
R42 004624 005067 173636 CLR SEC
R43 004630 005067 002245 CLR TRK
R44 004634 016701 173656 MOV MBKTR,R1 ; LOAD REG. 1 WITH BLK PER TRACK
R45 ; ; REG 0 HAS BLK1 (BLOCK #) IN IT
R46 004640 016702 173650 MOV MBLKRV,R2 ; LOAD REG. 2 WITH BLK PER REVOLUTION
R47 004644 016702 000002 002226 BITI,FLAG ; BLOCK # ON CYLINDER 410. OR LOWER ?
R48 004652 011403 ; YES, CONTINUE
R49 004654 012767 000632 173600 1S: MOV #410,CVL ; NO, LOAD HIGH BASE VALUE FOR CYLINDER ADR.
R50 004662 020001 ; CORRECT CYLINDER FOUND ?
R51 004664 013413 173570 BLD CVL ; YES, CONTINUE
R52 004666 005260 ; NO, STEP TO NEXT CYLINDER
R53 004672 016702 000002 002226 INC R1,R0 ; SUBTRACT 1 CYLINDER FROM BLOCK #
R54 004674 000492 001440 173556 BR 1S ; CONTINUE UNTIL CORRECT CYLINDER IS FOUND
R55 004676 022767 000632 173546 2S: CMP #800,CVL ; NOT OVER CVL 800 FOR PROTECT BAD SE.FILE
R56 004704 010103 ; RESET TO 410
R57 004706 010103 000632 173546 2S: MOV #410,CVL ; CORRECT TRACK FOUND ?
R58 004714 010002 002155 ALT R0,R2 ; YES, CONTINUE
R59 004716 012404 ; NO, STEP TO NEXT TRACK
R60 004720 012404 002155 INCR R2,R0 ; SUBTRACT 1 TRACK FROM BLOCK #
R61 004724 012404 ; CONTINUE UNTIL CORRECT TRACK IS FOUND
R62 004726 000772 000004 173524 3S: BR 2S ; CORRECT SECTOR FOUND ?
R63 004730 005700 ; YES, CONTINUE
R64 004732 012404 000004 173524 ADD #4,SEC ; NO, STEP TO NEXT 1024 WORDS
R65 004734 012404 000004 173524 DEC R0 ; DECREASE BLOCK # BY 1
R66 004742 005300 ; CONTINUE UNTIL CORRECT SECTOR IS FOUND
R67 004744 000772 000005 002125 4S: BR 3S ; LAST TRACK ?
R68 004746 005300 ; NO, CONTINUE
R69 004754 002767 173536 173502 BGT #5,TRK ; YES, LAST SECTOR ?
R70 004756 002767 ; NO, CONTINUE
R71 004764 003003 000004 173472 SEC ; YES, ADJUST SECTOR SO NO OVERFLOW
R72 004766 012767 000004 173552 5S: SEC,DSKADR ; LOAD SECTOR INTO DISK ADDRESS
R73 004768 012767 002093 173545 MOV TRK,DSKADR+1 ; LOAD TRACK INTO DISK ADDRESS
R74 005002 016767 002093 173545 ; CHECK THE SELECT ADDRESS IS IN THE BAD SPOT ?
R75 ; ; INDEX VALUE TO THE BAD SPOT TABLE
R76 005010 005000 173444 000254 BADLOP: CLR R0 ; CYLINDER ADDRESS IN THE SPOT TABLE?
R77 005012 016767 000004 000004 ADD #1,R0 ; YES
R78 005014 0061403 ; CHECK THE NEXT SPOT
R79 005016 0062700 000004 000004 ADD #1,R0 ; COMMON BRANCH POINT
R80 005018 000004 000004 000002 1S: ADD #2,R0 ; CHECK THE TRK AND SEC
R81 005020 000004 000002 000255 1S: TRK,BADSPT+1(R0) ; ON THE BAD SPOT TRACK ?
R82 005022 0126760 000002 000002 BEQ #2,RO ; YES
R83 005024 0061403 000002 000002 BR 1S ; CHECK THE NEXT SPOT
R84 005026 0062700 000002 000002 BR 1S ; COMMON BRANCH POINT
R85 005028 000004 173410 000254 2S: CMPR SEC,BADSPT(R0) ; CHECK THE SECTOR ADDRESS IS IN THIS BLOCK RANGE ?
R86 005030 000004 173376 000002 000002 BRT 1S ; NO NOT IN THIS BLOCK
R87 005032 000004 173372 000254 2S: CMPR SEC,BADSPT(R0) ; WITHIN THE BLOCK ?
R88 005034 000004 173372 000254 2S: BRT 1S ; NOT IN THE BLOCK IF LESS
R89 005036 016767 177777 177777 BLD #1,R0 ; ERROR FLAG
R90 005038 016767 000003 173352 3S: MOV #1,R0 ; COMMON BRANCH POINT
R91 005040 000405 000003 173352 3S: SUB #1,SEC ; RESTORE THE SECTOR VALUE
R92 005042 000002 000002 173352 4S: ADD #1,RO ; CHECK NEXT BAD SPOT
R93 005044 000002 000002 173352 4S: TRK,RO ; MINUS SECH SET. IF ON A BAD SPOT
R94 005046 000002 000002 173352 5S: TST R0 ; SKIP CURRENT SELECTED BLOCK IF BAD SPOT
R95 005048 000002 000002 173352 5S: BPL +6
R96 005122 000002

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R97 005124 000167 177310 JMP BLOCK ; SELECT OTHER BLOCK
R98 005130 022700 000200 CMP #1,R0 ; END OF TABLE
R99 005134 011326 BHI BADLOP ; NO, THEN BRANCH BACK
R00 005136 000207 RTS ; RETURN
R01 ;
R02 ;
R03 005140 016767 173316 173316 RM03CK: MOV CVL,CVLSAV ; SAVE THE CALCULATED ADDRESS
R04 005142 005067 024025 174446 MTD #24025,RMDT ; RESET MIX DRIVE FLAG
R05 005144 001417 024025 174446 BEQ 1S ; DUAL PROT RM02 ?
R06 005146 001417 024025 174436 BEQ 1S ; BRANCH IF SO
R07 005148 022777 020025 174436 CMP #20025,RMDT ; SINGLE PORT RM02 ?
R08 005150 022777 024024 174426 BEQ 1S ; BRANCH IF SO
R09 005152 022777 024024 174426 CMP #24024,RMDT ; DUAL PORT RM03 ?
R10 005154 001407 020024 174416 BEQ 1S ; YES
R11 005156 022777 020024 174416 CMP #20024,RMDT ; ...SINGLE PORT RM03 ?
R12 005158 001403 020024 174416 BEQ 1S ; YES
R13 005160 001403 020024 174416 BEQ 1S ; YES
R14 005162 001403 020024 174416 BEQ 1S ; YES
R15 005164 001403 020024 174416 BEQ 1S ; YES
R16 005166 001403 020024 174416 BEQ 1S ; YES
R17 005168 001403 020024 174416 BEQ 1S ; YES
R18 005170 001403 020024 174416 BEQ 1S ; YES
R19 005172 001403 020024 174416 BEQ 1S ; YES
R20 005174 001403 020024 174416 BEQ 1S ; YES
R21 005176 001403 020024 174416 BEQ 1S ; YES
R22 005178 001403 020024 174416 BEQ 1S ; YES
R23 005180 001403 020024 174416 BEQ 1S ; YES
R24 005182 001403 020024 174416 BEQ 1S ; YES
R25 005184 001403 020024 174416 BEQ 1S ; YES
R26 005186 001403 020024 174416 BEQ 1S ; YES
R27 005188 001403 020024 174416 BEQ 1S ; YES
R28 005190 001403 020024 174416 BEQ 1S ; YES
R29 005192 001403 020024 174416 BEQ 1S ; YES
R30 005194 001403 020024 174416 BEQ 1S ; YES
R31 005196 001403 020024 174416 BEQ 1S ; YES
R32 005198 001403 020024 174416 BEQ 1S ; YES
R33 005200 001403 020024 174416 BEQ 1S ; YES
R34 005202 001403 020024 174416 BEQ 1S ; YES
R35 005204 001403 020024 174416 BEQ 1S ; YES
R36 005206 001403 020024 174416 BEQ 1S ; YES
R37 005208 001403 020024 174416 BEQ 1S ; YES
R38 005210 001403 020024 174416 BEQ 1S ; YES
R39 005212 012767 177777 173354 1S: JMP NEXT ; TO NEXT DRIVE
R40 005214 012767 177777 173354 1S: MOV #1,NIXDV ; SET NON-RM03,RM02 DRIVE FLAG
R41 005216 000207 177777 173354 1S: RTS
R42 005218 000207 177777 173354 1S:
R43 005220 000207 177777 173354 1S:
R44 005222 000207 177777 173354 1S:
R45 005224 000207 177777 173354 1S:
R46 005226 000207 177777 173354 1S:
R47 005228 000207 177777 173354 1S:
R48 005230 000207 177777 173354 1S:
R49 005232 000207 177777 173354 1S:
R50 005234 000207 177777 173354 1S:
R51 005236 000207 177777 173354 1S:
R52 005238 000207 177777 173354 1S:
R53 005240 000207 177777 173354 1S:
R54 005242 000207 177777 173354 1S:
R55 005244 000207 177777 173354 1S:
R56 005246 000207 177777 173354 1S:
R57 005248 000207 177777 173354 1S:
R58 005250 000207 177777 173354 1S:
R59 005252 000207 177777 173354 1S:
R60 005254 000207 177777 173354 1S:
R61 005256 000207 177777 173354 1S:
R62 005258 000207 177777 173354 1S:
R63 005260 000207 177777 173354 1S:
R64 005262 000207 177777 173354 1S:
R65 005264 000207 177777 173354 1S:
R66 005266 000207 177777 173354 1S:
R67 005268 000207 177777 173354 1S:
R68 005270 000207 177777 173354 1S:
R69 005272 000207 177777 173354 1S:
R70 005274 000207 177777 173354 1S:
R71 005276 000207 177777 173354 1S:
R72 005278 000207 177777 173354 1S:
R73 005280 000207 177777 173354 1S:
R74 005282 000207 177777 173354 1S:
R75 005284 000207 177777 173354 1S:
R76 005286 000207 177777 173354 1S:
R77 005288 000207 177777 173354 1S:
R78 005290 000207 177777 173354 1S:
R79 005292 000207 177777 173354 1S:
R80 005294 000207 177777 173354 1S:
R81 005296 000207 177777 173354 1S:
R82 005298 000207 177777 173354 1S:
R83 005300 000207 177777 173354 1S:
R84 005302 000207 177777 173354 1S:
R85 005304 000207 177777 173354 1S:
R86 005306 000207 177777 173354 1S:
R87 005308 000207 177777 173354 1S:
R88 005310 000207 177777 173354 1S:
R89 005312 000207 177777 173354 1S:
R90 005314 000207 177777 173354 1S:
R91 005316 000207 177777 173354 1S:
R92 005318 000207 177777 173354 1S:
R93 005320 000207 177777 173354 1S:
R94 005322 000207 177777 173354 1S:
R95 005324 000207 177777 173354 1S:
R96 005326 000207 177777 173354 1S:
R97 005328 000207 177777 173354 1S:
R98 005330 000207 177777 173354 1S:
R99 005332 000207 177777 173354 1S:
R00 005334 000207 177777 173354 1S:
R01 005336 000207 177777 173354 1S:
R02 005338 000207 177777 173354 1S:
R03 005340 000207 177777 173354 1S:
R04 005342 000207 177777 173354 1S:
R05 005344 000207 177777 173354 1S:
R06 005346 000207 177777 173354 1S:
R07 005348 000207 177777 173354 1S:
R08 005350 000207 177777 173354 1S:
R09 005352 000207 177777 173354 1S:
R10 005354 000207 177777 173354 1S:
R11 005356 000207 177777 173354 1S:
R12 005358 000207 177777 173354 1S:
R13 005360 000207 177777 173354 1S:
R14 005362 000207 177777 173354 1S:
R15 005364 000207 177777 173354 1S:
R16 005366 000207 177777 173354 1S:
R17 005368 000207 177777 173354 1S:
R18 005370 000207 177777 173354 1S:
R19 005372 000207 177777 173354 1S:
R20 005374 000207 177777 173354 1S:
R21 005376 000207 177777 173354 1S:
R22 005378 000207 177777 173354 1S:
R23 005380 000207 177777 173354 1S:
R24 005382 000207 177777 173354 1S:
R25 005384 000207 177777 173354 1S:
R26 005386 000207 177777 173354 1S:
R27 005388 000207 177777 173354 1S:
R28 005390 000207 177777 173354 1S:
R29 005392 000207 177777 173354 1S:
R30 005394 000207 177777 173354 1S:
R31 005396 000207 177777 173354 1S:
R32 005398 000207 177777 173354 1S:
R33 005400 000207 177777 173354 1S:
R34 005402 000207 177777 173354 1S:
R35 005404 000207 177777 173354 1S:
R36 005406 000207 177777 173354 1S:
R37 005408 000207 177777 173354 1S:
R38 005410 000207 177777 173354 1S:
R39 005412 000207 177777 173354 1S:
R40 005414 000207 177777 173354 1S:
R41 005416 000207 177777 173354 1S:
R42 005418 000207 177777 173354 1S:
R43 005420 000207 177777 173354 1S:
R44 005422 000207 177777 173354 1S:
R45 005424 000207 177777 173354 1S:
R46 005426 000207 177777 173354 1S:
R47 005428 000207 177777 173354 1S:
R48 005430 000207 177777 173354 1S:
R49 005432 000207 177777 173354 1S:
R50 005434 000207 177777 173354 1S:
R51 005436 000207 177777 173354 1S:
R52 005438 000207 177777 173354 1S:
R53 005440 000207 177777 173354 1S:
R54 005442 000207 177777 173354 1S:
R55 005444 000207 177777 173354 1S:
R56 005446 000207 177777 173354 1S:
R57 005448 000207 177777 173354 1S:
R58 005450 000207 177777 173354 1S:
R59 005452 000207 177777 173354 1S:
R60 005454 000207 177777 173354 1S:
R61 005456 000207 177777 173354 1S:
R62 005458 000207 177777 173354 1S:
R63 005460 000207 177777 173354 1S:
R64 005462 000207 177777 173354 1S:
R65 005464 000207 177777 173354 1S:
R66 005466 000207 177777 173354 1S:
R67 005468 000207 177777 173354 1S:
R68 005470 000207 177777 173354 1S:
R69 005472 000207 177777 173354 1S:
R70 005474 000207 177777 173354 1S:
R71 005476 000207 177777 173354 1S:
R72 005478 000207 177777 173354 1S:
R73 005480 000207 177777 173354 1S:
R74 005482 000207 177777 173354 1S:
R75 005484 000207 177777 173354 1S:
R76 005486 000207 177777 173354 1S:
R77 005488 000207 177777 173354 1S:
R78 005490 000207 177777 173354 1S:
R79 005492 000207 177777 173354 1S:
R80 005494 000207 177777 173354 1S:
R81 005496 000207 177777 173354 1S:
R82 005498 000207 177777 173354 1S:
R83 005500 000207 177777 173354 1S:
R84 005502 000207 177777 173354 1S:
R85 005504 000207 177777 173354 1S:
R86 005506 000207 177777 173354 1S:
R87 005508 000207 177777 173354 1S:
R88 005510 000207 177777 173354 1S:
R89 005512 000207 177777 173354 1S:
R90 005514 000207 177777 173354 1S:
R91 005516 000207 177777 173354 1S:
R92 005518 000207 177777 173354 1S:
R93 005520 000207 177777 173354 1S:
R94 005522 000207 177777 173354 1S:
R95 005524 000207 177777 173354 1S:
R96 005526 000207 177777 173354 1S:
R97 005528 000207 177777 173354 1S:
R98 005530 000207 177777 173354 1S:
R99 005532 000207 177777 173354 1S:
R00 005534 000207 177777 173354 1S:
R01 005536 000207 177777 173354 1S:
R02 005538 000207 177777 173354 1S:
R03 005540 000207 177777 173354 1S:
R04 005542 000207 177777 173354 1S:
R05 005544 000207 177777 173354 1S:
R06 005546 000207 177777 173354 1S:
R07 005548 000207 177777 173354 1S:
R08 005550 000207 177777 173354 1S:
R09 005552 000207 177777 173354 1S:
R10 005554 000207 177777 173354 1S:
R11 005556 000207 177777 173354 1S:
R12 005558 000207 177777 173354 1S:
R13 005560 000207 177777 173354 1S:
R14 005562 000207 177777 173354 1S:
R15 005564 000207 177777 173354 1S:
R16 005566 000207 177777 173354 1S:
R17 005568 000207 177777 173354 1S:
R18 005570 000207 177777 173354 1S:
R19 005572 000207 177777 173354 1S:
R20 005574 000207 177777 173354 1S:
R21 005576 000207 177777 173354 1S:
R22 005578 000207 177777 173354 1S:
R23 005580 000207 177777 173354 1S:
R24 005582 000207 177777 173354 1S:
R25 005584 000207 177777 173354 1S:
R26 005586 000207 177777 173354 1S:
R27 005588 000207 177777 173354 1S:
R28 005590 000207 177777 173354 1S:
R29 005592 000207 177777 173354 1S:
R30 005594 000207 177777 173354 1S:
R31 005596 000207 177777 173354 1S:
R32 005598 000207 177777 173354 1S:
R33 005600 000207 177777 173354 1S:
R34 005602 000207 177777 173354 1S:
R35 005604 000207 177777 173354 1S:
R36 005606 000207 177777 173354 1S:
R37 005608 000207 177777 173354 1S:
R38 005610 000207 177777 173354 1S:
R39 005612 000207 177777 173354 1S:
R40 005614 000207 177777 173354 1S:
R41 005616 000207 177777 173354 1S:
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R50 005634 000207 177777 173354 1S:
R51 005636 000207 177777 173354 1S:
R52 005638 000207 177777 173354 1S:
R53 005640 000207 177777 173354 1S:
R54 005642 000207 177777 173354 1S:
R55 005644 000207 177777 173354 1S:
R56 005646 000207 177777 173354 1S:
R57 005648 000207 177777 173354 1S:
R58 005650 000207 177777 173354 1S:
R59 005652 000207 177777 173354 1S:
R60 005654 000207 177777 173354 1S:
R61 005656 000207 177777 173354 1S:
R62 005658 000207 177777 173354 1S:
R63 005660 000207 177777
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023 005222 005267 173344 FDNIT: INC UNITNO ; COUNT A DRIVE
024 005242 005267 173344 ; SET END OF DRIVES FLAG
025 005253 005267 173330 ; CLEAR END OF DRIVES FLAG
026 005242 005267 173310 ; ALL DRIVES CHECKED ?
027 005242 005267 173310 ; YES, GO FLAG END OF DRIVES
028 005242 005267 173310 ; NO, IS NEXT DRIVE CHOSEN ?
029 005253 005267 173310 ; NO, GO TRY ANOTHER DRIVE
030 005253 005267 173310 ; RETURN
031 005253 005267 173310 ; SET END OF DRIVES FLAG
032 005253 005267 173310 ; RESET DRIVE COUNTER
033 005253 005267 173310 ; RESTORE CHOSEN DRIVES
034 005253 005267 173310 ; RETURN
035 005253 005267 173310 ;
036 005253 005267 173310 ;
037 005253 005267 173310 ;
038 005253 005267 173310 ;
039 005253 005267 173310 ;
040 005253 005267 173310 ;
041 005253 005267 173310 ;
042 005253 005267 173310 ;
043 005253 005267 173310 ;
044 005253 005267 173310 ;
045 005253 005267 173310 ;
046 005253 005267 173310 ;
047 005253 005267 173310 ;
048 005253 005267 173310 ;
049 005253 005267 173310 ;
050 005253 005267 173310 ;
051 005253 005267 173310 ;
052 005253 005267 173310 ;
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054 005253 005267 173310 ;
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056 005253 005267 173310 ;
057 005253 005267 173310 ;
058 005253 005267 173310 ;
059 005253 005267 173310 ;
060 005253 005267 173310 ;
061 005253 005267 173310 ;
062 005253 005267 173310 ;

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063 005430 012777 174162 WHO: MOV BRMS,R1 ; GET THE ATTENTION SUMMARY
064 005430 012777 174162 ; SAVE THE STATUS REGISTER
065 005430 012777 174162 ; SET POINTER TO DRIVE 0
066 005430 012777 174162 ; ZERO THE DRIVE COUNTER
067 005430 012777 174162 ; FIND IT ?
068 005430 012777 174162 ; YES, CONTINUE
069 005430 012777 174162 ; INCREMENT THE DRIVE COUNTER
070 005430 012777 174162 ; SET POINTER TO NEXT DRIVE
071 005430 012777 174162 ; ALL DONE ?
072 005430 012777 174162 ; NO, GO AGAIN
073 005430 012777 174162 ; SOME BODY LIES -- NO ATTENTIONS SET
074 005430 012777 174162 ; CLEAR OUT OLD UNIT NUMBER
075 005430 012777 174162 ; LOAD THE NEW UNIT NUMBER
076 005430 012777 174162 ; RESTORE THE STATUS REGISTER
077 005430 012777 174162 ; RETURN
078 005430 012777 174162 ;
079 005430 012777 174162 ;
080 005430 012777 174162 ;
081 005430 012777 174162 ;
082 005430 012777 174162 ;
083 005430 012777 174162 ;
084 005430 012777 174162 ;
085 005430 012777 174162 ;
086 005430 012777 174162 ;
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100 005430 012777 174162 ;
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102 005430 012777 174162 ;
103 005430 012777 174162 ;
104 005430 012777 174162 ;
105 005430 012777 174162 ;
106 005430 012777 174162 ;
107 005430 012777 174162 ;
108 005430 012777 174162 ;
109 005430 012777 174162 ;
110 005430 012777 174162 ;
111 005430 012777 174162 ;

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1012
1013
1014
1015
1016
1017 005632 012777 000040 173750 REZET: MOV BIT5,RMCS2 ; ISSUE AN RH11 INIT
1018 005640 012767 177777 173750 MOV BIT5,RMAS ; CLEAR ALL ATA BITS
1019 005646 012767 077777 172602 MOV BIT5,CLK ; SET THE TIMER
1020 005654 032777 000200 173716 1$: BIT BIT7,RMCS1 ; CONTROLLER READY ?
1021 005662 001017 000000- RNE Z ; YES, CONTINUE
1022 005668 014407 000000- BREAK,BEGIN ; TEMPORARY RETURN TO MONITOR...
1023 005670 014407 000000- BREAK,BEGIN ; THEN CONTINUE AT NEXT INSTRUCTION.
1024 005674 005367 172556 DEC CLK ; WAIT SOME MORE ?
1025 005700 001365 172650 RNE Z ; YES
1026 005702 005008 172172 CLR DVICE ; NO, SET TO DROP THE MODULE
1027 005706 012767 000004 172172 MOV BIT5,RTYP ; CONTROLLER NOT READY
1028 ***** ; *****
1029 005714 104405 000000 001600- HRRD,BEGIN,TABLE ; CONTROLLER NOT READY
1030 ***** ; *****
1031 005722 000207 2$: RTS PC ; RETURN
1032

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1033 005724 016700 172056 SETUP: MOV ADDR,R0 ; GET DEVICE ADDRESS
1034 005730 005720 173644 MOV R0,RMCS1 ; GENERATE REGISTER ADDRESSES
1035 005736 010067 173640 TST (R0)+
1036 005744 005720 173634 MOV R0,RMWC
1037 005750 005720 173630 MOV R0,RMRA
1038 005756 010067 173620 MOV R0,RMDA
1039 005762 005720 173614 TST (R0)+
1040 005768 005720 173610 MOV R0,RMCS2
1041 005774 005720 173604 TST (R0)+
1042 005780 010067 173600 MOV R0,RMDS
1043 005786 005720 173594 TST (R0)+
1044 005792 005720 173588 MOV R0,RMER1
1045 005798 010067 173580 TST (R0)+
1046 005804 005720 173574 MOV R0,RMAS
1047 005810 005720 173568 TST (R0)+
1048 005816 005720 173564 MOV R0,RMLA
1049 005822 005720 173558 TST (R0)+
1050 005828 010067 173550 MOV R0,RMDB
1051 005834 005720 173544 TST (R0)+
1052 005840 005720 173538 MOV R0,RMMR1
1053 005846 010067 173530 TST (R0)+
1054 005852 005720 173524 MOV R0,RMDT
1055 005858 005720 173518 TST (R0)+
1056 005864 005720 173512 MOV R0,RMSN
1057 005870 010067 173506 TST (R0)+
1058 005876 005720 173500 MOV R0,RMOP
1059 005882 005720 173494 TST (R0)+
1060 005888 005720 173488 MOV R0,RMDC
1061 005894 010067 173480 TST (R0)+
1062 005898 005720 173474 MOV R0,RMHR
1063 005904 005720 173468 TST (R0)+
1064 005910 005720 173462 MOV R0,RMMR2
1065 005916 005720 173456 TST (R0)+
1066 005922 005720 173450 MOV R0,RMER2
1067 005928 005720 173444 TST (R0)+
1068 005934 005720 173438 MOV R0,RMEC1
1069 005940 005720 173432 TST (R0)+
1070 005946 010067 173426 MOV R0,RMEC2
1071 005952 005720 173420 BIT #ADDR22,RES1 ; 11/70 MONITOR?
1072 005958 001417 171732 BNO 1$ ; NO
1073 005964 016700 171654 ADD ADDR,R0 ; LOCATE THE RMBAE
1074 005970 005720 100050 171652 BIT BIT15,SR1 ; ASSUME 22 REGISTERS
1075 005976 032777 100000 171652 BNO 2$ ; 32 REGISTER ?
1076 005982 001402 000024 BNO 3$ ; BRANCH IF NOT 32 REGISTERS
1077 005988 005720 173472 ADD ADDR,R0 ; ADJUST THE RMBAE ADDRESS
1078 005994 005720 173466 3$: MOV R0,RMBAE
1079 005998 010067 173460 TST (R0)+
1080 006004 005720 173454 1$: MOV R0,RMCS3
1081 006010 016700 171640 MOV VECTOR,R0 ; GET VECTOR ADDRESS
1082 006016 016700 171634 MOV BIT7,(R0)+ ; SET POINTER JUST IN CASE
1083 006022 171630 171612 BRT,PC ; SET PRIORITY
1084 006028 171610 171612 2$: RTS PC ; RETURN
1085
1086

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1087	006200	020040	051124	047101	MES1:	.ASCIZ	"TRANSFER ERROR%"
1088	006210	041123	047522	022822			
1089	006216	051105	047522	022822			
1090	006224	0000					
1091	006225	0400	046440	051501	MES2:	.ASCIZ	"MASSBUS PARITY ERROR%"
1092	006230	041123	047522	022822			
1093	006240	040520	044522	054824			
1094	006246	020040	051105	047522			
1095	006254	022522	0000				
1096	006257	0400	046440	051501	MES3:	.ASCIZ	"MASSBUS DATA PARITY ERROR%"
1097	006264	041123	051525	020040			
1098	006272	040504	040524	020040			
1099	006300	040520	044522	054824			
1100	006306	020040	051105	047522			
1101	006306	022522	0000				
1102	006317	0400	042040	044522	MES4:	.ASCIZ	"DRIVE "
1103	006324	042526	020040	020040			
1104	006331	0400	042040	047522	MES5:	.ASCIZ	"DROPPED%"
1105	006336	050120	041105	000035			
1106	006344	051040	051105	054822	MES6:	.ASCIZ	"RETRY EXCEEDED%"
1107	006352	042440	041530	042505			
1108	006360	042504	020040	0000			
1109	006366	042504	020040	0000			
1110	006372	052111	000105	051127	MES7:	.ASCIZ	"WRITE"
1111	006376	020040	053440	044522	MES8:	.ASCIZ	"WRITE-CHECK"
1112	006404	042524	041455	042510			
1113	006412	042524	041455	042510			
1114	006415	0400	020040	042522	MES9:	.ASCIZ	"READ"
1115	006422	042101	0000				
1116	006425	0400	040524	040524	MES10:	.ASCIZ	"DATA LATE ERROR%"
1117	006425	046440	047522	022822			
1118	006440	051105	047522	022822			
1119	006446	0000					
1120	006447	0400	051104	053111	MES11:	.ASCIZ	"DRIVE NOT READY%"
1121	006447	020040	042101	022531			
1122	006447	042522	042101	022531			
1123	006470	0000					
1124	006471	020040	047503	046125	MES12:	.ASCIZ	"COULD NOT GET DRIVE%"
1125	006476	020040	047503	046125			
1126	006504	042507	020124	051104			
1127	006512	053111	022505	0000			
1128	006517	047522	057537	020137	MES13:	.ASCIZ	"---- NOT A RM03/RM02%"
1129	006517	047522	057537	020137			
1130	006521	046525	031460	051054			
1131	006540	030115	022462	0000			
1132	006545	042045	051104	050117	MESLDP:	.ASCIZ	"%DROPPED RMDP LOAD DRIVE%"
1133	006545	042045	051104	050117			
1134	006560	050104	046504	046524			
1135	006566	020104	051104	053111			
1136	006574	022505	0000				
1137	006574	022505	0000				
1138	006604	031440	020052	040502	XMES14:	.ASCIZ	"%OVER 32 BAD SECTORS RECORDED,PACK NOT ACCEPTABLE%"
1139	006612	020104	042523	052103			
1140	006620	051117	020123	042522			
1141	006626	050053	047503	042103			
1142	006634	050054	047503	042103			

1143	006642	047516	020124	041501			
1144	006642	047516	020124	041101			
1145	006656	042514	006045				
1146	006662	051045	052105	044522	XMES15:	.ASCIZ	"%RETRIEVING THE BAD SPOT FILE FAILS-- DROP MODULE%"
1147	006670	053105	047111	020107			
1148	006711	020104	020105	040504			
1149	006711	020104	020105	040504			
1150	006712	043040	046111	020105			
1151	006720	040506	046111	026523			
1152	006725	020050	051107	040511			
1153	006725	046440	042107	046125			
1154	006742	022505	0000				
1155	006746	006746					
1156	006756	006746					
1157	006756	006746					
1158	006752	006202					
1159	006754	177777					
1160	006756	006746					
1161	006760	177777					
1162	006762	006257					
1163	006764	177777					
1164	006765	006365					
1165	006770	006344					
1166	006772	177777					
1167	006774	006375					
1168	006776	006344					
1169	007000	177777					
1170	007000	006415					
1171	007004	006344					
1172	007006	177777					
1173	007010	177777					
1174	007012	177777					
1175	007014	006447					
1176	007016	177777					
1177	007016	177777					
1178	007020	006471					
1179	007022	177777					
1180	007024	006317					
1181	007026	007061					
1182	007030	006331					
1183	007030	006331					
1184	007032	177777					
1185	007034	006317					
1186	007036	007061					
1187	007040	006517					
1188	007042	177777					
1189	007044	006577					
1190	007046	177777					
1191	007050	006662					
1192	007052	177777					
1193	007054	000005					
1194	007061	0000					
1195	007061	0000					
1196	007062	0000					
1197	007064	007064					
1198	007064	0000					

NEED DO NOT DELETE FROM CODE

,
-EVE
-END

[illegible]

[illegible][illegible]

ZERN = 000552R 362# 793
= 007104R 373# 446 465 566 577 896 1155# 1194# 1197# 1213#

• ARS. 000000 000
007104 001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

XRMADO,XRMADO/SOL/CRF:SYM=DDXCOM,XRMADO
RUN-TIME: 2 3 .5 SECONDS
RUN-TIME RATIO: 28/6=4.5
CORE USED: 7K (13 PAGES)

.REM 8

IDENTIFICATION

PRODUCT CODE: AC-E968C-MC
PRODUCT NAME: CXRMBCO RH11/RM03 DUAL PORT MOD
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

RMB IS AN IOMOD THAT EXERCISES RM03/RM02 DISK DRIVES ON AN RH11 CONTROLLER. IT EXERCISES THE DRIVES BY DOING WRITES, WRITE-CHECKS, READS, AND IN-CORE COMPARISONS. ALL ERRORS DETECTED ARE REPORTED ON THE CONSOLE TTY.

2. REQUIREMENTS

HARDWARE: 1 TO 8 RM03/RM02'S WITH TWO RH11 CONTROLLERS

STORAGE: RMB REQUIRES:
1. DECIMAL WORDS: 1793
2. OCTAL WORDS: 03401
3. OCTAL BYTES: 7002

3. PASS DEFINITION

ONE PASS OF THE RMB MODULE CONSISTS OF 300 CYCLES OF THE BASIC TEST SEQUENCE. A-PORT DOES A WRITE, WRITE-CHECK, READ, DATA COMPARE ON THE CURRENT SECTOR (CALLED BLK THROUGHOUT PROGRAM) AND THEN WRITES BLK 0 WITH BIT 4 SET IN THE FLAG WORD (THE FIRST WORD OF BLOCK ZERO), AND THE SECOND WORD WITH THE CURRENT BLK. A-PORT THEN SITS IN A LOOP, PERIODICALLY READING BLK 0 TO SEE IF B-PORT HAS MODIFIED THE FLAG. B-PORT SITS IN A LOOP WAITING FOR THE FLAG IN BLK ZERO TO BE WRITTEN WITH BIT 4 SET. WHEN IT IS B-PORT PICKS THE CURRENT BLK FROM THIS DATA READ FROM BLK 0 AND GOES TO THAT SECTOR AND READS THE DATA THAT A-PORT JUST WROTE. THEN B-PORT WRITES THE DATA BACK AND WRITE CHECKS IT. NEXT IT RE-WRITES BLK 0 WITH BIT 4 CLEARED AND WITH BIT 2 SET, INDICATING TO A-PORT THAT HE IS DONE. A-PORT AFTER READING BLK 0 AND SEEING THE FLAGS REVERSED, RE-READS THE DATA WHICH IT HAD WRITTEN AND AGAIN DOES AN IN-CORE COMPARE. THIS VERIFIES THE ABILITY OF A-PORT TO RE-READ DATA WHICH IT HAD ORIGINALLY WROTE BUT THAT HAD BEEN READ AND RE-WRITTEN BY B-PORT. B-PORT DOES NOT DO ANY IN CORE COMPARISONS.

B PORT SHOULD NEVER ENCOUNTER A BAD BLK BECAUSE IT ONLY USES BLK'S WHICH A-PORT HAS ALREADY SUCCESSFULLY USED. THEREFORE, WHEN A-PORT GETS 4 ERRORS WHILE ATTEMPTING A SEQUENCE ON ONE BLK, IT MOVES TO THE NEXT BLK, NEVER LETTING B-PORT EVEN TRY THIS BAD BLK. IF B-PORT GETS 4 ERRORS ON A BLK, THAT DRIVE IS DROPPED BECAUSE IT IS MOST LIKELY FAULTY SINCE A-PORT WAS JUST ABLE TO SUCCESSFULLY USE THIS BLK.

LOCATION 206 THROUGH 244 CONTAIN ROOM FOR 16 BAD BLKS. ON ERROR TYPEOUTS WHICH DUMP THE RH REGISTERS, THE LAST ITEM TYPED IN THE TABLE IS THE CURRENT BLK NUMBER. ENTER THIS INTO THE BAD BLK TABLE TO AVOID ERRORS FROM KNOWN MEDIA BAD SPOTS. THIS TABLE ONLY MAKES SENSE ON THE A-PORT SIDE SINCE B-PORT

ALWAYS GETS ITS BLK ADDRESSES FROM A-PORT. IF YOU MODIFY THE WRITE BUFFER SIZE, YOU MUST ADD SECTORS TO THE BAD BLK TABLE TO AVOID THE ERRORS. FOR EXAMPLE, IF BLK 3474 IS BAD AND YOU DOUBLE THE WRITE TRANSFER SIZE TO 1000 OCTAL BYTES YOU MUST ADD BLK 3473 TO THE TABLE SO THE PROGRAM DOES NOT START A TRANSFER THAT WILL EXTEND ON INTO THE KNOWN BAD BLK.

B-PORT MUST HAVE SR1 BIT 4 SET. ***** SR1 = 20 (OCTAL) *****
THIS MODULE IS A DUAL-PORT
MODULE TEST, AND ONLY USES THE FIRST 77777 BLKS
OF THE PACK. IF YOU WANT TO VERIFY THE COMPLETE MEDIUM, YOU
SHOULD RUN RMA MODULE, THE NORMAL SINGLE PORT TEST.

4. EXECUTION TIME

ONE PASS OF RMB RUNNING ALONE ON A PDP-11/70 TAKES APPROXIMATELY ONE MINUTE.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DEVADR: 176700, VECTOR: 254, BR1: 5, DEVCNT: 1

REQUIRED PARAMETERS:

NONE

6. DEVICE/OPTION SETUP

MAKE CERTAIN THAT ALL DRIVES ARE POWERED UP, WRITE ENABLED, AND READY

THIS MODULE ALSO SUPPORTS RP04/5/6 ON THE SAME MASSBUS CONTROLLER.
HOWEVER, THIS MODULE IS NOT USED TO EXERCISE RP04/5/6 ALONE.

7. OPERATION OPTIONS

SR1 BIT2 SET(1):
COUNT DATA LATE ERRORS BUT DO NOT TYPE THEM OUT

SR1 BIT2 CLEAR(0):
TYPE OUT DATA LATE ERRORS AND COUNT THEM

SR1 BIT4 SET (1) ;B-PORT PROGRAM *****SR1 = 20 (OCTAL) *****

SR1 BIT15 SET (1):
32 REGISTER OPTION ON RH70

SR1 BIT15 CLEAR (0):
22 REGISTER OPTION ON RH70

8. NON-STANDARD PRINTOUTS

- A. MOST PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN THE DEC/X11 DOCUMENT
- B. ERROR MESSAGES DUMP THE CONTENTS OF THE 20 RH11/RM03 REGISTERS IN THE FOLLOWING ORDER:

RMCS1	RMWC	RMBA	RMDA	RMCS2	RMD5	RMER1	RMA5
RMLA	RMD8	RMMR1	RMDI	RMSN	RMOF	RMDC	RMMR
RMMR2	RMER2	RMEC1	RMEC2	BLK			

9. DUAL PORT SETUP:

TO RUN A DUAL PORT SYSTEM, SR1 HAS TO BE MODIFIED TO INDICATE TO THE MODULE, WHICH PORT THE MODULE IS LOCATED ON. SEE SECTION 7. FOR SR1 OPTIONS.

THE CONTROLLER SELECT SWITCH ON THE RM03 MUST BE IN THE A/B POSITION. THIS SWITCH IS ACTIVATED WHEN THE DRIVE IS CYCLED UP. IF SWITCH WAS NOT IN THIS POSITION WHEN DRIVE WAS POWERED UP, THE FOLLOWING STEPS MUST BE TAKEN. PLACE THE SWITCH IN THE A/B POSITION, DISABLE THE DRIVE, (USING THE DISABLE SWITCH ON THE DRIVE), THEN ENABLE THE DRIVE WITH THE SAME SWITCH. THIS WILL PUT THE RM03 IN THE DUAL PORT MODE.

10. BAD SPOT FILE

TOTAL 16 BAD BLOCKS CAN BE RETRIEVED FROM PORT A. THE TABLE LABELED "BADSP" IS SET UP TO RETRIEVE ALL BAD SPOT FILES FROM ALL DRIVES ASSIGNED IN THE BIT MAP "DVID1". (CYLINDER 822, TRACK 4, SECTOR 0 - MANUFACTUR BAD SPOT FILE) (CYLINDER 822, TRACK 4, SECTOR 12 - USER BAD SPOT FILE) LOCATION 1722 MUST BE SET TO 10 TO SEARCH USER BAD SPOT FILE

```

000000' LOMODX <RMBC >,176700,254,5,0,0,300,145,RBUF,256.,256.
000000' MODULE 150000,RMBC ,176700,254,5,0,0,300,145,RBUF,256.,256.
; .TITLE RMBC DEC/X11 SYSTEM EXERCISER MODULE
DDXCOM VERSION 6 23-MAY-78
; .LIST BIN
;*****
000000' BEGIN:
000000' 046522 041502 040 MODNAM: .ASCII /RMBC / ;MODULE NAME.
000005' 000 XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF RBUF USAGE
000006' 176700 ADDR: 176700+0 ;1ST DEVICE ADDR.
000010' 000254 VECTOR: 254+0 ;1ST DEVICE VECTOR.
000012' 240 BR1: .BYTE PRY5+0 ;1ST BR LEVEL.
000013' 000 BR2: .BYTE PRY0+0 ;2ND BR LEVEL.
000014' 000001 DVID1: 0+1 ;DEVICE INDICATOR 1.
000016' 000000 SR1: OPEN ;SWITCH REGISTER 1
000020' 000000 SR2: OPEN ;SWITCH REGISTER 2
000022' 000000 SR3: OPEN ;SWITCH REGISTER 3
000024' 000000 SR4: OPEN ;SWITCH REGISTER 4
;*****
000026' 150000 STAT: 150000 ;STATUS WORD.
000030' 001604' INIT: START ;MODULE START ADDR.
000032' 000252' SPOINT: MODSP ;MODULE STACK POINTER.
000034' 000000 PASCNT: 0 ;PASS COUNTER.
000036' 000300 ICONT: 300 ;# OF ITERATIONS PER PASS=300
000040' 000000 ICGUNT: 0 ;LOC TO COUNT ITERATIONS
000042' 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000044' 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000046' 000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000050' 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000052' 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
000054' 000000 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
000056' 000000 CONFIG:
000056' 000000 RES1: 0 ;RESERVED FOR MONITOR USE
000060' 000000 RES2: 0 ;RESERVED FOR MONITOR USE
000062' 000000 SVR0: OPEN ;LOC TO SAVE R0.
000064' 000000 SVR1: OPEN ;LOC TO SAVE R1.
000066' 000000 SVR2: OPEN ;LOC TO SAVE R2.
000070' 000000 SVR3: OPEN ;LOC TO SAVE R3.
000072' 000000 SVR4: OPEN ;LOC TO SAVE R4.
000074' 000000 SVR5: OPEN ;LOC TO SAVE R5.
000076' 000000 SVR6: OPEN ;LOC TO SAVE R6.
000100' 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
000102' SBADR: ;ADDR OF GOOD DATA, OR
000102' 000000 ACSK: OPEN ;CONTENTS OF CSR.
000104' WASAUK: ;ADDR OF BAD DATA, OR
000104' 000000 ASTA1: OPEN ;STATUS REG CONTENTS.
000106' ERRTYP: ;TYPE OF ERROR
000106' 000000 ASB: OPEN ;EXPECTED DATA.
000110' 000000 AWA5: OPEN ;ACTUAL DATA.
000112' 002236' RSTR1: RSTR1 ;RESTART ADDRESS AFTER END OF PASS
000114' 000000 WDO1: OPEN ;WORDS TO MEMORY PER ITERATION
000116' 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
000120' 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
000122' 000145 IDNUM: 145 ;MODULE IDENTIFICATION NUMBER=145
000124' 000444' RBUFA: RBUF ;READ BUFFER VIRTUAL ADDRESS
000126' 000000 RBUFA: OPEN ;READ BUFFER PHYSICAL ADDRESS

```

```

DO NOT CHANGE THE ORDER OF THE NEXT 4 LOCATIONS
;NEEDED FOR MAP22 ROUTINE
PA18: 0
XMEM: 0
PA22: 0
EA22: 0
BLK: 0
DSKADR: 0
CYL: 0
TRACK: 0
FLAG: 0
DVICE: 0
DRIVE: 0
UNITNO: 0
CNT: 0
CNT1: 0

```

```

DLTCNT: 0
FUNC: 0
TIMER: 0
ZERO: 0
FERADR: 0
CLK: 0
TRY: 0
MIXDRV: 0 ;MIX DRIVE FLAG,=-1,IF NOT RM03,RM02
BADSEC: 0 ;HARDWARE DETECT BAD SECTOR
RBUF: .BLKW 256.

TABLE:
S
S+2
S+4
S+6
S+10
S+12
S+14
S+16
S+20
S+22
S+24
S+26
S+30
S+32
S+34
S+36
S+40
S+42
S+44
S+46
BLK
177777
RMCS1: 0
RMWC: 0
RMSA: 0
RMDA: 0
RMCS2: 0
RMD5: 0
RMER1: 0
RMAS: 0
RMLA: 0
RMD5: 0
RMER1: 0
RMUT: 0
RMSN: 0
RMOF: 0
RMDC: 0
RMHK: 0
RMER2: 0
RMEC1: 0
RMEC2: 0
RMBAE: 0
RMCS3: 0

```

366	001574'	000432'		XFERAD: FERADH
367	001576'	000416'		XFERCI: CNT
368	001600'	000376'		XBLK: BLK
369	001602'	177777		177777

370	001604'	012767	000400	176302	START:	MOV	#256.,WD10	;256 WORDS TO MEM/ITERATION
371	001612'	012767	000400	176276		MOV	#256.,WDPR	;256 WORDS FROM MEM/ITERATION
372	001620'	012767	000003	176272		MOV	#3,INTR	;3 INTERRUPTS/ITERATION
373	001626'	016767	176162	176554		MOV	DVID1,DVICE	;GET DRIVES TO TEST
374	001634'	016706	176172			MOV	SPOINT,R6	;INITIATE STACK POINT
375	001640'	012767	000001	176530		MOV	#1,BLK	;START AT BLOCK(SECTOR) 1
376	001646'	012767	000001	176532		MOV	#1,FLAG	;SET 1ST TIME FLAG BIT
377	001654'	012767	177777	176532		MOV	#-1,UNITNO	;INITIATE UNIT NUMBER
378	001662'	004767	004102			JSR	PC,SETUP	
379	001666'	004767	002542			JSR	PC,PICKDR	;INITIATE UUT
380	001672'	000240				MOV		;DUMMY ERROR RETURN
381	001674'	004767	004332			JSR	PC,SETUP2	;SETUP PSEL BIT
382	001700'	004767	003744			JSR	PC,REZET	;CLEAR THE RH
383	001704'	104415	000000'	000124'		GETPAS,BEGIN, RBUFVA		;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
384	001712'	005067	176500			CLR	CNT	;RESET END-OF-PASS COUNTER
385	001716'	032767	000020	176072		BIT	#20,SR1	;B-PORT 7,
386	001724'	001161				BNE	LOOP1	;THEN,DON'T HAVE TO RETRIEVE THE BAD SPOT FILE
387	001726'	012767	177777	176460		MOV	#-1,UNITNO	;RESET UNIT NUMBER
388	001734'	012701	000252'			MOV	#BADSPTR1	;TABLE ENTRY
389	001740'	012702	000020			MOV	#16.,R2	;16 WORDS
390	001744'	005021			1S:	CLR	(R1)+	;CLEAR THE TABLE
391	001746'	005302				DEC	R2	;DECREMENT ONE WORD
392	001750'	001375				BNE	1S	;BRANCH IF NOT DONE
393	001752'	004767	002456		RES2X:	JSR	PC,PICKDR	;PICK UP THE FIRST ON LINE DRIVE
394	001756'	000520				BR	10S	;BRANCH IF ALL DRIVES ARE ACCESSED
395	001760'	005067	176452			CLR	TRY	;CLEAR RETRY COUNTER
396	001764'	012767	000770	176414		MOV	#770,FLAG	;SPECIAL FLAG TAG FOR RETRIEVE BAD SPOT FILE
397	001772'	012767	001466	176402		MOV	#822.,CYL	;CYL 822
398	002000'	112767	000004	176373		MOVB	#4,DSKADR+1	;TRACK 4
399	002006'	112767	000000	176364		MOVB	#0,DSKADR	;SEC 0 (SEC 12 USER)
400	002014'	004567	003440			JSR	R5,READY	;ACCESS THE DRIVE
401	002020'	000402				BR	+6	;BRANCH IF READY
402	002022'	004767	003174			JSR	PC,NOTRDY	;WAIT IF NOT READY
403	002026'	005767	176406			TSI	MIXDV	;EXIT,IF NOT RMO3 OR RMO2
404	002032'	100471				BMI	9S	;EXIT
405	002034'	004567	001222			JSR	R5,READ	;READ THE BAD SPOT FILE
406	002040'	012701	000444'			MOV	#RBUF,R1	;INPUT BUFFER
407	002044'	010102				MOV	R1,R2	;END ADDRESS
408	002046'	062702	001000			ADD	#512.,R2	;LAST LOCATION OF THE INPUT BUFFER
409	002052'	062701	000010			ADD	#10,R1	;BAD SPOT FILE STARTS FROM 5TH WORDS
410	002056'	012703	000252'			MOV	#BADSPTR3	;BADSPTR RELOCATABLE, TABLE ADDRESS
411	002062'	010304				MOV	R3,R4	;LAST LOCATION OF THE TABLE
412	002064'	062704	000040			ADD	#32.,R4	;16 WORDS,
413	002070'	022711	000632		1S:	CMP	#410.,(R1)	;BLOCK OVER CYL 410, THEN DON'T WORRY
414	002074'	101433				BLUS	0S	;INCREMENT THE POINTER
415	002076'	005067	176274			CLR	BLK	;INITIATE BLOCK COUNTER
416	002102'	011100				MOV	(R1),R0	;CYLINDER ADDRESS
417	002104'	001405				BEQ	3S	;BRANCH , IF CYL 0
418	002106'	062767	000240	176262	2S:	ADD	#32.*5,BLK	;BLOCK # FOR ONE CYLINDER
419	002114'	005300				DEC	R0	;DECREMENT TEMP COUNT
420	002116'	001373				BNE	2S	;LOOP IF NOT ZERO
421	002120'	116100	000003		3S:	MOVB	3(R1),R0	;TRACK NUMBER
422	002124'	001405				BEQ	5S	;BRANCH IF TRACK 0
423	002126'	062767	000040	176242	4S:	ADD	#32.,BLK	;ADJUST BLOCK FOR ONE TRACK
424	002134'	005300				DEC	R0	;BRANCH IF NOT DONE
425	002136'	001373				BNE	4S	;

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426 002140' 116100 000002 5s: MOV B 2(R1),R0 ;SECTION NUMBER
427 002144' 060067 176226 ADD R0,BLK ;UPDATE SECTOR # FOR BLOCK COUNT
428 002150' 005713 TST (R3) ;ENTRY FOR THE TABLE ?
429 002152' 001016 BNE B6 ;BRANCH IF NOT
430 002154' 016713 176216 MOV BLK,(R3) ;LOAD INTO TABLE
431 002160' 062703 000002 ADD #2,R3 ;UPDATE TABLE POINTER
432 002164' 062701 000004 6s: ADD #4,R1 ;UPDATE THE BAD SPOT FILE POINTER
433 002170' 020102 7s: CMP R1,R2 ;END OF FILE
434 002172' 103011 BHS B7 ;BRANCH IF IT IS
435 002174' 020304 CMP R3,R4 ;END OF TABLE ?
436 002176' 103734 BLO B8 ;BRANCH IF NOT
437 002200' 104403 006766' 000000' MSGNS,MES15,BEGIN
438 002206' 000403 BR B9 ;DUN'T DON ANYTHING
439 002210' 062703 000002 8s: ADD #2,R3 ;UPDATE THE TABLE ENTRY
440 002214' 000765 BR B7 ;CHECK THE TABLE AND FILE ENTRY
441 002216' 000655 9s: BR #2,R3 ;TO NEXT DRIVE
442 002220' 012767 000001 176150 10s: MOV #1,BLK ;RESET BLOCK NUMBER
443 002226' 012767 000001 176152 MOV #1,FLAG ;RESTORE FIRST TIME FLAG
444 ;COUNTINUE
445 002234' 000410 BR RSTRT1 ;+ SUPPORT - DT03
446
447 002236' 005767 176156 RSTRT1: TST CNT1 ;+ / SUPPORT
448 002242' 001005 BNE RSTRT1 ;+ FOR
449 002244' 005767 176146 TST CNT ;+ DT03
450 002250' 001002 BNE RSTRT1 ;+ BUS
451 002252' 000167 177326 JMP SIART ;+ SWITCH
452 ;+
453 002256' 104415 000000' 000124' RSTRT1: GETPAS,BEGIN, RBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
454 002264' 005067 176126 CLR CNT ;CLEAR THE LOCATE COUNTER
455 002270' 012767 177777 176116 LOOP1: MOV #1,UNITNO ;PRE-SET UNIT NUMBER
456 002276' 104414 000000' GMBUFS, BEGIN ;GET WRITE BUFFER INFORMATION
457 002302' 004767 002244 JSR PC,PICKBK ;GO PICK A BLK
458 002306' LOOP2: JSR PC,PICKDR ;GO PICK A DRIVE
459 002306' 004767 002122 3s: BR B9 ;RETURNS HERE IF ALL DRIVES DONE
460 002312' 000405 CLR TRY ;ELSE CLR RE TRY COUNT
461 002314' 005067 176116 JSR PC,CYCLE ;GO DO A CYCLE ON THIS DRIVE
462 002320' 004767 000034 BR LOOP2 ;DO IT TO NEXT DRIVE
463 002324' 000770
464
465 002326' 042767 000001 176052 1s: BIC #1,FLAG ;CLEAR FIRST TIME FLAG
466 002334' 005767 176050 TST DVICE ;ANYBODY LEFT TO CHECK?
467 002340' 001002 BNE B2 ;OR IF YES
468 002342' 104410 000000' ENDS,BEGIN ;
469 002346' 005267 176046 2s: INC CNT1 ;+BUMP COUNTER FOR DT03
470 002352' 104413 000000' ENDTs,BEGIN ;SIGNAL END OF ITERATION.
471 ;MONITOR SHALL TEST END OF PASS
472 002356' 000744 BR LOOP1

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473
474 002360' 032767 000020 175430 CYCLE: BIT #20,SRI ;B-PORT?
475 002366' 001114 BNE CYCLEB ;IF B IS 0, ELSE DO A-PORT
476 002370' 004567 003064 JSR R5,READY ;READY ?
477 002374' 000402 BR CYA ;YES
478 002376' 004767 002620 JSR PC,NOTRDY ;NO
479 002402' 052767 000004 175776 CYA: BIS #4,FLAG ;SET THE A-PORT FLAG
480 002410' 004767 001636 JSR PC,BLKADR ;CONVERT BLK TO DISK ADDR
481 002414' 004567 000452 JSR R5,WRITE ;GO WRITE A BLOCK
482 002420' 005767 176016 TST BADSEC ;FOUND BAD SPOT ?
483 002424' 001401 BEQ #4 ;BRANCH IF NOT
484 002426' 000207 RTS PC ;OTHERWISE EXIT
485 002430' 004567 000532 JSR R5,WRICK ;GO DO WRITE CHECK
486 002434' 004567 000622 JSR R5,READ ;GO READ A BLOCK
487 002440' 104412 000000' 000126' CDATAs,BEGIN,RBUFA ;REQUEST FOR MONITOR TO CHECK DATA
488 002446' 002450' .+2 ; IF ERROR, CONTINUE
489
490 ;NOW UPDATE BLOCK 0 FOR B-PORT'S INFORMATION
491
492 002450' 004767 002154 JSR PC,CLRRB ;CLEAR THE READ BUFFER
493 002454' 016767 175726 MOV FLAG,RBUF ;PUT INFO IN FIRST WORD
494 002462' 016767 175710 175756 MOV BLK,RBUF+2 ;PUT CURRENT ADDR IN NAT
495 002470' 004567 001052 JSR R5,WRITE ;GO UPDATE BLOCK 0
496 002474' 012767 000000 175724 MOV #0,TIMER ;INIT WAIT LOOP
497 002502' 012777 000013 177010 1s: MOV #13,PMCS1 ;GIVE THE DRIVE TO B
498 002510' 005367 175712 2s: DEC TIMER
499 002514' 001006 BNE B3
500 002516' 004767 002126 JSR PC,DROP
501 002522' 104403 000000' 006744' MSGNS,BEGIN,FORTHG ;ASCII MESSAGE CALL WITH COMMON HEADER
502 002530' 000207 RTS PC
503 002532' 012700 000002 3s: MOV #2,R0
504 002536' 4s:
505 002536' 104407 000000' BREAKs,BEGIN ;TEMPORARY RETURN TO MONITOR....
506 002542' 104407 000000' BREAKs,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
507 002546' 005300 DEC R0
508 002550' 001372 BNE B4
509 002552' 004567 002702 JSR R5,READY ;READY ?
510 002556' 000402 BR B5 ;YES
511 002560' 004767 002436 JSR PC,NOTRDY ;NO
512 002564' 004567 001052 5s: JSR R5,RD00 ;GO READ BLOCK 0
513 002570' 000240 NOP ;CHANGE TO BR IF ONLY SINGLE PORT
514 002572' 032767 000002 175644 BIT #2,RRUF ;HAS B UPDATED BLK 0 YET?
515 002600' 001740 BEQ B6 ;BR BACK IF NOT
516 002602' 004567 000454 JSR R5,READ ;GO READ THE DATA B WROTE
517 002606' 104412 000000' 000126' CDATAs,BEGIN,RBUFA ;REQUEST FOR MONITOR TO CHECK DATA
518 002614' 002616' .+2 ; IF ERROR, CONTINUE
519 002616' 000207 RTS PC

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520
521 002620' 012767 000000 175600 CYCLEB: MOV #0,TIMER
522 002626' 004567 002626 JSR R5,READY ;READY ?
523 002632' 000402 BR C1B ;YES
524 002634' 004767 002362 JSR PC,NOTRDY ;NO
525 002640' 004567 000776 JSR R5,RD00 ;GO SEE IF A HAS DONE YET
526 002644' 032767 000001 175534 BIT #1,FLAG ;IS THIS THE FIRST TIME SINCE START?
527 002652' 001410 BEQ 15 ;BR IF NO, SKIP THIS CHECK
528 002654' 032767 000004 175562 B1I #4,RBUF ;HAS A WRITTEN THIS BLOCK?
529 002662' 001410 BEQ 25 ;BR IF NO
530 002664' 032767 000001 175552 BIT #1,RBUF ;HAS A SET THE FIRST TIME FLAG?
531 002672' 001404 BEQ 25 ;BR IF NOT, MUST WAIT
532 002674' 032767 000002 175542 1s: BIT #2,RBUF ;HAS A WRITTEN THIS BLOCK SINCE B DID?
533 002702' 001432 BEQ 35 ;BR IF SO, ELSE
534 002704' 012777 000013 176606 2s: MOV #13,ARMCS1 ;GIVE PORT TO A
535 002712' 012700 000002 MOV #2,R0
536 002716' 4s: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
537 002722' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
538 002722' 104407 000000' DEC R0
539 002726' 005300 BNE 45
540 002730' 001372 175470 DEC TIMER
541 002732' 005367 BNE 65
542 002736' 001006 JSR PC,DRUP
543 002740' 004767 001704 MSGNS,BEGIN,PORTING ;ASCII MESSAGE CALL WITH COMMON HEADER
544 002744' 104403 000000' 006744' RTS PC
545 002752' 000207 JSR R5,READY ;READY ?
546 002754' 004567 002500 6s: BR C1B ;YES
547 002760' 000727 JSR PC,NOTRDY ;NO
548 002762' 004767 002234 BR CYB ;TRY AGAIN
549 002766' 000724
550
551 002770' 016767 175452 175400 3s: MOV RBUF+2,BLK ;GET THE CURRENT BLK
552 002776' 004767 001250 JSR PC,BLKADR ;GENERATE DISK ADDR FROM IT
553 003002' 004567 000254 JSR R5,READ ;GO READ WHAT A WROTE
554 003006' 004567 000344 JSR R5,WRITEB ;GO WRITE IT BACK OUT
555 003012' 004567 000434 JSR R5,WRITCB ;GO WRITE CHECK IT
556 003016' 004767 001606 JSR PC,CLRHB ;CLEAR BUFFER
557 003022' 052767 000002 175356 BIS #2,FLAG ;SET BIT SAYING B'S DONE
558 003030' 016767 175352 175406 MOV FLAG,RBUF ;PUT INFO INTO RBUF
559 003036' 004567 000504 JSR R5,RTIO ;GO WRITE IF FOR A TO SEE
560 003042' 012777 000013 176450 MOV #13,ARMCS1 ;GIVE DRIVE TO A
561 003050' 012700 000002 MOV #2,R0
562 003054' 5s: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
563 003054' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
564 003060' 104407 000000' DEC R0
565 003064' 005300 BNE 55
566 003066' 001372 RTS PC
567 003070' 000207

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568
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579 003072' 012767 000161 175324 WRITE: MOV #161,FUNC ; LOAD WRITE FUNCTION
580 003100' 012767 003072' 175324 MOV #WRITE,FERADR ;SAVE WHERE WE WERE
581 003106' 016746 175030 MOV #BUFSZ,-(SP) ;GET WRITE SIZE
582 003112' 005416 NEG (SP) ;NEGATE IT
583 003114' 012677 176402 MOV (SP)+,ARMWC ; LOAD WORD COUNT
584 003120' 016777 175010 176376 MOV #BUFPA,ARMBA ; LOAD BUFFER ADDRESS
585 003126' 016777 175246 176372 MOV DSKADR,ARMDA ; LOAD DISK ADDRESS
586 003134' 016777 175242 176412 MOV CYL,ARMDC ; LOAD CYLINDER ADDRESS
587
588 003142' 016700 174770 LINEUP #BUFEA ; LINE UP EA BITS FOR RHCS1
589 003146' 006300 MOV #BUFEA,R0 ; GET EXTENDED MEMORY BITS
590 003150' 006300 ASL R0 ; SHIFT 4 PLACES TO THE LEFT
591 003152' 006300 ASL R0 ; TO LINE UP WITH RHCS1
592 003154' 006300 ASL R0 ;
593 003156' 010067 175206 MOV R0,XMEM ; SAVE THE SHIFTED BITS
594 003162' 000167 000556 JMP GO ; CONTINUE
595 003166' 012767 000151 175230 WRITCK: MOV #151,FUNC ; LOAD WRITE-CHECK FUNCTION
596 003174' 012767 003166' 175230 MOV #WRITCK,FERADR ;SAVE WHERE WE WERE
597 003202' 016746 174734 MOV #BUFSZ,-(SP) ;GET WRITE SIZE
598 003206' 005416 NEG (SP) ;NEGATE IT
599 003210' 012677 176306 MOV (SP)+,ARMWC ; LOAD WORD COUNT
600 003214' 016777 174714 176302 MOV #BUFPA,ARMBA ; LOAD BUFFER ADDRESS
601 003222' 016777 175152 176276 MOV DSKADR,ARMDA ; LOAD DISK ADDRESS
602 003230' 016777 175146 176316 MOV CYL,ARMDC ; LOAD CYLINDER ADDRESS
603
604 003236' 016700 174674 LINEUP #BUFEA ; LINE UP EA BITS FOR RHCS1
605 003242' 006300 MOV #BUFEA,R0 ; GET EXTENDED MEMORY BITS
606 003244' 006300 ASL R0 ; SHIFT 4 PLACES TO THE LEFT
607 003246' 006300 ASL R0 ; TO LINE UP WITH RHCS1
608 003250' 006300 ASL R0 ;
609 003252' 010067 175112 MOV R0,XMEM ; SAVE THE SHIFTED BITS
610 003256' 000167 000462 JMP GO ; CONTINUE
611 003262' 012767 000171 175134 READ: MOV #171,FUNC ; LOAD READ FUNCTION
612 003270' 012767 003262' 175134 MOV #READ,FERADR ;SAVE WHERE WE WERE
613 003276' 016746 174630 MOV #BUFSZ,-(SP) ;GET READ SIZE
614 003302' 005416 NEG (SP) ;NEGATE IT
615 003304' 012677 176212 MOV (SP)+,ARMWC ; LOAD WORD COUNT
616 003310' 016777 174612 176206 MOV #BUFPA,ARMBA ; LOAD BUFFER ADDRESS
617 003316' 016777 175056 176202 MOV DSKADR,ARMDA ; LOAD DISK ADDRESS
618 003324' 016777 175052 176222 MOV CYL,ARMDC ; LOAD CYLINDER ADDRESS
619
620 003332' 016700 174572 LINEUP #BUFEA ; LINE UP EA BITS FOR RHCS1
621 003336' 006300 MOV #BUFEA,R0 ; GET EXTENDED MEMORY BITS
622 003340' 006300 ASL R0 ; SHIFT 4 PLACES TO THE LEFT
623 003342' 006300 ASL R0 ; TO LINE UP WITH RHCS1

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624 003344' 006300      ASL      R0      ;
625 003346' 010067      MOV      R0,XMEM  ; SAVE THE SHIFTED BITS
626 003352' 000167      JMP      GO      ; CONTINUE
627 003356' 012767      MOV      #161,FUNC ; LOAD WRITE FUNCTION
628 003364' 012767      MOV      #WRITEB,FERADR ; LOAD WRITE FUNCTION
629 003372' 016746      MOV      RBUF$2,-(SP) ; GET WRITE SIZE
630 003376' 005416      NEG      (SP)      ; NEGATE IT
631 003400' 012677      MOV      (SP)+,RMC ; LOAD WORD COUNT
632 003404' 016777      MOV      RBU$PA,RMBA ; LOAD BUFFER ADDRESS
633 003412' 016777      MOV      DSKADR,RMDA ; LOAD DISK ADDRESS
634 003420' 016777      MOV      CYL,RMDC  ; LOAD CYLINDER ADDRESS
635      ; LINEUP RBU$EA      ; LINE UP EA BITS FOR RHCS1
636 003426' 016700      MOV      RBU$EA,R0  ; GET EXTENDED MEMORY BITS
637 003432' 006300      ASL      R0      ; SHIFT 4 PLACES TO THE LEFT
638 003434' 006300      ASL      R0      ; TO LINE UP WITH RHCS1
639 003436' 006300      ASL      R0      ;
640 003440' 006300      ASL      R0      ;
641 003442' 010067      MOV      R0,XMEM  ; SAVE THE SHIFTED BITS
642 003446' 000167      JMP      GO      ; CONTINUE
643 003452' 012767      MOV      #151,FUNC ; LOAD WRITE-CHECK FUNCTION
644 003460' 012767      MOV      #WRITCB,FERADR ; LOAD WRITE-CHECK FUNCTION
645 003466' 016746      MOV      RBUF$2,-(SP) ; GET WRITE SIZE
646 003472' 005416      NEG      (SP)      ; NEGATE IT
647 003474' 012677      MOV      (SP)+,RMC ; LOAD WORD COUNT
648 003500' 016777      MOV      RBU$PA,RMBA ; LOAD BUFFER ADDRESS
649 003506' 016777      MOV      DSKADR,RMDA ; LOAD DISK ADDRESS
650 003514' 016777      MOV      CYL,RMDC  ; LOAD CYLINDER ADDRESS
651      ; LINEUP RBU$EA      ; LINE UP EA BITS FOR RHCS1
652 003522' 016700      MOV      RBU$EA,R0  ; GET EXTENDED MEMORY BITS
653 003526' 006300      ASL      R0      ; SHIFT 4 PLACES TO THE LEFT
654 003530' 006300      ASL      R0      ; TO LINE UP WITH RHCS1
655 003532' 006300      ASL      R0      ;
656 003534' 006300      ASL      R0      ;
657 003536' 010067      MOV      R0,XMEM  ; SAVE THE SHIFTED BITS
658 003542' 000167      JMP      GO      ; CONTINUE
659 003546' 012767      MOV      #161,FUNC ; LOAD WRITE FUNCTION
660 003554' 012767      MOV      #WRT00,FERADR ; LOAD WRITE FUNCTION
661 003562' 016746      MOV      RBUF$2,-(SP) ; GET WRITE SIZE
662 003566' 005416      NEG      (SP)      ; NEGATE IT
663 003570' 012677      MOV      (SP)+,RMC ; LOAD WORD COUNT
664 003574' 016777      MOV      RBU$PA,RMBA ; LOAD BUFFER ADDRESS
665 003602' 012777      MOV      #0,RMDA   ; LOAD DISK ADDRESS
666 003610' 012777      MOV      #0,RMDC   ; LOAD CYLINDER ADDRESS
667      ; LINEUP RBU$EA      ; LINE UP EA BITS FOR RHCS1
668 003616' 016700      MOV      RBU$EA,R0  ; GET EXTENDED MEMORY BITS
669 003622' 006300      ASL      R0      ; SHIFT 4 PLACES TO THE LEFT
670 003624' 006300      ASL      R0      ; TO LINE UP WITH RHCS1
671 003626' 006300      ASL      R0      ;
672 003630' 006300      ASL      R0      ;
673 003632' 010067      MOV      R0,XMEM  ; SAVE THE SHIFTED BITS
674 003636' 000167      JMP      GO      ; CONTINUE
675 003642' 012767      MOV      #171,FUNC ; SET FUNCTION TO A READ
676 003650' 012767      MOV      #RDO0,FERADR ; SET WHERE WE WERE
677 003656' 000741      BR      WRT00R    ; THEN USE WRT00 ROUTINE FOR THE READ
678
679 003660' 016777      MOV      UNITNO,RMC$2 ; LOAD UNIT ADDRESS

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680 003666' 012777      MOV      #11,RMC$1 ; ISSUE A DRIVE CLEAR
681 003674' 000240      NOP      ;*ALL
682 003676' 000240      NOP      ;FOR DRIVE CLEAR TO FINISH
683 003700' 012777      MOV      #23,RMC$1 ; ISSUE A PACK ACK
684 003706' 105777      TSTB     RMC$1
685 003712' 100401      BMI      2$
686 003714' 000774      BR      1$
687 003716' 017746      MOV      #RMAS,-(SP) ; NO, WAIT TILL DONE
688 003722' 012677      MOV      (SP)+,RMAS ; CLEAR AS BIT
689 003726' 012777      MOV      #BIT14,RMC$1 ; CLEAR ANY CONTROLLER ERRORS
690 003734' 012777      MOV      #BIT12,RMDF ; SET BIT FOR 11 FORMAT
691 003742' 000205      RIS      R5      ; RETURN
692
693 003744' 016777      MOV      UNITNO,RMC$2 ; LOAD UNIT SELECT
694 003752' 032767      BIT      #ADDR22,RES1 ; 11/70 MONITOR?
695 003760' 001434      BEQ      1$
696 003762' 017767      MOV      #RMBA,PA18 ; NO
697 003770' 006267      ASR      XMEM      ; GET 18 BIT ADDR
698 003774' 006267      ASR      XMEM      ; SHIFT EA BITS TO POSITION 4,5
699 004000' 006267      ASR      XMEM
700 004004' 006267      ASR      XMEM
701 004010' 104416      MAP22$, BEGIN,PA18 ; GET 22-BIT ADDR FROM 18-BIT ADDR
702 004016' 016777      MOV      #A22,RMBA ; LOAD BA REG
703 004024' 016777      MOV      #EA22,RMB$E ; LOAD BAE REG
704 004032' 042767      BIC      #34,EA22 ; CLEAR UNWANTED BITS
705 004040' 000367      SWAB     EA22      ; LOAD INTO BITS 8,9
706 004044' 016767      MOV      #EA22,XMEM ; LOAD XMEM TO SET INTO FUNCTION CODE
707 004052' 056767      BIS      XMEM,FUNC ; LOAD EXTENDED MEMORY BITS
708 004060' 016777      MOV      #FUNC,RMC$1 ; EXECUTE THE FUNCTION
709 004066' 104400      EXITS,BEGIN ; EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
710 004072' 010046      MOV      R0,-(SP) ; SAVE R0
711 004074' 016700      MOV      UNITNO,R0 ; DRIVE #
712 004100' 116000      MOV      BITTAB(R0),R0 ; DRIVE INDICATOR
713 004104' 017746      MOV      #RMAS,-(SP) ; CLEAR CORRESPONDING
714 004110' 040016      BIC      R0,(SP) ; ATTENTION BIT
715 004112' 012677      MOV      (R6)+,RMAS ; IN THE ATTENTION SUMMARY REGISTER
716 004116' 012600      MOV      (SP)+,R0 ; RESTORE R0
717
718 004120' 000004      PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
719
720
721 004126' 004567      JSR      R5,ERRORS ; GO CHECK FOR ERRORS
722 004132' 000401      BR      2$
723 004134' 000205      RIS      R5      ; ERRORS DETECTED
724      ; OTHERWISE, RETURN OK
725 004136' 005267      INC      TRY      ; COUNT AN ERROR
726 004142' 026727      CMP      TRY,#4 ; TOO MANY FOR THIS CYCLE?
727 004150' 001035      BNE      4$ ; BR IF NOT
728 004152' 104403      MSGNS$,BEGIN,EXCED ; ASCII MESSAGE CALL WITH COMMON HEADER
729 004160' 012605      MOV      (SP)+,R5 ; FOR RETURN WE WANT DO
730 004162' 032767      BIT      #20,SR1
731 004170' 001411      BEQ      3$
732 004172' 004767      JSR      PC,DROP ; ASCII MESSAGE CALL WITH COMMON HEADER
733 004176' 104403      MSGNS$,BEGIN,DRP ; ASCII MESSAGE CALL WITH COMMON HEADER
734 004204' 016706      MOV      SPOINT,R6
735 004210' 000167      JMP      LOOP2

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736 004214' 022767 000770 174164 3S: CMP #770,FLAG ;IN PROCESS OF RETRIVING BAD SPOT ?
737 004222' 001002 BNE .+6 ;BRANCH IF NOT
738 004224' 000167 175522 JMP RES2X ;IF SO, THEN BRANCH BACK
739 004230' 004767 000316 JSR PC,PICKBK ;TRY A DIFFERENT BLOCK
740 004234' 005367 174154 DEC UNITNO ;WANT TO RE-DO SAME DRIVE WERE ON
741 004240' 000167 176042 JMP LOOP2 ;GO DO IT
742 004244' 162705 000004 4S: SUB #4,R5 ;BUMP BACK RETURN FOR A RETRY
743 004250' 000205 RTS R5
744
745 ; -----
746
747 004252' 005067 174126 BLKADR: CLR TRACK ;START ADDR AT ZERO
748 004256' 005067 174116 CLR DSKADR
749 004262' 005067 174114 CLR CYL
750 004266' 005767 174146 TST MIXDV ;MIXED DRIVE ?
751 004272' 100430 BMI 56 ;BRANCH IF SO
752 004274' 016700 174076 MOV BLK,R0 ;GET BLOCK NUMBER
753 004300' 162700 000240 1S: SUB #32,*5,,R0 ;SUBTRACT A CYLINDER'S WORTH OF SECTORS
754 004304' 100403 BMI 26 ;BR OUT IF WENT TOO FAR
755 004306' 005267 174070 INC CYL ;ELSE COUNT A CYLINDER
756 004312' 000772 BR 16 ;AND DO IT AGAIN
757 004314' 062700 000240 2S: ADD #32,*5,,R0 ;PUT BACK ONE
758 004320' 162700 000040 3S: SUB #32,,R0 ;SUBTRACT A TRACK'S WORTH OF SECTORS
759 004324' 100403 BMI 46 ;BRANCH IF WE WENT TO FAR
760 004326' 105267 174053 INCB TRACK+1 ;ELSE COUNT ANOTHER TRACK
761 004332' 000772 BR 36 ;AND DO IT AGAIN
762 004334' 062700 000040 4S: ADD #32,,R0 ;PUT BACK ONE
763 004340' 010067 174034 MOV R0,DSKADR ;PUT IN SECTOR ADDRESS
764 004344' 116767 174035 17402' MOVB TRACK+1,DSKADR+1 ;PUT IN TRACK ADDR
765 004352' 000427 BR 106 ;EXIT
766 004354' 016700 174016 5S: MOV BLK,R0 ;LOAD THE BLOCK NUMBER
767 004360' 162700 000642 6S: SUB #22,*19,,R0 ;22 SECTOR*19 TRACK
768 004364' 100403 BMI 76 ;DONE IF NEG
769 004366' 005267 174010 INC CYL ;CYLINDER ADDRESS
770 004372' 000772 BR 66 ;LOOPING BACK
771 004374' 062700 000642 7S: ADD #22,*19,,R0 ;ADJUST ONE CYLINDER
772 004400' 162700 000026 8S: SUB #22,,R0 ;FIND WHICH TRACK
773 004404' 100403 BMI 96 ;EXIT IF DONE
774 004406' 105267 173773 INCB TRACK+1 ;INCREMENT TRACK COUNT
775 004412' 000772 BR 86 ;BRANCH IF NOT DONE
776 004414' 062700 000026 9S: ADD #22,,R0 ;ADJUST ONE TRACK
777 004420' 010067 173754 MOV R0,DSKADR ;TRACK AND SECTOR ADDRESSES
778 004424' 116767 173755 173747 MOVB TRACK+1,DSKADR+1 ;
779 004432' 000207 10S: RTS PC
780
781 004434' PICKBK:
782 004434' 005067 174000 CLR MIXDV ;RESET THE MIX DRIVE FLAG
783 004440' 005267 173750 1S: INC UNITNO ;POINT TO NEXT DRIVE
784 004444' 026727 173744 000010 CMP UNITNO,#8 ;DONE LOOKING?
785 004452' 002401 BLT 26 ;BR IF NO, ELSE
786 004454' 000207 RTS PC
787 004456' 016700 173732 2S: MOV UNITNO,R0 ;USE AS AN INDEX
788 004462' 136067 004542' 173720 BITB BITTAB(R0),DEVICE ;TEST THIS DEVICE?
789 004470' 001001 BNE 36 ;BR IF YES, ELSE
790 004472' 000762 BR 16
791 004474' 062716 3S: ADD #2,(SP) ;BUMP FOR A GOOD RETURN

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792 004500' 004567 000754 JSR R5,READY ;SEE IF DRIVE IS READY
793 004504' 000402 BR 46 ;BR IF IT WAS READY
794 004506' 004767 000510 JSR PC,NOIRDY ;ELSE GO CLEAR IT AND CHECK AGAIN
795 004512' 022777 024025 175026 4S: CMP #24025,*RMDT ;RMD2 ?
796 004520' 001407 BEQ 56 ;EXIT IF SO
797 004522' 022777 024024 175016 CMP #24024,*RMDT ;RMD3 ?
798 004530' 001403 BEQ 56 ;BRANCH IF SO
799 004532' 012767 177777 173700 MOV #*1,MIXDV ;OTHERWISE SET THE FLAG
800 004540' 000207 5S: RTS PC ;RETURN WITH A UNITNO READY
801 004542' 001001 BITTAB: 1001
802 004544' 004004 4004
803 004546' 020020 20020
804 004550' 100100 100100
805
806
807 004552' 062767 000001 173616 PICKBK: ADD #1,BLK ;DO NEXT BLOCK(SECTOR)
808 004560' 026727 173612 077777 CMP BLK,#77777
809 004566' 103403 BLO 16
810 004570' 012767 000001 173600 MOV #1,BLK ;GO BACK TO BLOCK 1
811 004576' 005767 173636 1S: TST MIXDV ;NOT RMD3/RMD2 ?
812 004602' 100411 BMI 36 ;BRANCH IF NOT
813 004604' 012700 000252' MOV #BADSP,RO ;GET BAD SPOT TABLE
814 004610' 012701 000020 MOV #16,,R1 ;LOOK FOR 16 ENTRIES
815 004614' 026720 173556 2S: CMP BLK,(R0)+ ;IS THIS A BAD BLK?
816 004620' 001754 BEQ PICKBK ;IF YES, GO PICK A NEW ONE
817 004622' 005301 DEC R1 ;COUNT A TABLE LOOK-UP
818 004624' 001373 BNE 26 ;BR BACK IF MORE TO GO
819 004626' 000207 3S: RTS PC
820
821 004630' 012700 000444' CLRKB: MOV #RBUF,RO ;CLEAR RBUF BUFFER
822 004634' 016701 173272 MOV RBUF$2,R1 ;GET ITS ADDR AND SIZE
823 004640' 005020 CLRCOM: CLR (R0)+ ;CLEAR ANOTHER
824 004642' 005301 DEC R1 ;COUNT ANOTHER
825 004644' 001375 BNE CLRCOM ;BR BACK IILL DONE
826 004646' 000207 RTS PC
827
828 004650' 012701 000001 DRDP: MOV #1,R1 ; INITIALIZE DROP PICKER
829 004654' 016700 173534 MOV UNITNO,R0 ; GET THE DRIVE NUMBER
830 004660' 001403 BEQ 26 ; IF DRIVE 0 GO DROP IT
831 004662' 006301 1S: ASL R1 ; POINT TO NEXT DRIVE
832 004664' 005300 DEC R0 ; IS THIS THE ONE ?
833 004666' 001375 BNE 16 ; NO, LOOK AGAIN
834 004670' 040167 173514 2S: BIT R1,DEVICE ; DROP THE DRIVE
835 ;*****
836 ;CONVERT UNITNO TO ASCII AND
837 ;STORE AT ADDR1
838 004674' 104420 000000' 000414' OTOAS,BEGIN,UNITNO,ADR1
839 004702' 006772'
840
841 004704' 000207 RTS PC ; RETURN
842
843
844 004706' 005067 173530 ERRORS: CLR BADSEC ;
845 004712' 005777 174602 TST *RMC$1 ; ATTENTION OR ERROR ?
846 004716' 100402 BMI 16 ;BR IF YES
847 004720' 005725 TST (R5)+ ;BUMP FOR GOOD RETURN

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848 004722' 000205      RTS      R5      ;AND RETURN
849 004724' 005000      CLR      R0
850 004726' 016701 174566      MOV      RMCS1,R1      ;ADDRESS OF RMCS1
851 004732' 012160 000312' 22s:      MOV      (R1)+,S(R0)      ;READ AND STORE
852 004736' 005720      TST      (R0)+      ;ALL CONTROLLER AND RM03
853 004740' 022700 000046      CMP      #46,R0      ;REGISTERS
854 004744' 001372      BNE      22s
855 004746' 004767 000660      JSR      PC,ERSUB1      ; LOAD ERROR INFORMATION
856 004752' 005767 173344      TST      S+10      ;IS THIS A DATA LATE ERROR?
857 004756' 100012      BPL      11s      ;NO
858 004760' 005267 173436      INC      DLTCNT      ;ADD 1 TO DATA LATE COUNTER
859 004764' 032767 000004 173024      BIT      #BIT2,SRI      ;TYPE ERROR AND COUNT IT?
860 004772' 001103      BNE      8s      ;NO
861 004774' 104403 000000' 006730'      MSGNS,BEGIN,DLTERR      ;ASCII MESSAGE CALL WITH COMMON HEADER
862 005002' 000450      BR      5s      ;COUNT
863 005004' 032767 020000 173300 11s:      BIT      #BIT13,S      ; MASSBUS CONTROL PARITY ERROR ?
864 005012' 001035      BNE      3s      ; YES
865 005014' 032767 000400 173300      BIT      #BIT8,S+10      ; MASSBUS DATA PARITY ERROR ?
866 005022' 001035      BNE      4s      ; YES
867 005024' 032767 040000 173260      BIT      #BIT14,S      ; TRANSFER ERROR ?
868 005032' 001015      BNE      2s      ; YES
869 005034' 032767 040000 173262      BIT      #BIT14,S+12      ; ANY DRIVE ERRORS ?
870 005042' 001030      BNE      5s      ; YES
871 005044' 005767 173260      TST      S+16      ; ANY ATTENTIONS ACTIVE ?
872 005050' 001025      BNE      5s      ; YES, CONTINUE
873 005052' 005067 173030      CLR      ERRRTYP      ;UNKNOWN ERROR
874      ;*****
875 005056' 104405 000000' 001444'      HDRDS,BEGIN,TABLE      ; SPECIAL CONDITION SET BUT NO REASON FOUND
876      ;*****
877 005064' 000446      BR      8s      ; RETURN
878 005066'
879 005068' 032767 100000 173260 2s:      BIT      #BIT15,S+42      ;IS A BAD SPOT ***** ?
880 005074' 001045      BNE      9s      ;BRANCH IF SO *****
881 005076' 104403 000000' 006710'      MSGNS,BEGIN,TRERR      ;ASCII MESSAGE CALL WITH COMMON HEADER
882 005104' 000407      BR      5s      ; GO DUMP REGISTERS
883 005106'
884 005108' 104403 000000' 006714' 3s:      MSGNS,BEGIN,MCPERR      ;ASCII MESSAGE CALL WITH COMMON HEADER
885 005114' 000403      BR      5s      ; GO DUMP REGISTERS
886 005116'
887 005118' 104403 000000' 006720' 4s:      MSGNS,BEGIN,MDPERH      ;ASCII MESSAGE CALL WITH COMMON HEADER
888 005124' 005767 173200 5s:      TST      S+16      ; ANY ATTENTIONS ACTIVE ?
889 005130' 001004      BNE      6s      ; YES, CONTINUE
890 005132' 016777 173172 174376      MOV      S+16,#RMAS      ;KILL ALL BITS
891 005140' 000205      RIS      R5
892 005142' 016700 174374 6s:      MOV      RMDB,R0      ; SAVE ADDRESS OF DATA BUFFER
893 005146' 105767 173150      TSTB      S+10      ; CAN DATA BUFFER BE READ ?
894 005152' 100403      BMI      7s      ; YES, CONTINUE
895 005154' 012767 000430' 174360      MOV      #ZERO,RMDB      ; NO, LOAD ADDRESS OF ZERO
896 005162' 012767 000001 172716 7s:      MOV      #1,ERRTYP      ;DATA ERROR
897      ;*****
898 005170' 104405 000000' 001444'      HDRDS,BEGIN,TABLE      ; DUMP RH11 AND RP REGISTERS
899      ;*****
900 005176' 010067 174340      MOV      R0,RMDB      ; RESTORE DATA BUFFER ADDRESS
901 005202' 004767 000014 8s:      JSR      PC,NOTRDY      ;GO CLEAR OUT ERRORS
902 005206' 000205      RTS      R5      ; ERRORS DETECTED, RETURN
903 005210' 005725 9s:      TST      (R5)+      ;IF BAD SPOT DON'T REPORT

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904 005212' 012767 177777 173222      MOV      #1,BADSEC      ;SET BAD SECTOR DETECT FLAG
905 005220' 000205      RIS      R5      ;EXIT*****
906
907
908 005222' 012767 077777 173204 NOTRDY: MOV      #77777,CLK      ; SET THE TIMER
909 005230' 016777 173160 174272 4s:      MOV      UNITNO,#RMCS2      ;MOVE DRIVE NUMBER TO RH11/70
910 005236' 012777 000011 174254      MOV      #11,#RMCS1      ;ISSUE DRIVE CLEAR COMMAND
911 005244' 032777 004000 174246      BIT      #BIT11,#RMCS1      ;DO I HAVE THE DRIVE DVA?
912 005252' 001406      BEQ      6s      ;NO
913 005254' 010046      MOV      R0,=(SP)
914 005256' 012600      MOV      (SP)+,R0      ;JUST WASTE A LITTLE TIME
915 005260' 032777 004000 174232      BIT      #BIT11,#RMCS1      ;STILL GOT IT?
916 005266' 001023      BNE      2s
917 005270'
918 005270' 104407 000000' 6s:      BREAKS,BEGIN      ;TEMPORARY RETURN TO MONITOR....
919 005274' 104407 000000'      BREAKS,BEGIN      ;THEN CONTINUE AT NEXT INSTRUCTION.
920 005300' 104407 000000'      BREAKS,BEGIN      ;TEMPORARY RETURN TO MONITOR....
921 005304' 104407 000000'      BREAKS,BEGIN      ;THEN CONTINUE AT NEXT INSTRUCTION.
922 005310' 032777 010000 174212      BIT      #BIT12,#RMCS2      ;NED SET ?
923 005316' 001014      BNE      5s      ;YES
924 005320' 005367 173110      DEC      CLK      ;COUNT # OF TRIES
925 005324' 001341      BNE      4s      ;NOT DONE YET
926 005326' 104403 000000' 006734'      MSGNS,BEGIN,NUT ;ASCII MESSAGE CALL WITH COMMON HEADER
927 005334' 000426      BR      7s      ;COULD NOT GET DRIVE
928 005336' 004567 176316 2s:      JSR      R5,CLEAR      ;SET THE CONTROLLER AND DRIVE
929 005342' 004567 000112      JSR      R5,READY      ; IS DRIVE READY ?
930 005346' 000443      BR      1s      ; YES, CONTINUE
931 005350' 004767 000256 5s:      JSR      PC,ERSUB1      ; LOAD ERROR INFORMATION
932 005354' 005000      CLR      R0      ;MOVE DRIVE REG INTO TABLE
933 005356' 016701 174136      MOV      RMCS1,R1
934 005362' 012160 000312' 22s:      MOV      (R1)+,S(R0)      ;READ AND STORE ALL
935 005366' 005720      TST      (R0)+      ;RH11/70 AND RM03
936 005370' 022700 000046      CMP      #46,R0      ;REGISTERS
937 005374' 001372      BNE      22s
938 005376' 012767 000006 172502      MOV      #6,ERRTYP      ;DRIVE NOT READY
939      ;*****
940 005404' 104405 000000' 001444'      HDRDS,BEGIN,TABLE      ; DRIVE NOT READY
941      ;*****
942 005412' 012777 000013 174100 7s:      MOV      #13,#RMCS1      ;RELEASE DRIVE
943 005420' 004767 177224      JSR      PC,DROP      ; NO, DROP THE DRIVE
944 005424' 104403 000000' 006756'      MSGNS,BEGIN,DRP ;ASCII MESSAGE CALL WITH COMMON HEADER
945 005432' 016706 172374      MOV      SPOINT,R6
946 005436' 022767 000770 172742      CMP      #770,FLAG      ;IN PROCESS OF RETRIEVING BAD SPOT
947 005444' 001002      BNE      .+6      ;BRANCH IF NOT
948 005446' 000167 174300      JMP      RES2X      ;IF SO, BRANCH BACK
949 005452' 000167 174630      JMP      LOOP2
950 005456' 000207 1s:      RTS      PC      ;RETURN
951      ;
952
953      ;DVA=0,NED=0      NOT DETERMINE
954      ;DVA=1,NED=0      DRIVE EXIST
955      ;DVA=0,NED=1      DRIVE NOT EXIST
956      ;DVA=1,NED=1      HARDWARE ERROR
957
958
959 005460' 016777 172730 174042 READY: MOV      UNITNO,#RMCS2      ; LOAD UNIT ADDRESS

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960 005466' 012777 000011 174024      MOV    #11,RMCS1    ;CLEAR DRIVE AND GRASP THE DRIVE
961 005474' 017700 174020      MOV    @RMCS1,R0    ;R0= BASE ADDRESS OF RH CONTROLLER
962 005500' 032700 004000      BIT     #BIT11,R0   ;DVA BIT = 1 ?
963 005504' 001436              BEQ     1$              ;NO
964 005506' 017700 174020      MOV    @RMDS,R0    ; SAVE STATUS IN R0
965 005512' 105700              TSTB   R0              ; DRIVE READY ?
966 005514' 100032              BPL     1$              ; NO
967 005516' 032700 000100      BIT     #BIT6,R0    ; VOLUME VALID ?
968 005522' 001427              BEQ     1$              ; NO
969 005524' 032700 000400      BIT     #BIT8,R0    ; DRIVE PRESENT ?
970 005530' 001424              BEQ     1$              ; NO
971 005532' 032700 004000      BIT     #BIT11,R0   ; WRITE LOCKED ?
972 005536' 001021              BNE     1$              ; YES
973 005540' 032700 010000      BIT     #BIT12,R0   ; MEDIUM ON LINE ?
974 005544' 001416              BEQ     1$              ; NO
975 005546' 032700 040000      BIT     #BIT14,R0   ; ANY ERRORS ?
976 005552' 001013              BNE     1$              ; YES
977 005554' 005700              TST     R0              ; ATTENTION SET ?
978 005556' 100411              BMI     1$              ; YES
979 005560' 032777 004000 173732      BIT     #BIT11,@RMCS1 ;DVA SET?
980 005566' 001405              BEQ     1$              ;BK IF NOT
981 005570' 032777 010000 173732      BIT     #BIT12,@RMCS2 ;MED BIT SET ?
982 005576' 001001              BNE     1$              ;YES,ERROR
983 005600' 000205              RTS     R5              ; RETURN READY
984 005602' 005725      1$: TST     (R5)+           ; SKIP INSTRUCTION FOLLOWING CALL
985 005604' 000205              RTS     R5              ; RETURN AS NOT READY
986                                     ; -----
987
988
989 005606' 014167 172274      ERSUB2: MOV    ~(R1),ASB    ; LOAD THE DATA
990 005612' 010167 172264      MOV    R1,SBADR    ; LOAD ADDRESS OF DATA WRITTEN
991 005616' 014267 172266      MOV    ~(R2),AWAS    ; LOAD THE DATA
992 005622' 010267 172256      MOV    R2,WASADR    ; LOAD ADDRESS OF DATA READ
993 005626' 005721              TST     (R1)+           ; RESET REG. 1
994 005630' 005722              TST     (R2)+           ; RESET REG. 2
995
996 005632' 016767 173662 172240      ERSUB1: MOV    RMCS1,CSRA    ; LOAD ADR OF CURRENT CSK
997 005640' 017767 173654 172234      MOV    @RMCS1,ACSR    ; LOAD CONTENTS OF CURRENT CSR
998 005646' 000207              RTS     PC              ; RETURN
999
1000
1001 005650' 017700 173644      REZET:  MOV    @RMCS1,R0    ;DVA BIT OF RMCS1
1002 005654' 032700 004000      BIT     #BIT11,R0
1003 005660' 001005              BNE     3$
1004 005662' 104407 000000'      BREAKS,BEGIN    ;TEMPORARY RETURN TO MONITOR....
1005 005666' 104407 000000'      BREAKS,BEGIN    ;THEN CONTINUE AT NEXT INSTRUCTION.
1006 005672' 000766              BR      REZET
1007 005674' 012777 000040 173626 3$: MOV    #BIT5,@RMCS2    ; ISSUE AN RM11 INIT
1008 005702' 012767 077777 172524      MOV    #77777,CLK    ; SET THE TIMER
1009 005710' 017700 173604      1$: MOV    @RMCS1,R0    ; CONTROLLER READY ?
1010 005714' 105700              TSTB   R0
1011 005716' 100417              BMI     2$              ; YES, CONTINUE
1012 005720' 104407 000000'      BREAKS,BEGIN    ;TEMPORARY RETURN TO MONITOR....
1013 005724' 104407 000000'      BREAKS,BEGIN    ;THEN CONTINUE AT NEXT INSTRUCTION.
1014 005730' 005367 172500      DEC     CLK          ; WAIT SOME MORE ?
1015 005734' 001365              BNE     1$              ; YES

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1016 005736' 012767 000004 172142      MOV    #4,ERRTYP    ;CONTROLLER NOT READY
1017                                     ;*****
1018 005744' 104405 000000' 000000      HRDEMS,BEGIN,NULL    ; CONTROLLER NOT READY
1019                                     ;*****
1020 005752' 104410 000000'      ENDS,BEGIN
1021 005756' 017746 173554      2$: MOV    @RMAS,~(SP)    ;CLEAR ALL BITS
1022 005762' 012677 173550      MOV    (SP)+,@RMAS    ;IN THE SUMMARY REGISTER
1023 005766' 000207              RIS     PC              ; RETURN
1024                                     ; -----
1025
1026
1027 005770' 016700 172012      SETUP:  MOV    ADDR,R0    ; GET DEVICE ADDRESS
1028 005774' 010067 173520      MOV    R0,RMCS1    ; GENERATE REGISTER ADDRESSES
1029 006000' 062700 000002      ADD     #2,R0
1030 006004' 010067 173512      MOV    R0,RMC
1031 006010' 062700 000002      ADD     #2,R0
1032 006014' 010067 173504      MOV    R0,RMBA
1033 006020' 062700 000002      ADD     #2,R0
1034 006024' 010067 173476      MOV    R0,RMDA
1035 006030' 062700 000002      ADD     #2,R0
1036 006034' 010067 173470      MOV    R0,RMCS2
1037 006040' 062700 000002      ADD     #2,R0
1038 006044' 010067 173462      MOV    R0,RMDS
1039 006050' 062700 000002      ADD     #2,R0
1040 006054' 010067 173454      MOV    R0,RMER1
1041 006060' 062700 000002      ADD     #2,R0
1042 006064' 010067 173446      MOV    R0,RMAS
1043 006070' 062700 000002      ADD     #2,R0
1044 006074' 010067 173440      MOV    R0,RMLA
1045 006100' 062700 000002      ADD     #2,R0
1046 006104' 010067 173432      MOV    R0,RMDB
1047 006110' 062700 000002      ADD     #2,R0
1048 006114' 010067 173424      MOV    R0,RMMR1
1049 006120' 062700 000002      ADD     #2,R0
1050 006124' 010067 173416      MOV    R0,RMDT
1051 006130' 062700 000002      ADD     #2,R0
1052 006134' 010067 173410      MOV    R0,RMSN
1053 006140' 062700 000002      ADD     #2,R0
1054 006144' 010067 173402      MOV    R0,RMOF
1055 006150' 062700 000002      ADD     #2,R0
1056 006154' 010067 173374      MOV    R0,RMDC
1057 006160' 062700 000002      ADD     #2,R0
1058 006164' 010067 173366      MOV    R0,RMHR
1059 006170' 062700 000002      ADD     #2,R0
1060 006174' 010067 173358      MOV    R0,RMMR2
1061 006200' 062700 000002      ADD     #2,R0
1062 006204' 010067 173352      MOV    R0,RMEK2
1063 006210' 062700 000002      ADD     #2,R0
1064 006214' 010067 173344      MOV    R0,RMEC1
1065 006220' 062700 000002      ADD     #2,R0
1066 006224' 010067 173336      MOV    R0,RMEC2
1067 006230' 000207              RTS     PC
1068 006232' 032767 000001 172146      SETUP2: BIT     #1,FLAG    ;FIRST TIME THROUGH?
1069 006240' 001426              BEQ     1$              ;BR IF NOT
1070 006242' 016700 171540      MOV    ADDR,R0    ;BASE ADDRESS OF RH11/RH70
1071 006246' 062700 000046      ADD     #46,R0      ;INDEX VALUE

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1072 006252' 032767 001000 171576      BIT      #ADDR22,RES1      ;11/70 SUPPORT?
1073 006260' 001416                      BEQ      1$              ;NO
1074 006262' 062700 000002                      ADD      #2,R0
1075 006266' 032767 100000 171522      BIT      #BIT15,SH1      ;SPECIFY 32 REGISTER ON RH70 ?
1076 006274' 001402                      BEQ      3$              ;BRANCH IF NOT
1077 006276' 062700 000024                      ADD      #24,R0          ;OTHERWISE ADJUST THE RMBAE ADDRESS
1078 006302' 010067 173262      3$:      MOV      R0,RMBAE
1079 006306' 062700 000002                      ADD      #2,R0
1080 006312' 010067 173254                      MOV      R0,RMCS3
1081 006316' 016700 171466      1$:      MOV      VEC10H,R0      ; GET VECTOR ADDRESS
1082 006322' 012720 004072'      MOV      #NTRUPT,(R0)+      ; SET POINTER JUST IN CASE
1083 006326' 116710 171460      MOV      BR1,(R0)      ; SET PRIORITY
1084 006332' 000207      2$:      RIS      PC      ; RETURN
1085
1086 006334' 020040 051124 047101      MES1: .ASCIZ ' TRANSFER ERROR%'
1087 006342' 043123 051105 020040
1088 006350' 051105 047522 022522
1089 006356' 000
1090 006357' 040 046440 051501      MES2: .ASCIZ ' MASSBUS PARITY ERROR%'
1091 006364' 041123 051525 020040
1092 006372' 040520 044522 054524
1093 006400' 020040 051105 047522
1094 006406' 022522 000
1095 006411' 040 046440 051501      MES3: .ASCIZ ' MASSBUS DATA PARITY ERROR%'
1096 006416' 041123 051525 020040
1097 006424' 040504 040524 020040
1098 006432' 040520 044522 054524
1099 006440' 020040 051105 047522
1100 006446' 022522 000
1101 006451' 040 042040 044522      MES4: .ASCIZ ' DRIVE '
1102 006456' 042526 020040 000
1103 006463' 040 042040 047522      MES5: .ASCIZ ' DROPPED%'
1104 006470' 050120 042105 000045
1105 006476' 051040 052105 054522      MES6: .ASCIZ ' RETRY EXCEEDED%'
1106 006504' 042440 041530 042505
1107 006512' 042504 022504 000
1108 006517' 040 040504 040524      MES10: .ASCIZ ' DATA LATE ERROR%'
1109 006524' 046040 052101 020105
1110 006532' 051105 047522 022522
1111 006540' 000
1112 006541' 040 051104 053111      MES11: .ASCIZ ' DRIVE NOT READY%'
1113 006546' 020105 047516 040124
1114 006554' 042522 042101 022531
1115 006562' 000
1116 006563' 040 047503 046125      MES12: .ASCIZ ' COULD NOT GET DRIVE%'
1117 006570' 020104 047516 020124
1118 006576' 042507 020124 051104
1119 006604' 053111 022505 000
1120 006611' 040 052117 042510      MES14: .ASCIZ ' OTHER PORT NOT UPDATING DRIVE%'
1121 006616' 020122 047520 052122
1122 006624' 047040 052117 052440
1123 006632' 042120 052101 047111
1124 006640' 020107 051104 053111
1125 006646' 022505 000
1126 006651' 045 053117 051105      XMES15: .ASCIZ '%OVER 16 BAD BLOCKS DETECTED%'
1127 006656' 030440 020066 040502

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1128 006664' 020104 046102 041517
1129 006672' 051513 042040 052105
1130 006700' 041505 042524 022504
1131 006706' 000
1132 006710' 000 .EVEN
1133 006710' 006334'      TRERR: MES1
1134 006712' 177777      177777
1135 006714' 006357'      MCPERR: MES2
1136 006716' 177777      177777
1137 006720' 006411'      MDPERR: MES3
1138 006722' 177777      177777
1139 006724' 006476'      EXCED: MES6
1140 006726' 177777      177777
1141 006730' 006517'      DLTERR: MES10
1142 006732' 177777      177777
1143
1144 006734' 006541'      NOT: MES11
1145 006736' 177777      177777
1146 006740' 006563'      TOUT: MES12
1147 006742' 177777      177777
1148
1149
1150 006744' 006451'      PORTHC: MES4
1151 006746' 006777'      NUMB
1152 006750' 006463'      MES5
1153 006752' 006611'      MES14
1154 006754' 177777      177777
1155 006756' 006451'      DHP: MES4
1156 006760' 006777'      NUMB
1157 006762' 006463'      MES5
1158 006764' 177777      177777
1159 006766' 006651'      MES15: XMES15
1160 006770' 177777      177777
1161
1162 006772' 000005      ADR1: .BLKB 5
1163 006777' 000      NUMB: .BYTE 0
1164 007000' 000      .BYTE 0
1165 007002' 000 .EVEN
1166
1167
1168 000001 .END

```

SEQ 002b

DLCNT	000422R	310#	858*
DLTERR	006730R	861	1141#
DRIVE	000412R	306#	
DRUP	004650R	500	543 732 828# 943
DRP	006756R	713	944 1159#
DSNADR	000400R	301#	198# 399# 585 601 617 633 649 748* 763* 764* 777* 778*
DVICE	000410R	305#	373# 466 788 834*
DVID1	000010R	211#	373
EAZZ	000374R	299#	703 704* 705* 706
ENDITS=	104413	270#	470
ENDS =	104410	270#	468 1020
ERRORS	004706R	721	644#
ERRTYP	000106R	244#	873* 896* 938* 1016*
ERSUB1	005632R	855	931 996#
ERSUB2	005606R	989#	
EXCED	006724R	728	1139#
EXIT\$ =	104400	270#	709
FEBDR	000432R	314#	366 580* 596* 612* 628* 644* 660* 676*
FLAG	000406R	304#	376# 396* 443* 465* 479* 493 526 557* 558 736 946 1068
FREE	000150R	262#	
FUNC	000424R	311#	579* 595* 611* 627* 643* 659* 675* 707* 708
GETPAS=	104415	270#	383 453
GO	003744R	594	610 626 642 658 674 693#
GWBUF\$=	104414	270#	456
HRCUNT	000044R	224#	
HRDER#=	104405	270#	875 898 940 1018
HRDPAS	000050R	226#	
ICUNIT	000036R	221#	
ICOUNT	000040R	222#	
IDNUM	000122R	251#	
IMUDX.=	000000	263#	457
INIT	000030R	218#	
INTH	000120R	250#	372*
LOOP1	002270R	386	455# 472
LOOP2	002306R	458#	463 735 741 949
MAP22\$=	104416	270#	701
MCPERR	006714R	884	1135#
MDPERR	006720R	887	1137#
MES1	006334R	1086#	1133
MES10	006517R	1108#	1141
MES11	006541R	1112#	1144
MES12	006563R	1116#	1146
MES14	006611R	1120#	1153
MES15	006766R	437	1159#
MES2	006357R	1090#	1135
MES3	006411R	1095#	1137
MES4	006451R	1101#	1150 1155
MES5	006463R	1103#	1152 1157
MES6	006476R	1105#	1139
MIXDV	000440R	317#	403 750 782* 799* 811
MUDNAM	000000R	205#	
MODSP	000252R	219	268#
MSGN\$ =	104403	270#	437 501 544 728 733 861 881 884 887 926 944
MSG\$S =	104402	270#	
MSG\$ =	104401	270#	
NOT	006734R	926	1144#

RMBC DEC/111 SYSTEM EXERCISER MODULE		MACY111 30A(1052) 12-UCI-78 17:01 PAGE 31		CROSS REFERENCE TABLE -- USER SYMBOLS																SEQ 0028		
ARMBC0.P11 21-SEP-78 15:27																						
RMDT	001546R	355#	795	797	1050*																	
RMEC1	001564R	362#	1064*																			
RMEC2	001566R	363#	1066*																			
RMER1	001534R	350#	1040*																			
RMEK2	001562R	361#	1062*																			
RMHR	001556R	359#	1058*																			
RMLA	001540R	352#	1044*																			
RMMR1	001544R	354#	1048*																			
RMMR2	001560R	360#	1060*																			
RMUF	001552R	357#	690*	1054*																		
RMSN	001550R	356#	1052*																			
RMWC	001522R	345#	583*	599*	615*	631*	647*	663*	1030*													
RSTRT	000112R	247#																				
RSTRT1	002256R	445	448	450	452*																	
S	000312R	286#	322	323	324	325	326	327	328	329	330	331	332	333								
		334	335	336	337	338	339	340	341	851*	856	863	865	867								
		869	871	879	888	890	893	934*														
SBADR	000102R	240#	990*																			
SETUP	005770R	378	1027#																			
SETUP2	006232R	381	1068#																			
SOPCNT	000042R	223#																				
SOFER#	104406	270#																				
SOPPAS	000046R	225#																				
SPOINT	000032R	219#	374	734	945																	
SPSIZ	000040	1#	263																			
SR1	000016R	212#	385	474	730	859	1075															
SR2	000020R	213#																				
SR3	000022R	214#																				
SR4	000024R	215#																				
STAKT	001604R	218	370#	451																		
STAT	000026R	217#																				
SVR0	000062R	232#																				
SVR1	000064R	233#																				
SVR2	000066R	234#																				
SVR3	000070R	235#																				
SVR4	000072R	236#																				
SVR5	000074R	237#																				
SVR6	000076R	238#																				
SYS CNT	000052R	227#																				
TABLE	001444R	321#	875	898	940																	
TIMER	000426R	312#	496*	498*	521*	541*																
TGUT	006740R	114#																				
TRACK	000404R	303#	747*	760*	764	774*	778															
TRERR	006710R	681	1133#																			
TRPDFD	000022	270#																				
TRY	000436R	316#	395*	461*	725*	726																
UNITNO	000414R	307#	377*	387*	455*	679	693	711	740*	783*	784	787	829	838								
		909	959																			
VECTOR	000010R	208#	1081																			
WASADR	000104R	242#	992*																			
WBUFEA	000136R	257#	588	604																		
WBUFPA	000134R	256#	584	600																		
WBUFRQ	000140R	258#																				
WBUFSZ	000142R	259#	581	597																		
WDFR	000116R	249#	371*																			
WDT0	000114R	248#	370*																			

WRITCB	003452R	555	643#	644														
WRITCK	003166R	485	595#	596														
WRITE	003072R	481	579#	580														
WRITEB	003356R	554	627#	628														
WRT00	003546R	495	559	659#	660													
WRT00R	003562R	661#	677															
XBLK	001600R	368#																
XFERAD	001574R	366#																
XFERCT	001576R	367#																
XFLAG	000005R	206#																
XMEH	000370R	297#	593*	609*	625*	641*	657*	673*	697*	698*	699*	700*	706*	707				
XMES15	006651R	1126#	1159															
ZERO	000430R	313#	895															
.	= 007002R	319#	401	483	488	518	737	947	1132#	1162#	1165#							

. ABS. 000000 000
 007002 001

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

XRMB0,XRMB0/SUL/CRF:SYM=DDACOM,XRMB0
 RUN-TIME: 2 3 .5 SECONDS
 RUN-TIME RATIO: 27/5=4.7
 CORE USED: 7K (13 PAGES)

.REF

IDENTIFICATION

PRODUCT CODE: AC-E965G-MC
 PRODUCT NAME: CXRLAG0 RL11/RLV11/RLV12/RL01/RL02
 PRODUCT DATE: JUNE 1982
 MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

RLA IS AN IOMODX THAT EXERCISES RL01/RL02 DISK DRIVES ON RL11/RLV11/RLV12 CONTROLLERS. IT EXERCISES THE DRIVES BY DOING READ HEADERS, SEEKS, READS, WRITES AND IN-CORE COMPARISONS.

ALL ERRORS DETECTED ARE REPORTED ON THE CONSOLE DEVICE.

2. REQUIREMENTS

HARDWARE: 1 TO 4 RL01/RL02 DISK DRIVES (WITH SCRATCH PACKS).
 1 RL11, RLV11, OR RLV12 DISK CONTROLLER.

STORAGE: RLA REQUIRES:
 1. DECIMAL WORDS: 1445
 2. OCTAL WORDS: 2845
 3. OCTAL BYTES: 5512

3. PASS DEFINITION

ONE PASS OF THE RLA MODULE CONSISTS OF 20 X 100 CYCLES OF THE BASIC TEST SEQUENCE (READ HEADER, SEEK, READ HEADER, WRITE, WRITE CHECK, READ). THE TEST SEQUENCE WRITES 1024 WORDS, READS BACK THE FIRST 256, AND DATA CHECKS THE SAME.

4. EXECUTION TIME

ONE PASS OF RLA RUNNING ALONE ON A PDP-11/40 TAKES APPROXIMATELY ONE MINUTE.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:
 DEVADR: 174400, VECTOR: 160, HRI: 5, DEVCNT: 1

6. DEVICE/OPTION SETUP

INSURE THAT ALL DRIVES UNDER TEST ARE POWERED UP, WRITE ENABLED, AND READY -- AND OF COURSE, SCRATCH PACKS INSTALLED !!!

IF MORE THAN 1 DRIVE, CHANGE DVID1: (LOC 14') ACCORDINGLY.

IF RLV12 CONTROLLER, SET SR1: = 10 (LOC 16', BIT 3).

7. SR1 OPTIONAL SETUP

BIT 0 - DROP DRIVE ON ERROR
 BIT 1 - RANDOM SEEKS
 BIT 2 - DON'T PRINT SOFT ERRORS

DEC/X11 SYSTEM EXBERSIZER MACRO DEFINITION MODULE

BIT 3 - CONTROLLER IS AN RLV12

H. ERROR REPORTING

ON ERROR ALL REGISTERS ARE PRINTED IN THE FOLLOWING ORDER:

RLV11/RLV12 RLCS RLHA RLDA RLMF DRIVE STATUS
 RLV12 RLCS RLHA RLDA RLMF RLBAE DRIVE STATUS

```
*****
*
*   EDIT:  BY:      DATE:      REASON:
*
*   1      G.PASQUINTONIO  MAY-81  "HNF" ERRORS WERE BEING
*                                     REPORTED AS "UPI".
*
*   2      G.PASQUINTONIO  MAY-81  CONTROLLER ERROR ON
*                                     WRITE-CHECK CAUSED A
*                                     TERMINAL TABLE SEARCH
*                                     (ULTIMATELY TRAPPING TO 4).
*
*   3      G.PASQUINTONIO  MAY-81  "DROP ON ERROR" OPTION
*                                     (SRIC0>) NOT IMPLEMENTED
*                                     CORRECTLY.
*
*   4      G.PASQUINTONIO  MAY-81  RLV12 22 BIT UPGRADE.
*
*   5      P. ANASTAS      DEC-81  DATA CHECK ERRORS ON FLAGGED
*                                     BAD SPOIS ABOVE CYL. 177
*                                     24 DECIMAL PATCH LOCATIONS
*                                     ADDED.
*
*****
```

DEC/X11 SYSTEM EXBERSIZER MACRO DEFINITION MODULE

```
000000      IUMODA <RLAG >,174400,160,5,0,0,20,,146,HUFIN,256,,1024.
000000      MODULE 150000,RLAG,174400,160,5,0,0,20,,146,HUFIN,256,,1024.
;          TITLE RLAG DEC/X11 SYSTEM EXBERSIZER MODULE
;          DDXCUM VERSION 6.4 28-JAN-92
;          .LIST BIN
;*****
;BEGIN:
;MODNAM: .ASCII /RLAG / ;MODULE NAME.
000000      122      114      101      XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
000000      107      VECTOR: 160+0 ;1ST DEVICE ADDR.
000000      000      BR1: .BYTE PRTYS+0 ;1ST DEVICE VECTON.
000000      174400    000160    RR2: .BYTE PRTYU+0 ;1ST BR LEVEL.
000012      240      DV101: 0+1 ;2ND BR LEVEL.
000013      000      SR1: OPEN ;DEVICE INDICATOR 1.
000014      000001    SR2: OPEN ;SWITCH REGISTER 1.
000015      000000    SR3: OPEN ;SWITCH REGISTER 2.
000020      000000    SR4: OPEN ;SWITCH REGISTER 3.
000022      000000    SR4: OPEN ;SWITCH REGISTER 4.
000024      000000    STAT: 150000 ;STATUS WORD.
;*****
000026      150000    INIT: START ;MODULE START ADDR.
000030      000252    SPOINT: MODSP ;MODULE STACK POINTER.
000032      000252    PASCNT: 0 ;PASS COUNTER.
000034      000000    ICON1: 20. ;# OF ITERATIONS PER PASS=20.
000036      000024    ICOUNT: 0 ;LOC TO COUNT ITERATIONS
000040      000000    SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000042      000000    HRCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000044      000000    SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000046      000000    HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000050      000000    SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
000052      000000    RANUM: 0 ;RNGS RANDOM # WHEN RAND MACRO IS CALLED
000054      000000    CONFIG:
000056      000000    RES1: 0 ;RESERVED FOR MONITOR USE
000060      000000    RES2: 0 ;RESERVED FOR MONITOR USE
000062      000000    SVR0: OPEN ;LOC TO SAVE R0.
000064      000000    SVR1: OPEN ;LOC TO SAVE R1.
000066      000000    SVR2: OPEN ;LOC TO SAVE R2.
000070      000000    SVR3: OPEN ;LOC TO SAVE R3.
000072      000000    SVR4: OPEN ;LOC TO SAVE R4.
000074      000000    SVR5: OPEN ;LOC TO SAVE R5.
000076      000000    SVR6: OPEN ;LOC TO SAVE R6.
000100      000000    CSRA: OPEN ;ADDR OF CURRENT CSR.
000102      000000    CSRA: OPEN ;ADDR OF GOOD DATA, OR
000104      000000    *ASADR: ;CONTENTS OF CSR.
000106      000000    ASTAT: OPEN ;ADDR OF BAD DATA, OR
000110      000000    ERR1: OPEN ;STATUS REG CONTENTS.
000112      000424    *ASADR: ;TYPE OF ERROR.
000114      000000    RSTRT: RESTRT ;EXPECTED DATA.
000116      000000    WDFW: OPEN ;ACTUAL DATA.
000120      000000    INTR: OPEN ;PESTART ADDRESS AFTER END OF PASS
000122      000146    IDNUM: 146 ;WORDS TO MEMORY PER ITERATION
000124      003636    RHUFVA: BUFIN ;WORDS FROM MEMORY PER ITERATION
000126      000000    RHUFPA: OPEN ;# OF INTERRUPTS PER ITERATION
;MODULE IDENTIFICATION NUMBER=146
;READ BUFFER VIRTUAL ADDRESS
;READ BUFFER PHYSICAL ADDRESS
```

DEC/X11 SYSTEM EXPANSIZER MACRO DEFINITION MODULE

```

000130 000000 RHUFPA: OPEN
000132 000400 RHUFPA: 256
000134 000000 RHUFPA: OPEN
000136 000000 RHUFPA: OPEN
000140 000000 RHUFPA: 1024
000142 000000 RHUFPA: OPEN
000144 000000 COERCT: OPEN
000146 000000 CDWDCI: OPEN
000150 000000 FREE: OPEN
000040 000040 .REPT SPSIZ
000040 .NLST
000040 .LIST
000040 .ENDM

000252 MODSP:
*****
159 000252 012767 002000 177636 START: MOV #1024, WDFR ;1024. WORDS FROM MEM/ITERATION
160 000260 012767 000400 177626 MOV #256, WDTU ;256 WORDS TO MEM/ITERATION
161 000260 012767 000005 177624 MOV #5, INTR ;5 INTERRUPTS/ITERATION
162 000260 012767 000057 004424 MOV #57, NUMH
163 000302 012767 177400 003316 MOV #400, DRIVE ;SET DRIVE SELECT
164 000310 012767 000001 003254 MOV #1, DRVMSK ;SETUP DRIVE SELECT MASK
165 000316 005067 003274 CLR DLTCH ;CLEAR DATA LATE COUNT
166 000322 004767 001300 JSP PC, SETUP ;GO SET UP REGISTERS
167 000326 016767 177462 003274 MOV DVID1, DVICE ;COPY DRIVE SELECTION
168 000334 122737 000014 000041 3S: CMPL #14, #41 ;WAS RL LOAD DEVICE?
169 000342 001020 BNE 7S ;N-BRANCH: Y-SEE IF LOAD UNIT SELECTED
170 000344 012767 000001 000040 MOV #1, W2 ;SET UP FOR MASK
171 000350 113701 000040 MOV #40, R1 ;IF ZERO GO MASK OUT UNIT
172 000354 001403 BNE K2 ;SHIFT MASK
173 000356 004302 4S: ASL K2 ;DEC COUNT
174 000360 105301 BNC 5S ;KEEP CHECKING
175 000362 001375 BIT K2, DVICE ;WAS THAT DRIVE SELECTED?
176 000364 030267 003240 BIC 7S ;N-BRANCH: Y-CONTINUE
177 000370 001405 BEQ K2, DVICE ;DELETE UNIT FROM DEVICE MAP
178 000372 040267 MSGNS, BEGIN, DROPLD ;ASCII MESSAGE CALL WITH COMMON HEADER
179 000376 104403 000000 005474 7S: TST DVICE ;ANY DRIVES SELECTED?
180 000376 104403 000000 005474 RNE HSTR1 ;YES, CONTINUE
181 000410 001011 MSGNS, BEGIN, AROHT ;ASCII MESSAGE CALL WITH COMMON HEADER
182 000412 014403 000000 005424 TST CNT ;MESSAGE CALL WITH COMMON MODULE
183 000420 000167 001176 RSTR1: TST CNT ;+ SUPPORT
184 000424 005767 003162 RNE RSTR1 ;+ FOR
185 000430 001001 BR START ;+ DT03
186 000432 000707 RSTR1: GETPAS, BEGIN, RHUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RHUFVA
187 000432 000707 BR CHKDR1 ;CHKDR1
188 000442 000421 000000 000124 190: ASL DVMASK ;SHIFT MASK FOR NEXT DRIVE
189 000442 000421 000000 000124 LOOP: CMPL #20, DRVMSK ;DRIVE MASK OVERSHIFT CHECK
190 000444 005367 003122 003114 BNE CHKDR1 ;BRANCH IF MASK ON
191 000444 005367 003122 003114 MOV #1, DRVMSK ;RESET DRIVE SELECT MASK
192 000446 012767 000057 004232 MOV #57, NUMH ;RESET DRIVE SELECT
193 000446 012767 177400 003124 CHKDRV: MOV #400, DRIVE
194 000502 104413 000000 000502 ENDIS, BEGIN ;SIGNAL END OF ITERATION.
195 000502 104413 000000 000502 ;MONITOR SHALL TEST END OF PASS

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DEC/X11 SYSTEM EXPANSIZER MACRO DEFINITION MODULE

```

198 000506 062767 000400 003112 CHKDR1: ADD #400, DRIVE ;NEXT DRIVE
199 000514 005267 004206 1S: INC NUMH
200 000520 036767 003046 003102 BIT DRVMSK, DVICE ;IS THAT DRIVE PRESENT
201 000526 001746 BEQ LOOP ;NO, GO FOR NEXT ONE
202 000530 005067 002772 CLR DLTCH ;CLEAR A FEW LOCATIONS
203 000534 005067 003026 CLR RWK ;READ WHITE ERROR FLAG
204 000540 005067 003046 CNT ;COUNT
205 000540 005067 003046 ;
206 000540 005067 003046 ;
207 000540 005067 003046 ;
208 000540 005067 003046 ;
209 000544 004767 000722 JSP PC, WTRDY ;ISSUE DRIVE RESET, CLEAR VOLUME
210 000550 004567 001430 JSR #100077, MASK2 ;SET UP FOR RL01
211 000554 012767 100077 003024 MOV #77724, FSTBSC
212 000562 012767 077724 003004 MOV #77750, LSTBSC
213 000570 012767 077750 003000 MOV #100177, MASK
214 000576 012767 100177 002774 MOV #77600, LSTCYL
215 000604 012767 077600 002770 MOV #77700, LSTTRK
216 000612 012767 077700 002764 BIT #200, I.MP ;TEST RL01 OR RL02
217 000620 032767 000200 002730 BEQ 2S ;RL01 BRANCH
218 000626 001422 BIC #100000, MASK ;FIX FOR RL02
219 000630 042767 100000 002742 BIC #100000, MASK2
220 000636 052767 100000 002736 BIC #100000, LSTCYL
221 000644 052767 100000 002732 BIC #100000, LSTTRK
222 000652 052767 100000 002716 BIC #100000, LSTBSC
223 000660 042767 100000 002720 BIC #100000, MASK2
224 000666 052767 100000 002700 BIC #100000, FSTBSC
225 000674 012767 000201 002722 2S: MOV #201, DIFWD
226 000702 004567 001256 LOOP: JSR #5, R0HDR ;READ HEADER ON DISK
227 000706 016767 002644 002706 MOV #4, R0HDR ;GET HEADER
228 000706 016767 002644 002706 ;
229 000706 016767 002644 002706 ;
230 000706 016767 002644 002706 ;
231 000714 032767 000002 177074 TAG: BIT #BIT1, SR1 ;INCREMENTAL OR RANDOM SEKS?
232 000722 001446 BEQ TAG1 ;INCREMENTAL TAG1
233 000722 001446 BIC #177, HDRWD ;CLEAR HEAD AND SECTOR BITS
234 000724 042767 000177 002670 RANDS, BEGIN
235 000732 104417 000000 000000 MOV RANUM, R0 ;STORE IT AWAY
236 000736 016700 177112 MOV R0, R1 ;SAVE A COPY
237 000742 010001 MOV #0, R1 ;CLEAR HEAD AND SECTOR
238 000744 046700 002630 BIC MASK, R0 ;LET'S CALCULATE DIFFERENCE WORD
239 000750 010067 002659 R0, DIFWD ;GET DIFFERENCE TO SEEK
240 000754 166767 002642 002642 SUB HDRWD, DIFWD
241 000762 103003 BCC 1S ;MAKE DIFF ABSOLUTE
242 000764 005467 002634 NEG DIFWD
243 000770 000403 BR 2S ;SET DIRECTION BIT
244 000772 052767 000004 002624 1S: BIC #4, DIFWD ;SET MARKER
245 001000 052767 000001 002616 2S: BIC #1, DIFWD ;TEST MARKER
246 001006 032701 000100 BIT #100, R1 ;TEST HEAD
247 001012 001403 BEQ 3S ;IF 0, DON'T SET HEAD IN DIFF
248 001014 052767 000020 002602 3S: BIC #20, DIFWD ;SET HEAD
249 001022 010167 002574 MOV R1, HDRWD ;GET EXPECTED HEADER
250 001026 046767 002554 BIC MASK2, HDRWD ;CLEAR SECTOR BITS
251 001034 000167 000134 JMP TAG2
252 001040 042767 000177 002554 TAG1: BIC #177, HDRWD ;CLEAR OUT SECTOR BITS & HEAD
253 001046 032767 177600 002546 BIT #177600, HDRWD ;ON TRACK 0?

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DEC/X11 SYSTEM EXERSIZER MACRO DEFINITION MODULE

```

255 001054 001007 000200 002530 BNE 1$ ;NO, GO CHECK FOR LAST CYLINDER
256 001056 012767 000200 002530 MOV #200,HDRWD ;SET NEXT ADDRESS=CYL 1
257 001064 012767 000205 002532 MOV #205,DIFWD ;DIF WD 1, MARKER, SEEK IN, HS=0
258 ;SET CURRENT HD=0, SEEK IN
259 001072 000440 TAG2 ;CURRENT ADDRESS=LAST TRACK?
260 001074 026767 002502 002520 1$: CMP LSTCYL,HDRWD ;NO, CONTINUE
261 001102 001072 000200 002510 SUB #200,HDRWD
262 001104 162767 000200 002510 BNE 4$ ;NO, CYL WILL INCREMENT
263 001112 052767 000100 002502 BLS #100,HDRWD ;SKIP OVER
264 001120 012767 000221 002476 MOV #221,DIFWD ;NO, CYL WILL DECREMENT
265 ;HEAD SET?
266 001126 000422 TAG2 ;NO, LEAVE EXPECTED ALONE
267 001130 032767 000604 002466 2$: BIT #4,DIFWD ;YES, SET HEAD SELECT BIT
268 001136 001404 BEQ 3$ ;WAIT FOR SEEK TO FINISH
269 001140 062767 000200 002454 ADD #200,HDRWD ;HEAD HEADER VERIFY CORRECT
270 001146 000403 000200 002440 4$: SUB #200,HDRWD ;SEEK
271 001150 162767 000200 002444 3$: BIT #20,DIFWD ;READ HEADER
272 001156 032767 000200 002440 4$: BEQ TAG2 ;CLEAR OUT SECTOR BITS
273 001164 001403 000100 002426 TAG2: BLS #100,HDRWD ;WAS SEEK CORRECT?
274 001166 052767 000740 002426 JSH R5,SEK ;YES, CONTINUE
275 001174 004567 000740 002426 JSH PC,WTROY ;NO REPORT ERROR
276 001200 004767 000266 JSH R5,RDHDR ;SEEK
277 001204 004567 000754 JSH R5,RDHDR ;HEAD HEADER VERIFY CORRECT
278 ;SEEK
279 001210 016767 002342 003504 MOV T,MP,CURADH ;READ HEADER
280 001216 042767 000077 003476 BIC #77,CURADR ;CLEAR OUT SECTOR BITS
281 001224 026767 003472 002370 CMP CURADR,HDRWD ;WAS SEEK CORRECT?
282 001232 001425 000000 002370 BEQ 6$ ;YES, CONTINUE
283 ;NO REPORT ERROR
284 001234 016767 002272 176636 MOV HLCS,CSRA ;ASCII MESSAGE CALL WITH COMMON HEADER
285 001242 017767 002264 176632 MOV #RLCS,ACSH
286 001246 016767 002264 176626 MOV #RLCS,ASTAT
287 001256 104403 000000 005470 MSGNS,BEGIN,BUSEK ;ASCII MESSAGE CALL WITH COMMON HEADER
288 ;SEEK
289 001264 012767 000051 176614 MOV #51,ERRTYP ;BAD SEEK
290 001272 104405 000000 005500 HDRMS,BEGIN,TABLE ;SEEK WAS BAD
291 ;*****
292 001300 016767 003416 002314 MOV CURADR,HDRWD ;MAKE MISTAKE NEW HDRWD
293 ;*****
294 001306 026767 002310 002270 6$: CMP HDRWD,LSTIRK ;ARE WE ON LAST TRACK
295 001314 001002 000167 177372 BNE 7$ ;NO, CONTINUE
296 001316 000167 177372 JMP TAG ;YES, GO GET ANOTHER CAUSE ITS THE BAD SECTOR TRACK
297 ;*****
298 001322 016767 176604 002304 7$: MOV #BUFSSZ,WCNT2 ;GET BUFFER SIZE (READ)
299 001330 005467 002300 002264 NEG #CNT2 ;NEGATE FOR RMP
300 001334 104414 000000 002264 G#BUFS, BEGIN ;GET WRITE BUFFER INFORMATION
301 001340 016767 176576 002264 MOV #BUFSSZ,WCNT1 ;GET BUFFER SIZE (WRITE)
302 001346 005467 002260 002264 NEG #CNT1 ;NEGATE FOR RMP
303 001352 004567 000502 JSH R5,WRITE ;WRITE DATA
304 001356 005767 002204 TST R#ERR ;CONTROLLER ERROR ??
305 001362 000416 000172 BEQ 8$ ;SKIP HEADS IF SO.
306 001364 004567 000460 JSH R5,WHCHK ;ERROR ??
307 001370 005767 002172 TST R#ERR ;SKIP HEADS IF SO.
308 001374 001012 000506 BNE 5$ ;SKIP HEADS IF SO.
309 001376 004567 000506 JSH R5,HEAD ;HEAD DATA

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DEC/X11 SYSTEM EXERSIZER MACRO DEFINITION MODULE

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310 001402 005767 002160 TST R#ERR ;ERROR ??
311 001406 001005 BNE 5$ ;SKIP DATA CHECK IF SO.
312 001410 104412 000000 000126 C#DATAS,BEGIN,R#UFPA ;REQUEST FOR MONITOR TO CHECK DATA
313 001416 001422 000000 000126 5$: BNE 4$ ;IF ERROR, RETURN AT TAG 5$
314 001420 000416 000000 000126 5$: BIT #BIT0,SK1 ;IF NO DATA ERRORS.
315 001430 001404 000000 000126 5$: BEQ 4$ ;DROP ON ERROR ??
316 001432 004567 000134 JSH R5,DROP ;YES, DROP IT.
317 001436 000167 177002 JMP LOOP ;...AND TRY ANOTHER.
318 ;*****
319 001442 005067 002120 4$: CLR R#ERR
320 001446 005267 002140 INC CNT
321 001452 022767 000020 002132 CMP #16,CNT
322 001460 001402 BEQ 4$
323 001462 000167 177214 JMP LOOP ;REITERATE 16 TIMES/DRIVE...
324 001466 000167 176752 3$: JMP LOOP ;...THEN GET ANOTHER DRIVE.
325 ;*****
326 ;WAIT FOR DRIVE READY. DROP DRIVE IF IT NEVER COMES UP.
327 ;*****
328 001472 042777 001400 002032 WTRDY: BIC #1400,RLCS
329 001500 056777 002122 002024 HIS DRIVE,RLCS
330 001506 012767 077777 002074 MOV #77777,CLK
331 001514 032777 000001 002010 1$: BIT #1,RLCS ;SET UP TIMEOUT
332 001522 001401 BEQ 2$ ;DRIVE READY?
333 001524 000207 RTS PC ;NOT YET
334 001526 104407 000000 000126 2$: BREAKS,BEGIN ;YES, RETURN TO CALLER.
335 001532 104407 000000 000126 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR.
336 001536 005367 002046 DEC CLK ;THEN CONTINUE AT NEXT INSTRUCTION.
337 001544 012767 000006 176334 MOV #0,ERRTYP ;CHECK TIMEOUT
338 001552 104405 000000 003532 HDRMS,BEGIN,RLCS ;LOOP TIL TIMER EXPIRES.
339 001560 004567 000006 TST R5,DROP ;DRIVE NOT READY
340 001564 005726 176652 JSH (SP)+ ;CLEAR DRIVE (DROP) FROM LIST.
341 001566 000167 176652 JMP LOOP ;...AND GO FOR ANOTHER.
342 ;*****
343 ;DROP CURRENT DRIVE. DROP MODULE IF NO DRIVES LEFT.
344 ;*****
345 001572 104403 000000 005410 1$: MSGNS,BEGIN,DROPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
346 001600 046767 001766 002022 BIC 1$ ;CLEAR THIS DRIVE BIT.
347 001606 001401 BEQ 1$ ;DROP MODULE IF NO MORE.
348 001610 000205 RTS R5 ;OTHERWISE, RETURN TO CALLER.
349 001612 104403 000000 005420 1$: MSGNS,BEGIN,NOLEFT ;ASCII MESSAGE CALL WITH COMMON HEADER
350 001620 005726 TST (SP)+ ;FIX STACK.
351 001622 104410 000000 005410 FINI: ENDS,BEGIN ;END OF THE MODULE
352 ;*****
353 ;ROUTINE TO SET UP RL11 REGISTERS, VECTOR AND BR LEVEL
354 ;*****
355 001626 016700 176154 SETUP: MOV ADDR,R0 ;GET BASE ADDRESS
356 001632 010064 001674 MOV R0,RLCS ;CONTROL REGISTER
357 001636 005720 TST (R0)+ ;INCREMENT FOR NEXT
358 001640 010067 001670 MOV R0,RLBA ;BUS ADDRESS

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DEC/X11 SYSTEM EXERCISER MACRO DEFINITION MODULE

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359 001644 005720 TST (R0)+ ;INCREMENT FOR NEXT
360 001646 010067 MOVB R0,RLDA ;DISK ADDRESS
361 001652 005720 TST (R0)+ ;INCREMENT FOR NEXT
362 001654 010067 MOVB R0,RLMP ;DATA BUFFER
363 001660 032767 000010 176130 BIT #BIT1,SR1 ;TEST IF AN RLV12 CONTROLLER ;GP-4
364 001666 001411 BEQ 15 ;IF NOT ;GP-4
365 001670 005720 TST (R0)+ ;INCREMENT FOR NEXT ;GP-4
366 001672 010067 MOVB R0,RLAAE ;BUS EXTENDED ADDRESS REGISTER ;GP-4
367 001676 012767 003560 003604 MOV #1,BAT,TABLEY ;FIX ERROR REPORT ;GP-4
368 001704 012767 003562 003600 MOV #1,SIAT,TABLEZ ;FOR RLV12 CONTROLLER ;GP-4
369 001712 016700 005720 1S: MOV VECTOR,R0 ;GET VECTOR ADDRESS
370 001714 012720 005720 MOV #START,(R0)+ ;SET POINTER
371 001722 116710 176064 MOVB #R1,(R0) ;SET PRIORITY
372 001726 000207 RTS PC ;RETURN
373
374 ; SUBROUTINE TO SET 18 OR 22 BIT BUS ADDRESS IN CONTROLLER. ;GP-4
375 ; THIS ENTIRE SUBROUTINE IS NEW.
376
377 001730 016767 176200 000102 SETWRA: MOV #RUFFA,PA18 ; GET 18 BIT WRITE BUFFER ADDRESS.
378 001736 016767 176174 000076 MOVB #RUFFA,EA18 ;
379 001744 000406 BR SETCMN ;
380 001746 016767 176154 000064 SETWRA: MOV #RUFFA,PA18 ; GET 18 BIT READ BUFFER ADDRESS.
381 001754 016767 176150 000060 MOVB #RUFFA,EA18 ;
382 001756 016770 000052 001544 SETCMN: MOV PA18,#RLDA ; SET BA<15:0>...
383 001770 042777 000060 001534 BIC #00,RLCS ;...AND BA<17:16> IN CSRC<5:4>.
384 001776 056777 000040 001526 BIS EA18,#RLCS ;...AND BA<17:16> IN CSRC<5:4>.
385 002004 032767 000010 176004 BIT #BIT1,SR1 ; ARE WE RLV12 ??
386 002012 001411 BEQ 15 ; IF NOT
387 002014 104416 000000 002040 22S: MOV #BEGIN,PA18 ; GET 22-BIT ADDR FROM 18-BIT ADDR
388 002022 016777 000016 001504 MOVB #A22,#RLBA ; LOAD BA<15:0>...
389 002030 016777 000012 001504 MOVB #A22,#RLAAE ;...AND BA<21:16>.
390 002036 000207 RTS PC
391
392 002040 000000 PA18: 0 ; 18 BIT BA<15:0>...
393 002042 000000 EA18: 0 ;...AND EA<17:16> IN BITS <5:4>.
394 002044 000000 PA22: 0 ; 22 BIT BA<15:0>...
395 002046 000000 EA22: 0 ;...AND EA<21:16> IN BITS <5:0>.
396
397 ; DRIVES (INTERUPT)
398
399 002050 012767 000102 001542 WRCHK: MOV #102,FUNC ; WRITE-CHECK...
400 002056 000403 403 ;...OR...
401 002060 012767 000112 001532 WRITE: MOV #112,FUNC ;...WRITE FUNCTION.
402 002066 016777 001540 001444 MOV #CNT1,RLMP ; WORD COUNT
403 002074 016777 001522 001434 MOV #HADR,RLDA ; DISK ADDRESS
404 002102 004767 177622 JSP PC,SEIRBA ; WRITE BUFFER ADDRESS ;GP-4
405 002106 000444 BR LAEC ;
406 002110 012767 000114 001502 READ: MOV #114,FUNC ; READ FUNCTION
407 002116 016777 001522 001434 MOV #CNT2,RLMP ; WORD COUNT
408 002124 016777 001472 001404 MOV #HADR,RLDA ; DISK ADDRESS
409 002132 004767 177610 JSP PC,SEIRBA ; READ BUFFER ADDRESS ;GP-4
410 002136 000430 BR LAEC ;
411 002140 012767 000106 001452 SEEK: MOV #106,FUNC ; SEEK FUNCTION
412 002146 016777 001452 001362 MOV #DIFND,RLDA ; DIFFERENCE WORD
413 002154 052777 000001 001354 BIS #1,RLDA ; SET MARKER BIT
414 002162 000416 BR LAEC ;
415 002164 012767 000110 001426 RDHDR: MOV #110,FUNC ; READ HEADER FUNCTION

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DEC/X11 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

416 002172 000412 416 EXEC
417 002174 012777 000003 001334 GSTAT: MOV #3,RLDA ;GET STATUS...
418 002202 000403 403 ;...OR...
419 002204 012777 000013 001324 DPVRTS: MOV #13,RLDA ;...RESET AND GET STATUS.
420 002212 012767 000104 001400 MOV #104,FUNC ;GET STATUS FUNCTION.
421
422 002220 042777 001416 001304 EXEC: BIC #1416,RLCS ; CLEAR THE OLD... ;GP-4
423 002226 056777 001374 001276 DRIVE,#RLCS ;...AND INSERT NEW DRIVE... ;GP-4
424 002234 056777 001300 001270 BIS #FUNC,RLCS ;...AND UPDATE BITS. ;GP-4
425 002242 012777 002262 175540 MOV #INSTR,VECTOR ;SET INTERRUPT VECTOR... ;GP-4
426 002250 042777 000200 001254 BIC #200,RLCS ;...AND EXECUTE. ;GP-4
427 002256 104400 000000 EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
428
429 ; CONTINUE HERE ON RL INTERRUPT.
430
431 002262 INTSRV:
432 002262 000004 000000 002270 PIPUS,BEGIN,1S ;
433 002270 005067 001266 175576 1S: CLR #1,SIAT ;
434 002274 016767 001232 175576 MOVB #RLCS,CSRA ;LOAD ADDR OF CSR
435 002302 017767 001224 175572 MOVB #RLCS,ACSR ;LOAD CONTENTS OF CSR
436 002316 017767 001212 001226 MOVB #RLDA,T.BA
437 002324 017767 001206 001222 MOVB #RLDA,T.OA
438 002332 017767 001202 001216 MOVB #RLMP,T.OP
439 002340 032767 000010 175450 BIT #BIT1,SR1 ;TEST IF RLV12 CONTROLLER ;GP-4
440 002346 001403 BEQ 20S ;IF NOT ;GP-4
441 002350 017767 001166 001202 20S: MOVB #RLBAE,T.BAE ;GET EA BITS
442 002356 005767 001166 T.CS ;ANY ERRORS
443 002362 100403 HLT 11S ;YES, CONTINUE TO CHECK
444 002364 005067 001136 CLR #METRY ;
445 002370 000205 RTS R5 ;NO, RETURN CALL+4 SKIP RETRY
446
447 002372 005267 001170 11S: INC #METRY ;
448 002376 012767 005404 003056 MOVB #NULLX,HTYPE ;SETUP FOR NULL PRINT
449 002404 032767 040000 001136 BIT #BIT14,T.CS ;DRIVE ERROR
450 002412 001457 BEQ 2S ;NO BRANCH
451 002414 012777 000003 001114 MOV #3,RLDA ;GET STATUS
452 002422 012767 000004 001116 MOV #4,TMP ;
453 002430 056767 001172 001110 BIS #DRIVE,TMP ;
454 002436 016777 001104 001066 MOV #IMP,RLCS ;
455 002444 104407 000000 005500 99S: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
456 002450 104407 000000 001050 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
457 002462 001770 000200 001050 BIT #200,RLCS ;
458 002464 017767 001050 001070 MOVB #RLMP,T.SIAT ;
459 002472 104403 000000 005454 MSGNS,BEGIN,ORVER ;ASCII MESSAGE CALL WITH COMMON HEADER
460 002500 012767 000006 175400 MOV #0,ERRTP ;DRIVE ERROR
461
462 002506 104405 000000 005500 *****
463 002514 012777 000013 001014 *****
464 002522 016777 001020 001002 *****
465 002530 104407 000000 98S: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....

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DEC/X11 SYSTEM EXERSIZER MACRO DEFINITION MODULE

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002534 104407 000000' 000764 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
002540 032777 000200 000764 BFI #200,RLCS
002546 001770 BEQ 98S
002550 000522 BR NURPT
002552 032767 020000 000770 2S: BIT #BIT13,T.CS ;NXM SET
002560 001404 BEO 21S
002562 012767 005400' 002672 MOV #NXM,HTYPE
002570 000515 BR HRDRPT
002572 032767 002000 000750 21S: BIT #BIT10,T.CS ;UPI SET
002600 001423 BEO 4S ;NO CHECK DCRC,CK
002602 012767 005353' 002652 MOV #UPI,HTYPE ;INITIAL SET FOR OPI
002610 032767 004000 000732 BIT #BIT11,T.CS ;MCRC?
002616 001404 REU 3S ;NO BRANCH
002620 000505 MOV #MCRC,HTYPE ;HRCR ERROR
002626 000505 BR FNDBSC ;FIND BAD SECTOR
002630 032767 010000 000712 3S: BIT #BIT12,T.CS ;HNF
002636 001477 HRDRPT ;NO, REPORT AS REPORT "OPI" ;GP-1
002640 012767 005367' 002614 MOV #HNF,HTYPE ;HNF ERROR
002646 000475 BR FNDBSC ;GO CHECK BAD SECTOR FILE
002650 032767 004000 000672 4S: BIT #BIT11,T.CS ;DCK?
002656 001406 BEO 5S ;NO,MUST BE DLT
002660 012767 000670 DLT ;BACK UP TO SECTOR THAT WAS BAD
002664 012767 005363' 002570 MOV #DCK,HTYPE ;DCK ERROR
002672 000463 BR FNDBSC ;GO CHECK BAD SECTOR FILE
002674 012767 005357' 002560 5S: MOV #DLT,HTYPE ;SETUP DLT ERROR
002702 032767 000004 175106 RPTERR: BIT #BIT2,SR1 ;PRINTING SOFTERRORS ??
002710 001011 BNE 55S ;NO, SKIP PRINT
002712 104403 000000' 005460' MSGNS,BEGIN,SUPT ;ASCII MESSAGE CALL WITH COMMON HEADER
002720 012767 000001 175160 MOV #SUPT,HTYPE ;DATA ERROR
002726 104406 000000' 005500' SUFES,BEGIN,TABLE ;*****
002734 026767 000566 000566 55S: CMP RETRY,LIMIT ;RETRY EXHAUSTED
002742 001405 BEQ 55S ;YES, NO MORE RETRIES
002744 005267 000556 INC RETRY ;ADJUST RETURN PC...
002750 162705 SUB #4,R5 ;...AND TRY AGAIN.
002754 000205 RTS
002756 016700 000636 6S: MOV #FUNC,R0 ;GET FUNCTION CODE...
002762 042700 177761 BIC #C16,R0 ;...STRIP IT...
002766 004730' 002434 MOV #FNCLSTR(R0),EXCELD ;...AND GET APPROPRIATE TEXT.
002774 000767 002462 002440 MOV #TYPE,TER1 ;GET ERROR TYPE
003002 104403 000000' 005440' MSGNS,BEGIN,HARD ;ASCII MESSAGE CALL WITH COMMON HEADER
003010 104403 000000' 005430' MSGNS,BEGIN,EXCELD ;ASCII MESSAGE CALL WITH COMMON HEADER
003016 005067 NORPT: CLR RETRY
003022 000767 RPS ;
003024 016767 002432 002410 HRDRPT: MOV #HTYPE,TER1 ;ASCII MESSAGE CALL WITH COMMON HEADER
003032 104403 000000' 005440' MSGNS,BEGIN,HARD
003040 000766 BR NURPT
;
;ERROR WAS HRCR OR HNF OR DCK, POSITION TO LAST TRACK AND RECOVER
;BAD SECTOR FILES. IF DCK/HNF CHECK WHOLE DA, IF HRCR CHECK IF
;WE WERE DOING A RDRUR IF READ HUP THEN CHECK ONLY TRACK AND
;CYLINDER.
003042 016701 000534 FNDBSC: MOV #LSICYL,R1 ;LAST TRACK

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SEQ 12

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```

003046 016700 000550 MOV #RDRUR,R0 ;PRESENT POSITION
003052 042700 000100 BIC #100,R0 ;CLEAR OUT HEAD
003056 160001 SUB #R0,R1 ;CALC SEEK DIFFERENCE
003060 010777 000452 MOV #R1,RLDA ;LOAD SEEK DIFFERENCE
003064 052777 000025 000444 BIS #25,RLDA ;SET HEAD 1, SEEK IN
003072 016767 000530 000464 MOV #DRIVE,MFLG ;SELECT DRIVE. (MFLG USED)
003100 052767 000006 000456 BIS #0,MFLG ;SET UP SEEK
003106 016773 000452 000416 MOV #RLCS,RLCS ;SEEK
003114 004767 176352 JSR PC,WTROY ;WAIT FOR SEEK TO FINISH
003120 016700 000460 ;NOW SITTING ON LAST TRACK, RECOVER BAD SECTOR FILES AND COMPARE
003124 005067 000434 MOV #LSITRK,R0 ;STARTING SECTOR 0
003130 005067 000434 CLR MFLG ;SWITCH TO TELL US MANUF OR FIELD FILE
003134 010077 000376 CLR FND ;FLAG TO INDICATE HEADER FOUND IN LIST
003140 012777 177400 000372 MOV #0,RLDA ;LOAD SECTOR TO READ
003146 012777 000215 000356 MOV #215,RLCS ;TWO SECTOR READ
003154 004767 176566 JSR PC,SETRRA ; READ COMMAND
003160 056777 000442 000344 JSR PC,SETRRA ; BUFFER ADDRESS.
003166 042777 000200 000336 BIS #DRIVE,RLCS ; INSERT DRIVE
003174 004767 176272 JSR PC,WTROY ; EXECUTE IT
003200 005777 000326 TST #RLCS ;WAIT FOR DRIVE
003204 100023 BPL 4S ;READ SUCCESSFUL??
003206 062700 000004 ADD #4,R0 ;YES, GO CHECK FOR SECTOR
003212 005767 000346 TST MFLG ;NO, NEXT SECTOR
003216 001012 BNE 5S ;WHICH WE READING, MANUF OR FIELD
003220 026700 000350 CMP #STHSC,R0 ;FIELD COMPARE AGAINST 77750
003224 001343 BNE 2S ;MANUFACTURING AT END
003226 104403 000000' 005450' 99S: MSGNS,BEGIN,MUSEC ;ASCII MESSAGE CALL WITH COMMON HEADER
003234 004567 176332 JSR #5,DROP
003240 000167 175200 JMP LDUPL
003244 026700 000326 3S: CMP #STHSC,R0 ;AT END OF FIELD BAD
003250 001331 BNE 2S ;NO, GO BACK
003252 000765 BR 99S ;YES GO DROP DRIVE
003254 016701 176444 4S: MOV #BUFWA,R1 ;GET WHERE WE READ
003260 062701 000010 ADD #10,R1 ;SKIP PAST I.D. ETC.....
003264 012702 000176 MOV #126,R2 ;ONLY 126 ENTRIES
003270 012103 MOV #R1,R3 ;GET CYLINDER
003272 100440 BFI #8S ;MINUS WE'RE DONE
003274 012104 MOV #R1,R4 ;GET TRACK AND SECTOR
003276 000303 SWAB R3 ;ALIGN PROPERLY
003300 106003 RORH R3 ;PA005
003302 006003 ASR R3 ;PA005
003304 150403 BISH R4,R3
003306 032704 BIT #400,R4
003312 001402 BEO 5S
003314 000100 BIS #100,R3
003320 022767 005373' 002134 5S: CMP #MCRC,HTYPE ;IS ERROR HRCR?
003326 001012 BNE 6S ;NO, GO LOOK FOR BAD SECTOR
003330 022767 000110 000262 CMP #110,FUNC ;WE'RE WE DOING READ HEADER

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DEC/X11 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

575 003336 001006 BNE 6S ;NO, GO LOOK FOR BAD SECTOR
576 003340 042703 000077 BIC #77,H3 ;YES, CLEAR SECTOR BITS
577 003344 020367 000252 CMP R3,HDRWD ;HAD SECTOR
578 003350 001006 RELG RS
579 003352 000406 RS
580 003354 020367 000174 6S: CMP R3,T.DA ;IS THIS ONE IT??????
581 003360 001003 BNE RS ;NO
582 003362 005267 000202 7S: INC RND
583 003366 000412 RS
584 003370 005302 8S: DEC R2 ;CHECKED WHOLE FILE
585 003372 001336 44S ;NO
586 003374 005767 000164 88S: IST MFLG ;WHICH WE DOING
587 003380 001005 9S: INC R3 ;FIELD WE'RE DONE
588 003402 005267 000156 INC MFLG ;MANUFACT. THEN SET UP FIELD
589 003406 018700 000162 MOV FSTRSC,R0
590 003412 000650 BK ZS
591
592 003414 018700 000202 9S: MOV HDRWD,R0
593 003420 018701 000156 MOV LSTCYL,R1
594 003424 042700 000100 BIC #100,R0
595 003430 160001 SUB R0,R1
596 003432 010177 000100 MOV R1,MFLDA
597 003436 052777 000001 000072 BIS #1,MFLDA
598 003444 032767 000100 000150 RIT #100,HDRWD
599 003452 014003 RELG 10S
600 003454 052777 000020 000054 BIS #20,MFLDA
601 003462 018767 000140 000074 MOV DRIVE,MFLG
602 003470 052767 000006 000066 BIS #6,MFLG
603 003476 018767 000026 000026 MOV MFLG,MFLCS ; SEEK.
604 003504 004767 175762 JSH PC,MFLCS
605 003510 005767 000054 TST RND
606 003514 001002 HNE 11S
607 003516 000167 177169 JMP RPIEKK
608 003522 000167 177279 JMP RUMPT
609
610 ;LOCATIONS USED BY MODULE
611
612 003526 000000 GETRY: .WORD 0
613 003530 000003 LIMIT: .WORD 3
614 003532 000000 RLCS: .WORD 0
615 003534 000000 RLBA: .WORD 0
616 003536 000000 RLDA: .WORD 0
617 003540 000000 RLMP: .WORD 0
618 003542 177777 RLBAE: .WORD 177777 ; RLV12 BAE
619 003544 177777 RLBAE: .WORD 177777 ; TERMINATOR. ;GP-4
620 003546 000000 TMP: .WORD 0
621 003550 000000 T.CS: .WORD 0
622 003552 000000 T.BA: .WORD 0
623 003554 000000 T.DA: .WORD 0
624 003556 000000 T.MP: .WORD 0
625 003560 000000 T.BAE: .WORD 0 ; RLV12 BAE
626 003562 000000 T.STAT: .WORD 0 ;GP-4
627 003564 000000 MFLG: .WORD 0
628 003566 000000 MFLCS: .WORD 0
629 003570 000000 FND: .WORD 0
630 003572 000000 DRVMSK: .WORD 0
631 003574 000000 FSTRSC: .WORD 0

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DEC/X11 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

632 003576 000000 LSTRSC: .WORD 0
633 003600 000000 MASK: .WORD 0
634 003602 000000 LSTCYL: .WORD 0
635 003604 000000 LSTTRK: .WORD 0
636 003606 000000 MASK2: .WORD 0
637 003610 000000 CLK: .WORD 0
638 003612 000000 CNT1: .WORD 0
639 003614 000000 MFLDRV: .WORD 0
640 003616 000000 DLTCNT: .WORD 0
641 003620 000000 FUNC: .WORD 0 ;NUMBER OF DATA RATE ERRORS
642 003622 000000 HDRWD: .WORD 0 ;FUNCTION TO BE PERFORMED
643 003624 000000 DIRWD: .WORD 0 ;HEADER WORD (RND, R/N)
644 003626 000000 DRIVE: .WORD 0 ;DIFFERENCE WORD (SEEK)
645 003630 000000 DVICE: .WORD 0 ;DRIVE UNDER TEST (BITS 8,9)
646 003632 000000 WCNF1: .WORD 0 ;WORKING "DVICI"
647 003634 000000 WCNF2: .WORD 0 ;WORD COUNT (WRITE)
648 003636 000000 BUFIN: .BLKW 256. ;WORD COUNT (READ)
649 003638 000000 BSECHP: .BLKW 25. ;HAD SECTOR LIST
650 003640 000000 CORMSG: .WORD 0
651 003642 000000 CURADR: .WORD 0
652 003644 000000 NXTADR: .WORD 0
653 003646 000000 NUMB: .WORD 0
654 003648 005404 FNCLST: .NULLX
655 003650 005404 MES8: .MES8
656 003652 005107 MES9: .MES9 ; OPCODE 0 UNUSED. ;GP-2
657 003654 005053 MES6: .MES6 ; 1 = RIT CHK. ;GP-2
658 003656 005037 MES5: .MES5 ; 2 = RESET OR G.I STATUS.
659 003658 005065 MES8: .MES8 ; 3 = SEEK.
660 003660 005060 MES7: .MES7 ; 4 = READ HEADER.
661 003662 005404 NULLX: .NULLX ; 5 = WRITE.
662 ; 6 = READ.
663 ; 7 = READ NO-HEADER (UNUSED). ;GP-2
664
665 004750 116 117 040 MES1: .ASCIZ 'NO DRIVES PRESENT %'
666 004752 104 122 111
667 004754 126 105 123
668 004756 040 120 122
669 004758 105 123 105
670 004760 116 124 040
671 004762 045 000
672 004764 116 117 040 MES2: .ASCIZ 'NO DRIVES LEFT %'
673 004766 104 122 111
674 004768 126 105 123
675 004770 040 114 105
676 004772 106 124 040
677 004774 045 000
678 004776 116 117 040 MES3: .ASCIZ 'DRIVE '
679 004778 104 122 111
680 004780 126 105 123
681 004782 040 114 105
682 004784 106 124 040 MES4: .ASCIZ 'DROPPED %'
683 004786 045 000
684 004788 116 117 040 MES5: .ASCIZ 'READ HEADER'
685 004790 104 122 111
686 004792 126 105 123
687 004794 040 114 105 MES6: .ASCIZ 'SEEK'
688 004796 123 105 105
689 004798 113 000

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 DEC/X11 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

669 005060 122 105 101 MES7: .ASCIZ 'READ'
    005063 104 000
670 005065 127 122 111 MES8: .ASCIZ 'WRITE'
    005070 124 105 000
671 005073 127 122 111 MES8A: .ASCIZ 'WRITE-CHECK'
    005076 124 105 055
    005101 103 110 105
    005104 103 113 000
672 005107 104 122 111 MES9: .ASCIZ 'DRIVE RESET'
    005112 126 105 040
    005115 122 105 123
    005120 105 124 000
673 005123 040 122 105 MES10: .ASCIZ 'RETRY LIMIT EXCEEDED%'
    005126 124 122 131
    005131 040 114 111
    005134 115 111 124
    005137 040 105 130
    005142 103 105 105
    005145 104 105 104
    005150 045 000
674 005152 045 123 105 MES11: .ASCIZ '%SEEK TO WRONG CYLINDER%'
    005155 125 040
    005160 124 117 040
    005163 127 122 117
    005166 116 107 040
    005171 103 131 114
    005174 111 116 104
    005177 105 122 045
    005207 000
675 005203 040 101 124 MES12: .ASCIZ 'DATA LATE%'
    005206 101 040 114
    005211 101 124 105
    005214 045 000
676 005216 104 122 111 MES13: .ASCIZ 'DRIVE ERROR%'
    005219 126 105 040
    005224 105 122 122
    005227 117 122 045
    005232 000
677 005234 123 117 106 MES14: .ASCIZ 'SOFT ERROR '
    005236 124 040 105
    005241 122 122 117
    005244 122 040 040
    005247 040 000
678 005251 122 114 130 MES15: .ASCIZ 'RLX LOAD UNIT DROPPED'
    005254 040 114 117
    005257 101 104 040
    005262 125 116 111
    005265 124 040 104
    005270 122 117 120
    005273 120 105 104
    005276 000
679 005277 110 101 122 MES16: .ASCIZ 'HARD ERROR '
    005302 104 040 105
    005305 122 122 117
    005310 127 040 040
    005314 040 000
680 005315 103 101 116 NBOSEC: .ASCIZ 'CANT RECOVER HARD SECTOR FILE%'
  
```

;GP-2

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 DEC/X11 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

    005320 124 040 122
    005323 105 103 117
    005326 126 105 122
    005331 040 102 101
    005334 104 040 123
    005337 105 103 124
    005342 117 122 040
    005345 106 111 114
    005350 105 045 000
    005353 117 120 111 UP1: .ASCIZ 'UP1'
    005356 000
682 005357 104 114 124 OUT: .ASCIZ 'OUT'
    005362 000
683 005363 104 103 113 OCK: .ASCIZ 'OCK'
    005366 000
684 005367 110 106 106 HNF: .ASCIZ 'HNF'
    005372 000
685 005373 110 103 122 HCRC: .ASCIZ 'HCRC'
    005376 103 000
686 005400 116 130 115 NXM: .ASCIZ 'NXM'
    005403 000
687 005404 040 000
688 005406 045 000
    005409 000
    005410 005015'
    005412 004726'
    005414 005024'
    005416 177777
695
696 005420 004774'
697 005422 177777
698
699 005424 004750'
700 005426 177777
701
702 005430 000000
703 005432 005123'
704 005434 005406'
705 005436 177777
706
707 005440 005277'
708 005442 000000
709 005444 005406'
710 005446 177777
711
712 005450 005315'
713 005452 177777
714
715 005454 005216'
716 005456 177777
717
718 005460 005233'
719 005462 000000
720 005464 005406'
721 005466 177777
722
  
```

DROPMS: MES3
 NU4R
 MES4
 177777

NOLEFT: MES2
 177777

ABORT: MES1
 177777

EXCEED: .WORD 0
 MES10
 CR
 177777

HARD: MES16
 .WORD 0
 CR
 177777

NBOSEC: NBOSEC
 177777

DRVERR: MES13
 177777

SOFT: MES14
 .WORD 0
 CR
 177777

HLA DEC/11 SYSTEM EXERCISER M MACRO M1200 02-SEP-82 14:57 PAGE 3-13
 DEC/11 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

723 005470 005152' BOSEEK: MES11
724 005472 177777
725
726 005474 005251' DMOPLD: MES15
727 005476 177777
728
729
730
731 005500 003550'
732 005502 003552'
733 005504 003554'
734 005506 003556'
735 005510 003562'
736 005512 177777
737 005514 177777
738
739 005516 000030
740
741
742
743
744 000001

TABLE: .WORD T.CS
      .WORD T.BA
      .WORD T.DA
      .WORD T.MP
      .WORD T.STAT
      .WORD T.TERMINATOR
      .WORD T.TERMINATOR

PATCH: .REPT 24.
        .LIST 0
        .ENDK
        .END

;CONTROL AND STATUS REGISTER
;BUS ADDRESS REGISTER
;DISK ADDRESS REGISTER
;DISK DATA BUFFER ADDRESS
;T.HAE ;HAS STATUS ON DRIVE ERROR ;GP=4
;I.STAT ;TERMINATOR ;GP=4
;TERMINATOR
;24 DECIMAL PATCH LOCATIONS ;PA005

```

HLA DEC/11 SYSTEM EXERCISER M MACRO M1200 02-SEP-82 14:57 PAGE 3-14
 SYMBOL TABLE

```

ABORT 005424R DIFWD 003624R LOUPL 000444P PDP44 = 100000 SET#BA 001730R
ACSR 000102R DLT 005357R LSHSC 003576R PDP60 = 004000 SUPCNT 000042R
ADDR 000008R DLTCNT 003616R LSTCYL 003602R PDP70 = 010000 SUPCNT = 104406
ADDR22= 001000 DRIVE 003626R LSTTK 003604R PIRQS = 000004 SJFPAS = 000046R
APTPRE= 000200 DRIP 001572R MAP225= 104416 PUPSP = 005726 SFT 005460R
ASB 000106R DRUPLD 005474R MASK 003606H PUPSP2= 022626 SP0IN1 000032R
ASTAT 000104R DRUPLS 005410H PRHSS= 000002 SP512 = 000040
AUTO = 000010 DVERR 005454H MES1 004750R PTY = 000000 SM1 000016R
AAS 000110H DRVMSK 003572H MES10 005123P PTY0 = 000000 SR2 000020R
BOSEEK 005470R DRVMS 002204H MES11 005152P PTY1 = 000040 SR3 000022P
BEGIN 000000R DVICE 003630R MES12 005203H PTY2 = 000100 SR4 000024R
BIT0 = 000001 DVID1 000014H MES13 005216H PTY3 = 000140 START 000252R
BIT1 = 000002 EATH 002042H MES14 005233H PTY4 = 000200 STAT 000026R
BIT10 = 002000 EA22 002046R MES15 005251H PTY5 = 000240 SVR0 000062R
BIT11 = 004000 ECCMEM= 000100 MES16 005277H PTY6 = 000300 SVR1 000064R
BIT12 = 010000 ENRITS= 104413 MES2 004774H PTY7 = 000340 SVR2 000066R
BIT13 = 020000 ENDS = 104410 MES3 005015H PS = 177776 SVR3 000070R
BIT14 = 040000 ERRTYP 000106R MES4 005024R PSW = 177776 SVR4 000072R
BIT15 = 100000 EXCEED 005430H MES5 005037H PUSH = 005716 SVK5 000074R
BIT2 = 000004 EXEC 002220H MES6 005053H PUSH2 = 024646 SVR6 000076R
BIT3 = 000010 EXITS = 104400 MES7 005060H PWRFLG= 000002 SYSCNT 000052R
BIT4 = 000020 FINI 001622H MES8 005065K PWRFLG= 000002 TABLE 005500R
BIT5 = 000040 FNCLST 004730H MES9 005073P WANDS = 104417 TABLEY 005510R
BIT6 = 000100 FND 003570R MES9 005107R WANDS = 104417 TABLEZ 005512P
BIT7 = 000200 FNDHSC 003042H MFLG 003564R RMUFLA 000130R TAG 000714R
BIT8 = 000400 FREE 000150R MODNAM 000000H RMUFLA 000126R TAG1 001040R
BIT9 = 001000 FSTHSC 003574K MUOSP 000252H RMUFLS 000132R TAG2 001174R
BWEAKS= 104407 FUNK 003620R MSGS = 104403 RMUFLV 000124R T81 005442R
BR1 000012H GETPAS= 104415 MSGS = 104402 RMUFLV 002154R T8P 003546R
BR2 000013H GSTAT 002174R MSGS = 104401 RMUFLV 002110R TRPDFD= 000023
BSECHF 004636R GWAPFS= 104414 MULDHV 003614R RESTRT 000424R T.BA 003552R
BTUDS = 104421 HARD 005440R MBUSC 005315R RES1 000056R T.BAE 003560R
BUTLN 003636H HCHC 005373P MCPIDP= 000020 RES2 000060H T.CS 003550R
CAPRES= 000004 HDWD 003622H NOAPIY= 000002 RETRY 003526R T.DA 003554R
COATAS= 104412 HNF 005367R NOLEFT 005420R RH70 = 001000 T.MP 003556R
CDEACT 000144H HRUCNT 000044R NORPI 003016H RLBA 003534R T.STAT 003562R
CDEUCT 000146H HRDRPS= 104405 NUSEC 005450H RLBAE 003542H USPACK= 000001
CHKDPV 000502H HRDRPT 003024H NULL = 000000 RLCS 003532R VFC10H 000010H
CHKDR1 000506P HTYPE 005462R NULX 005404R RLDA 003536H WASADR 000104H
CKHNGS= 000001 ICDNT 000036P KXTADR 004724R RLMP 003540P WUFLA 000136R
CLK 003610R ICDNT 000040H KXTADR 004724R RPTPH 002702R WUFLA 000134R
CLKPRF= 000001 ICDNT 000040H KXTADR 004724R PSTK1 000112R WUFLRQ 000140R
CLKSPF= 104422 ICDNT 000122H OPEN = 000000 RSTRT1 000434R WUFLSZ 000142R
CNT 003612R ICDNT 000000 UFI 005353R RWER 003566R WCN11 003632R
CJFIG 000056R ICDNT 000040 UFI2 = 104420 RWER 003566R WCN12 003634H
CR 005406K INIT 000030K PAPER= 002000 R7 = 000000 WDFR 000116R
CSA 000100P INTR 000120K PAPER= 000034H SHADR 000122H WDT 000114H
CURADH 004722H INTRSV 002262R PATCH 005516H SEEK 002140H WRCHK 002050H
CURMSG 003720H KTRPS= 000400 PA1 002040R SETCH 001762R WRTE 002060R
DATCRS= 104411 KXTND= 040000 PA22 002044R SETRMA 001766H WTRDY 001472R
DATERS= 104404 LIMIT 003530R PDPL11= 000002 SETUP 001626R XFLAG 000005R
DCK 005363R LOOP 000702P PDPLS1= 000000

```

. AHS. 000000 000
 005576 001
 ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 13181 WORDS (52 PAGES)

RLAG DEC/X11 SYSTEM EXERCISER M MACRO M1200 02-SEP-82 14:57 PAGE 3-15
SYMBOL TABLE

DYNAMIC MEMORY: 19748 WORDS (75 PAGES)
ELAPSED TIME: 00:00:34
XRLAGO.XRLAGO/CH/-SP=DLXCOM.XRLAGO

[illegible]

XRLAGO CREATED BY MACRO ON 2-SEP-82 AT 14:57 PAGE 2 V01
 SYMBOL CROSS REFERENCE VALUE REFERENCES
 DATCKS = 104411 #3-158
 DATEMS = 104404 #3-158
 DCK 005363 R #3-486 #3-683
 DIFWD 003624 R #3-225 #3-239 #3-240 #3-242 #3-244 #3-245 #3-248 #3-257 #3-264
 DLT 005357 R #3-267 #3-272 #3-412 #3-643
 DLTCNT 003616 R #3-488 #3-682
 DRIVE 003620 R #3-164 #3-196 #3-199 3-329 3-423 3-453 3-524 3-538 3-601
 DRDP 001572 R #3-644
 DRUPLD 005474 P #3-316 3-339 #3-345 3-551
 DRUPMS 005410 P #3-160 #3-726
 DRVERH 005454 P #3-345 #3-691
 DRVMSK 003572 R #3-459 #3-715
 DRVTS 002204 R #3-165 #3-191 3-192 #3-194 3-201 3-346 #3-630
 DVICE 003630 R #3-210 #3-419
 DVIU1 000014 R #3-168 3-177 #3-179 3-181 3-201 #3-346 #3-645
 EATR 002042 R #3-158 #3-168
 EAZ2 002046 R #3-374 #3-381 3-384 #3-393
 ECCMEM = 000109 #3-389 #3-395
 ENDITS = 104413 #3-158 3-197
 FNUS = 104410 #3-158 #3-351
 FRKTYP 000106 R #3-158 #3-299 #3-337 #3-460 #3-493
 EXCLFD 005430 R #3-503 3-506 #3-702
 EXEC = 002220 R #3-405 3-410 3-416 #3-422
 EXITS = 104400 #3-158 3-427
 FINE 001822 R #3-351 #3-354
 FNCLST 004730 R #3-503 #3-654
 FND 003570 R #3-533 #3-582 3-605 #3-629
 FNDBSC 003042 R #3-478 3-482 #3-518
 FREE 000150 R #3-158
 FSTBSC 003574 R #3-210 3-547 #3-589 #3-631
 FUNC 003620 R #3-399 #3-401 #3-406 #3-411 #3-415 #3-420 3-424 3-501 3-574
 GETPAS = 104415 #3-641
 GSTAT = 002174 R #3-158 3-189
 GWRUFS = 104414 #3-117
 HARD 005440 R #3-158 3-300
 HCRC 005373 R #3-505 3-510 #3-707
 HDRAD 003622 R #3-477 3-512 #3-240 #3-249 #3-250 #3-253 3-254 #3-256 3-260
 HNF 005367 R #3-262 #3-263 #3-269 #3-271 #3-274 #3-281 #3-292 #3-294 3-298
 HNDICNT 000040 R #3-408 3-519 #3-684
 HNDERS = 104405 #3-481 #3-684
 HRDPAS 000050 R #3-158 3-290 3-338 3-461
 HRDRPT 003024 R #3-158 3-480 #3-509
 HTYPE 005462 R #3-471 #3-470 #3-474 #3-477 #3-481 #3-486 #3-488 3-504 3-509
 ICUNT 000036 R #3-572 #3-719
 ICOUNT 000040 R #3-158
 IDNUM 000122 R #3-158

XRLAGO CREATED BY MACRO ON 2-SEP-82 AT 14:57 PAGE 3 V01
 SYMBOL CROSS REFERENCE VALUE REFERENCES
 IMDDX = 000000 #3-158 3-300
 INDPAR = 000040 #3-158
 INIT 000030 R #3-158
 INTR 000120 R #3-158 #3-162
 INTSRV 002262 R #3-425 #3-431
 KIPKES = 000400 #3-158
 KIXTND = 000000 #3-158
 LIMIT 003530 P #3-158 #3-613
 LOOP 000702 R #3-226 3-323
 LOUPL 000444 R #3-191 3-202 3-317 3-324 3-341 3-552
 LSTBSC 003576 R #3-213 #3-222 3-554 #3-632
 LSTCYL 003602 R #3-215 #3-220 3-260 3-518
 LSTFKA 003604 R #3-216 #3-221 3-294 3-531 #3-635
 MAP225 = 104416 #3-158 3-387
 MASK 003600 R #3-214 3-219 #3-633
 MASK2 003606 R #3-211 #3-223 3-250 #3-636
 MES1 004750 R #3-663 3-699
 MES10 005123 R #3-673 3-703
 MES11 005152 R #3-674 3-723
 MES12 005203 R #3-675
 MES13 005216 R #3-676 3-715
 MES14 005233 R #3-677 3-718
 MES15 005251 R #3-678 3-726
 MES16 005277 R #3-679 3-707
 MES2 004774 R #3-664 3-696
 MES3 005015 R #3-665 3-691
 MES4 005024 R #3-666 3-693
 MES5 005037 R #3-667 3-667
 MES6 005053 R #3-657 #3-668
 MES7 005060 R #3-660 #3-669
 MES8 005065 R #3-659 3-670
 MES8A 005073 R #3-655 #3-671
 MES9 005107 R #3-656 #3-672
 MFLG 003564 R #3-524 #3-525 #3-526 #3-532 3-545 3-586 #3-588 #3-601 #3-602
 MUDNAH 000000 R #3-603 #3-627
 MSG18 = 104403 #3-158 #3-158 #3-160 3-183 3-287 3-345 3-349 3-459 3-492 3-505
 MSGSS = 104402 #3-158 3-510
 MSGS 104401 #3-158
 MUGURV 003614 R #3-158 #3-639
 MUDSC 005315 R #3-680 3-712
 MCPDUP = 000020 #3-158
 MDAPTY 000002 #3-158
 MULEFT 005420 R #3-349 #3-696
 MURPT 003016 R #3-467 3-511 3-608
 MUSEC 005450 R #3-550 #3-712
 NULL = 000000 #3-158
 NULLX 005304 R #3-158 3-654 #3-687
 NUMH 004726 R #3-158 #3-195 #3-200 #3-653 3-692
 NXM 005400 R #3-470 #3-686

[illegible][illegible]

SYMBOL	VALUE	REFERENCES								
T.BA	003552 H	*3-436	*3-622	3-732						
T.MAE	003550 R	*3-367	*3-441	*3-625						
T.CS	003550 R	*3-435	*3-442	*3-449	3-468	3-472	3-475	3-479	3-483	*3-621
		*3-731								
T.DA	003554 R	*3-437	*3-485	3-500	*3-623	3-733				
T.MP	003556 H	*3-717	*3-227	*3-279	*3-438	3-734				
T.SAT	003562 H	*3-368	*3-432	*3-458	*3-626	3-735				
USTACK	= 000001	*3-158								
VECTID	000010 H	*3-158	3-369	3-425						
WASADR	000104 R	*3-158								
WBUFEA	000136 R	*3-158	3-378							
WBUFEA	000134	*3-158	3-377							
WBUFEQ	000140 R	*3-158								
WBUFEZ	000142 H	*3-158	3-301							
WCNIJ	003632 K	*3-301	*3-302	3-402	*3-646					
WCNT2	003634 H	*3-298	3-407	*3-647						
WDRF	000116 K	*3-158	*3-160							
WDTU	000114 R	*3-158	*3-161							
WCHKA	002050 K	3-306	*3-399							
WRIITE	002060 K	3-303	*3-401							
WTHDY	001472	*3-209	3-276	*3-328	3-527	3-540	3-604			
XFLAG	000005 H	*3-158								

RLAGU	CREATED BY	MACHU	IN	Z-SEP-82 AT 14:57	PAGE	7
MACHU	CROSS REFERENCE				REF	V01
MACHU NAME	REFERENCES					
RKMOD	#1-124					
HPSAK	#1-222	3-334	3-455	3-464		
BUD	#1-246					
CKDATA	#1-282	3-312				
CLASP	#1-149					
DATACK	#1-291					
DATERR	#1-175					
DSEVRN	#1-314	3-158	3-158	3-158	3-158	3-158
	#1-156	3-158	3-158	3-158	3-158	3-158
DSEVNT	#1-374	3-158	3-158	3-158	3-158	3-158
END	#1-212	3-151				
ENDIT	#1-203	3-197				
ENDMOU	#1-208					
EQUATS	#1-330	3-158				
EXIT	#1-157	3-427				
GETPA	#1-273	3-189				
GMBUF+	#1-261	3-100				
HLPBRK	#1-277					
HRDER	#1-165	3-290	3-338	3-461		
IOMOU	#1-120					
IOMODP	#1-144					
IOMODH	#1-140					
IOMODX	#1-136	3-158				
MAPZ2	#1-277	3-187				
MODULE	#1-29	3-158				
MSG	#1-191					
MSGN	#1-195	3-180	3-183	3-287	3-345	3-349
	#1-510	3-550				
MSGS	#1-199					
NHKMOD	#1-132					
OTDA	#1-232					
PIRJ	#1-216	3-831				
RANMU	#1-161	3-235				
SHKMUU	#1-128					

PCSB DEC/X11 SYSTEM EXERCISER MODULE
XPCSR0.P11 12-OCT-78 12:05

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SEQ 0001

.REM _

IDENTIFICATION

PRODUCT CODE: AC-E962B-MC
PRODUCT NAME: CXPCSR0 PCS-11 MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT
PCS IS AN IOMOD THAT EXERCISES PCS CONTROLLER AND
FILE BOX. IT WILL EXERCISE ALL BITS OF CSR ADDRESS,
CHECK FOR MAINTENANCE INTERRUPT CAPABILITIES AND
CHECK ALL THE ADDRESSES FROM 171000 TO 171375 IN
MAINTENANCE MODE.
2. REQUIREMENTS
HARDWARE: IOCM CONTROL MODULE WITH FILE BOX
STORAGE:: PCS REQUIRES:
 1. DECIMAL WORDS: 748
 2. OCTAL WORDS: 1354
 3. OCTAL BYTES: 2730
3. TEST DESCRIPTION
ONE PASS OF THE MODULE CONSISTS OF CSR CHECKS,
INTERRUPT TEST AND IAR TEST IN MAINTENANCE MODE.
DURING THE TEST DBIT IS SET SO THE TEST WILL NOT
AFFECT I/O MODULES
4. EXECUTION TIME
ONE PASS OF THE TEST TAKES LESS THEN 1 SEC
5. CONFIGURATION REQUIREMENTS
DEFAULT PARAMETERS
DEVADR: 171376
VECTOR: 234
DEVCNT: 1
6. MODULE OPERATION
TEST SEQUENCE
 - A. SET UP THE DEVICE ADDRESSES
 - B. SET G BIT, READ IT BACK AND CLEAR IT
 - C. SET D BIT, READ IT BACK AND CLEAR IT
 - D. SET T BIT, READ IT BACK AND CLEAR IT
 - E. SET RIF BIT, READ IT BACK AND CLEAR IT
 - F. SET F BIT, READ IT BACK AND CLEAR IT
 - G. SET W BIT, READ IT BACK AND CLEAR IT
 - H. SET MBIT AND READ ALL ADDRESSES FROM
 - I. SET MAINTENCE INTERRUPT AND READ ALL ONES FROM IAR
171000 TO 171375
 - J. RESET CSR.
7. OPERATOR OPTIONS:
NONE
8. PRINTOUTS:

PCSB DEC/X11 SYSTEM EXERCISER MODULE
XPCSB0.P11 12-OCT-78 12:05

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SEQ 0003

ALL PRINTOUTS ARE STANDARD

-

PCSB
XPCSB

PCSB
XPCSB

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104 000000
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106 000000
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154 000000
155 000000
156 000000
157 000000
158 000000
159 000000

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041520 041123 040

```

IDMOD <PCSB> 171376 234 0 0 0 10 147
MODULE 140000 PCSB 171376 234 0 0 0 147
TITLE PCSB DEC/X11 SYSTEM EXERCISER MODULE
DDXCOM VERSION 6 23-MAY-78
LIST BIN
*****
REGIN:
MODNAM: .ASCII /PCSB / ;MODULE NAME
XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUF USAGE
ADDR: 171376+0 ;1ST DEVICE ADDR.
VECTOR: 234+0 ;1ST DEVICE VECTOR.
BRI: .BYTE PRTV0+0 ;1ST BR LEVEL.
BR2: .BYTE PRTV0+0 ;2ND BR LEVEL.
DVID1: 0 ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1
SR2: OPEN ;SWITCH REGISTER 2
SR3: OPEN ;SWITCH REGISTER 3
SR4: OPEN ;SWITCH REGISTER 4
*****
STAT: 140000 ;STATUS WORD.
INIT: START ;MODULE START ADDR.
SPDINT: MODSP ;MODULE STACK POINTER.
PASCNT: 0 ;PASS COUNTER.
ICONT: 10 ;# OF ITERATIONS PER PASS=10
ICOUNT: 0 ;LOC TO COUNT ITERATIONS
SOPCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
SOPAS: 0 ;LOC TO SAVE TOTAL HARD ERRORS
HROPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
SYSCNT: 0 ;LOC TO SAVE HARD ERRORS PER PASS
RANNUM: 0 ;# OF SYS ERRORS ACCUMULATED
CONFIG: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
RESA: 0 ;RESERVED FOR MONITOR USE
SVRO: OPEN ;LOC TO SAVE R0.
SVRI: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVR5: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSR.
SBAOR: OPEN ;ADDR OF GOOD DATA, OR
ACSR: OPEN ;CONTENTS OF CSR.
WASADR: OPEN ;ADDR OF BAD DATA OR
ASTAT: OPEN ;STATUS REG CONTENTS.
ERRTYP: OPEN ;TYPE OF ERROR
ASB: OPEN ;EXPECTED DATA.
AWAS: OPEN ;ACTUAL DATA.
RSTRT: RESTR ;RESTART ADDRESS AFTER END OF PASS
WDT: OPEN ;WORDS TO MEMORY PER ITERATION
WDF: OPEN ;WORDS FROM MEMORY PER ITERATION
INTR: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 147 ;MODULE IDENTIFICATION NUMBER=147
.REPT SPSIZ ;MODULE STACK STARTS HERE.

```

```

160
161
162
163
164 000224
165
166

```

```

.NLIST
.WORD 0
.LIST
.ENDR
MODSP:
*****

```

```
167 000224* 000300 TEMP1: -WORD 0 ;TEMPORARY STORAGE
168 000226* 000300 TEMP2: -WORD 0 ;TEMPORARY STORAGE
169 000228* 000300 TEMP3: -WORD 0 ;TEMPORARY STORAGE
170 000230* 000300 CSR: -WORD 0 ;ADDRESS OF CSR = 171377
171 000232* 000300 IAR: -WORD 0 ;ADDRESS OF IAR = 17136
172 000234* 000300 INTFLG: -WORD 0 ;INTERRUPT OCCURED FLAG
173 000236* 000300 CNT: -WORD 0 ;PASS COUNT
174 000238* 000300 BASE: -WORD 0 ;ADDRESS 171000
175 000240* 000300 VECT2: -WORD 0 ;SECOND ADDR OF VECTOR
176
177 000246* 012767 000001 177644 START: MOV #1,INTR ; ONE INTERRUPT/ITERATION
178 000248* 012767 000010 177626 MOV #8,WOTO ;8. WORDS TO NEW/ITERATION
179 000250* 012767 000010 177626 MOV #8,WDFR ;8. WORDS FROM NEW/ITERATION
180 000252* 005267 177744 CLR CNT ;CLEAR PASS COUNT
181 000254* 012700 177506 RESTR: MOV ADDR,R0 ;SET ADDRESS OF IAR = 171376
182 000256* 012700 177730 MOV R0,IAR ;SET ADDRESS OF CSR = 171377
183 000258* 012700 177720 MOV R0,CSR ;SET ADDRESS OF THE LOWEST ID = 171000
184 000260* 045200 000377 RIC #377,R0
185 000262* 012767 177720 MOV #377,R0
186
187 000322* 012703 000033 15: MOV #9,*3,R3 ;SET UP WAIT LOOP
188 000324* 152777 000002 177676 BISB #2,*CSR ;SET CRIT TO CLEAR CSR
189 000326* 005240 000002 32$: NOP ;WAIT FOR CLEAR
190 000328* 005240 000002 DEC R3
191 000330* 001375 000044 33$: BNE 32$ ;ANOTHER WAIT LOOP
192 000332* 012703 000042 177656 MOV #12,*3,R3
193 000334* 152777 000042 34$: BISB #2,*CSR
194 000336* 005240 000042 NOP
195 000338* 001375 177644 DEC R3
196 000340* 005240 177512 RNE 34$ ;IS CSR CLEAR?
197 000342* 105777 177536 TSTR *CSR
198 000344* 001407 177536 BEQ 25 ;NO, SET BAD DATA FOR ERROR CALL
199 000346* 117767 177534 MOVR *CSR,AWAS ;SET GOOD DATA.
200 000348* 005067 CLR ASB
201
202 000402* 104404 000000* DATERS,BEGIN ;DATA ERROR!!!
203
204
205
206
207
208
209
210 000406* 152777 000001 177616 ;TEST
211 000408* 132777 000001 177610 2$: GBIT
212 000410* 001317 177602 177450 BISB #1,*CSR ;SET RIF BIT
213 000412* 117767 177574 177440 RITB #1,*CSR ;IS IT SET
214 000414* 016767 000000* 002058- BNE 35 ;YES, GO TO 35
215 000416* 104403 000000* 002058- MOVB *CSR,ACSR ;LOAD CONTENTS OF CSR FOR ERROR
216 000418* 012767 000000* 002058- MOV *CSR,CSRA ;LOAD ADDRESS OF CSR FOR ERROR
217 000420* 104403 000000* 002058- MSGNS,BEGIN,RIFNOT ;ASCII MESSAGE CALL WITH COMMON HEADER
218
219 000446* 012767 000025 177432 MOV #25,ERRTYP ;BIT STUCK
220
221 000454* 104405 000000* 000000 HRDERS,BEGIN,NULL ;RIF BIT IS NOT SETTING
222
223
224 000462* 132777 000001 177542 3$: BITB #1,*CSR ;NOW RIF BIT SHOULD BE CLEAR
225 000470* 001417 BEQ 45 ;YES GO TO 45
```

```
223 000472* 117767 177534 177402 MOVR *CSR,ACSR
224 000474* 016767 177526 177392 MOV *CSR,CSRA
225 000476* 104403 000000* 002062- MSGNS,BEGIN,RIFCLR ;ASCII MESSAGE CALL WITH COMMON HEADER
226 000478* 012767 000025 177364 MOV #25,ERRTYP ;BIT STUCK
227
228 000522* 104405 000000* 000000 HRDERS,BEGIN,NULL ;RIF BIT IS NOT CLEARING
229
230
231
232 000530* 152777 000004 177474 4$: BISB #4,*CSR ;SET CRIT AT CSR
233 000532* 132777 000004 177466 RITB #4,*CSR ;TEST IF SET
234 000534* 001017 000004 177466 BNE 55 ;YES, GO TO 55
235 000536* 117767 177460 177326 MOVB *CSR,ACSR
236 000538* 016767 177452 177316 MOV *CSR,CSRA
237 000540* 104403 000000* 002066- MSGNS,BEGIN,GBITS ;ASCII MESSAGE CALL WITH COMMON HEADER
238 000542* 012767 000025 177310 MOV #25,ERRTYP ;BIT STUCK
239
240 000576* 104405 000000* 000000 HRDERS,BEGIN,NULL ;GENERIC RIT IS NOT SETTING
241
242
243
244 000604* 142777 000004 177420 5$: RITB #4,*CSR ;CLEAR GBIT AT CSR
245 000606* 001417 000004 177412 BEQ 65 ;TEST IF CLEAR
246 000608* 117767 177404 177252 MOVR *CSR,ACSR ;YES, GO TO 65
247 000610* 016767 177376 177242 MOV *CSR,CSRA
248 000612* 104403 000000* 002072- MSGNS,BEGIN,GBITC ;ASCII MESSAGE CALL WITH COMMON HEADER
249 000614* 012767 000025 177234 MOV #25,ERRTYP ;BIT STUCK
250
251 000652* 104405 000000* 000000 HRDERS,BEGIN,NULL ;GENERIC RIT IS NOT CLEARING
252
253
254
255 000660* 152777 000020 177344 6$: BISB #20,*CSR ;SET DBIT AT CSR
256 000662* 132777 000020 177336 RITB #20,*CSR ;TEST IF SET
257 000664* 001417 000020 177336 BNE 75 ;YES, GO TO 75
258 000666* 117767 177330 177176 MOVB *CSR,ACSR
259 000668* 016767 177322 177166 MOV *CSR,CSRA
260 000670* 104403 000000* 002076- MSGNS,BEGIN,DBITS ;ASCII MESSAGE CALL WITH COMMON HEADER
261 000672* 012767 000025 177160 MOV #25,ERRTYP ;BIT STUCK
262
263 000726* 104405 000000* 000000 HRDERS,BEGIN,NULL ;DBIT IS NOT SETTING
264
265
266
267 000734* 152777 000010 177270 7$: BISB #10,*CSR ;SET TBIT *CSR
268 000736* 132777 000010 177262 RITB #10,*CSR ;IS IT SET
269 000738* 001017 000010 177262 BNE 85 ;YES, CONT.
270 000740* 117767 177254 177176 MOVR *CSR,ACSR
271 000742* 016767 177246 177176 MOV *CSR,CSRA
272 000744* 104403 000000* 002102- MSGNS,BEGIN,TBITS ;ASCII MESSAGE CALL WITH COMMON HEADER
273 000746* 012767 000025 177104 MOV #25,ERRTYP ;BIT STUCK
274
275 001002* 104405 000000* 000000 HRDERS,BEGIN,NULL ;TBIT NOT SETTING
276
277
278 001010* 142777 000010 177214 8$: RITB #10,*CSR ;CLEAR TBIT
279 001012* 132777 000010 177206 BEQ 95 ;IS IT CLEAR
280 001014* 001417 177200 177046 MOVB *CSR,ACSR
```

```
279 001034* 016767 177172 177036- MOV CSR,CSRA
280 001042* 014403 000000- MSGNS,BEGIN,MBIT ;ASCII MESSAGE CALL WITH COMMON HEADER
281 001050* 012767 000025 177030- MOV #25,ERRTYP ;BIT STUCK
282 ***** ;*****
283 001056* 104405 000000* 000000- HRDRS,BEGIN,NULL ;TBIT NOT CLEARING
284 ***** ;*****
285
286 001064* 012703 000025 9S: MOV #7,*3,R3 ;SET UP WAIT LOOP
287 001070* 142777 000020 177134 50S: BICR #20,@CSR ;CLEAR DBIT
288 001100* 000000- ;WAIT FOR CSR
289 001102* 001375- DEC P3
290 001104* 132777 000020 177120- BNE 50S
291 001112* 001420- BITB #20,@CSR
292 001114* 117767 177110 176756- BEQ 10S
293 001120* 016767 177102 176746- MOV #CSR,ACSR
294 001132* 014403 000000- MSGNS,BEGIN,DBIT ;ASCII MESSAGE CALL WITH COMMON HEADER
295 001140* 012767 000025 176740- MOV #25,ERRTYP ;BIT STUCK
296 ***** ;*****
297 001146* 104405 000000* 000000- HRDRS,BEGIN,NULL ;DBIT NOT CLEARING
298 ***** ;*****
299
300 001154* 152777 000040 177050 10S: RISR #40,@CSR ;SET MBIT
301 001162* 132777 000040 177042- BITB #40,@CSR ;IS IT SET
302 001170* 001417- BNE 10S
303 001172* 177034 176702- MOV #CSR,ACSR ;NO
304 001200* 016767 177026 176772- MOV CSR,CSRA
305 001206* 014403 000000- MSGNS,BEGIN,MBITS ;ASCII MESSAGE CALL WITH COMMON HEADER
306 001214* 012767 000025 176664- MOV #25,ERRTYP ;BIT STUCK
307 ***** ;*****
308 001222* 104405 000000* 000000- HRDRS,BEGIN,NULL ;MAINTENANCE BIT IS NOT SETTING
309 ***** ;*****
310
311 001230* 142777 000040 176774 11S: BICR #40,@CSR ;CLEAR MBIT
312 001236* 132777 000040 176766- BITB #40,@CSR ;IS IT CLEAR
313 001244* 001417- BEQ 12S
314 001246* 176767 176726 176726- MOV #CSR,ACSR ;NO
315 001252* 014403 000000- MSGNS,BEGIN,MBIT ;ASCII MESSAGE CALL WITH COMMON HEADER
316 001270* 012767 000025 176610- MOV #25,ERRTYP ;BIT STUCK
317 ***** ;*****
318 001276* 104405 000000* 000000- HRDRS,BEGIN,NULL ;MAINTENANCE BIT IS NOT CLEARING
319 ***** ;*****
320
321 001304* 152777 000100 176720 12S: RISR #100,@CSR ;SET EBIT
322 001312* 132777 000100 176712- BITB #100,@CSR
323 001320* 001017- BNE 13S
324 001322* 176704 176552- MOV #CSR,ACSR
325 001330* 016767 000000- MSGNS,BEGIN,EBIT ;ASCII MESSAGE CALL WITH COMMON HEADER
326 001336* 012767 000025 176534- MOV #25,ERRTYP ;BIT STUCK
327 ***** ;*****
328 001352* 104405 000000* 000000- HRDRS,BEGIN,NULL ;INTERRUPT ENABLE BIT IS NOT SETTING
329 ***** ;*****
330
331
332
333
334
```

```
335 001360* 142777 000100 176644 13S: RISR #100,@CSR ;CLEAR EBIT
336 001366* 132777 000100 176636- BITB #100,@CSR
337 001374* 001417- BEQ 14S
338 001376* 117767 176630 176476- MOV #CSR,ACSR
339 001404* 016767 176622 176466- MOV CSR,CSRA
340 001412* 014403 000000- MSGNS,BEGIN,EBIT ;ASCII MESSAGE CALL WITH COMMON HEADER
341 001420* 012767 000025 176460- MOV #25,ERRTYP ;BIT STUCK
342 ***** ;*****
343 001426* 104405 000000* 000000- HRDRS,BEGIN,NULL ;INTERRUPT ENABLE BIT IS NOT CLEARING
344 ***** ;*****
345
346
347
348 001434* 112777 000040 176570 14S: MOV #40,@CSR ;SET MBIT
349 001442* 016702 176574- MOV BASE,R2
350 001446* 005000- CLR R0
351 001450* 005001- CLR P1
352 001452* 152777 000001 176552 15S: BISR #CSR,R1 ;SET RIF BIT
353 001462* 014403 177400- MOV #177400,R1 ;READ ADDRESS 171000+RC AND
354 ***** ;*****
355 ;THIS TEST WILL CHECK ALL ADDRESSES WITH MBIT SET
356
357 001466* 020001- CMP R0,R1 ;R1 SHOULD BE EQUAL R2
358 001470* 016702 176410- BEQ 16S ;YES
359 001472* 010167 176406- MOV R1,ASR ;NO, SAVE GOOD DATA
360 001474* 016767 176534 176372- MOV BASE,ACSR ;SAVE BAD DATA
361 001510* 150067 176366- BISR #CSR,ACSR ;SAVE ADDRESS
362 001514* 016767 176512 176356- MOV CSR,CSRA
363 ***** ;*****
364 001522* 104404 000000* 16S: HRDRS,BEGIN ;DATA ERROR!!!
365 ***** ;*****
366 INC R0 ;GO TO NEXT ADDRESS
367 001530* 127700- CMPB #376,R0 ;IS IT LAST ONE
368 001534* 001346- BNE 15S ;NO, DO IT AGAIN
369
370
371 ;THIS TEST CHECKS MAINTENANCE INTERRUPT
372 ;IF MBIT & EBIT ARE SET, ICM GENERATES
373 ;INTERRUPT AT ADDRESS 234 AND IAR HAS
374 ;UPPER BYTE OF CSR ADDRESS (377)
375
376 001536* 012777 001672 176244 17S: MOV #20S,@VECTOR ;SET INTERRUPT VECTOR
377 001544* 012767 176240 176472- MOV VECTOR,VECT2 ;SET VECTOR + 2
378 001552* 000002 176464- ADD #2,VECT2
379 001560* 116777 176226 176456- MOV R1,VECT2 ;SET PRIORITY LEVEL ON INTERRUPT
380 001566* 005067 176444- CLR INTFLG ;CLEAR INTERRUPT FLAG
381 001572* 012767 000005 176424- MOV #5,TEMP1 ;SET COUNT FOR TIMEOUT
382 001600* 152777 000140 176424- RISR #140,@CSR ;ENABLE INTERRUPT
383
384 001606* 18S: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
385 001612* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
386
387 001616* 005767 176414- TST INTFLG ;INTERRUPT OCCUR?
388 001624* 005364 176374- BNE 21S ;YES, 21S
389 DEC TEMP1 ;NO, IS IT TIMEOUT
390
```

```
391 001630* 001366 BNE 1RS ;NO, LOOP TO BREAK
392
393 001632* 117767 176374 176242 MOVB @CSR,ACSR
394 001640* 116767 176366 176234 MOV @CSR,CSRA
395 001640* 104403 000000 176234 MSCNS,BEGIN,NOINT ;ASCII MESSAGE CALL WITH COMMON HEADER
396 001654* 012767 000000 176224 MOV #2,ERRTYP ;NO INTERRUPT
397 *****
398 001662* 104405 000000 000000 HDRERS,BEGIN,NULL ;NO INTERRUPT
399 *****
400
401 001670* 000444 BR 22$
402
403 001672* 142777 000100 176332 20$: BICB #100,@CSR ;CLEAR INTERRUPT ENABLE
404 001700* 005267 176332 INC INTFLG ;GET INTERRUPT FLAG
405 001704* 000002 RTI
406
407 001706* 152777 000001 176316 21$: BISB #1,@CSR ;SET RIF RIT
408 001714* 117700 176314 MOVB @IAR,R0 ;CHECK IF IAR = 377
409 001720* 122700 000377 CMPR #377,R0
410 001724* 001426 BEQ 22$ ;YES, GO TO END
411 001726* 116367 176156 MOV R0,AWAS ;NO, SAVE BAD DATA
412 001732* 012767 000377 MOV #377,ASB ;SAVE GOOD DATA
413 001740* 117767 176274 MOVB @IAR,ACSR ;SAVE ADDRESS
414 001740* 117767 176274 MOVB @IAR,CSRA
415 001754* 104403 000000 002140 MSCNS,BEGIN,IARERR ;ASCII MESSAGE CALL WITH COMMON HEADER
416 001762* 012767 000001 176116 MOV #1,ERRTYP ;DATA ERROR
417 *****
418 001770* 104405 000000 000000 HDRERS,BEGIN,NULL ;WRONG DATA IN IAR AFTER INTERRUPT
419 *****
420 DATERS,BEGIN ;DATA ERROR!!!
421 *****
422
423 002002* 012703 000041 22$: MOV #11,*3,R3 ;SET UP WAIT LOOP
424 002006* 152777 000002 176216 RISR #2,ACSR ;CLEAR CSR
425 60$: NOP ;WAIT
426
427 002010* 005303 DEC R3
428 002020* 001375 BNE 60$ ;WAIT SOME MORE
429 002020* 000000 176176 65$: MOV #10,*3,R3 ;SET UP ANOTHER LOOP
430 002024* 152703 000002 8ISR #2,ACSR ;CLEAR AGAIN
431 65$: NOP
432 002030* 005303 DEC R3
433 002040* 001375 BNE 65$
434
435 002042* 104413 000000 23$: ENDITS,BEGIN ;SIGNAL END OF ITERATION.
436 002046* 000167 176222 JMP RSTRT ;MONITOR SHALL TEST END OF PASS
437
438 002050* 000000 FINI:
439 002052* 104410 000000 ENDS,BEGIN ;
440
441 .EVEN
442
443 002056* 002144 RIFNOT: MES1
444 002060* 177777 177777
445
446
```

```
447 002062* 002173 RIFCLR: MES2
448 002064* 177777 177777
449
450 002066* 022223 GBITS: MES3
451 002070* 177777 177777
452
453 002072* 002263 GBITC: MES4
454 002074* 177777 177777
455
456 002076* 002324 DBITS: MES5
457 002100* 177777 177777
458
459 002102* 002350 TBITS: MES6
460 002104* 177777 177777
461
462 002106* 002374 TBITC: MES7
463 002110* 177777 177777
464
465 002112* 002421 DBITC: MES8
466
467 002114* 002446 MBITS: MES9
468 002116* 177777 177777
469
470 002120* 002472 MBITC: MES10
471 002122* 177777 177777
472
473 002124* 002517 EBITS: MES11
474 002126* 177777 177777
475
476 002130* 002563 EBITC: MES12
477 002132* 177777 177777
478
479 002134* 002630 NOINT: MES13
480 002136* 177777 177777
481
482 002140* 002666 IARERP: MES14
483 002142* 177777 177777
484
485 002144* 044522 020106 044502 MES1: .ASCIZ "RIF RIT IS NOT SETTING"
486 002150* 020124 051511 047040
487 002160* 052117 051440 052105
488 002166* 044524 043516 0000
489 002170* 052117 043111 041040
490 002174* 052117 043440 025122
491 002178* 044522 020124 044502
492 002182* 040505 044522 043516
493
494 002222* 0000 047105 051105 MES3: .ASCIZ "GENERIC CODE BIT IS NOT SETTING"
495 002244* 041507 041440 042117
496 002250* 020105 044502 020124
497 002254* 051511 047040 052117
498 002258* 051440 052105 044524
499 002260* 043516 0000
500 002262* 041507 047105 051105 MES4: .ASCIZ "GENERIC CODE BIT IS NOT CLEARING"
501 002270* 041511 041440 042117
502 002276* 020105 044502 020124
```

.EVEN
-END

ACSR	000102R	148#	212*	223*	234*	245*	256*	267*	278*	294*	305*	316*	327*	338*
ADDR	000006R	151#	214*	262*	413*									
ADDR22=	001000R	166#												
ASB	000106R	152#	202*	359*	412*									
ASTAT	000104R	156#												
AWAS	000110R	153#	201*	360*	411*									
BASE	000242R	175#	185*	349	361									
BEGIN	000000R	244#	480	243	248	225	228	236	239	247	250	258	261	269
		365	385	386	395	398	415	418	421	435	439	332	340	343
RIT0	= 0000001	166#												
RIT1	= 0000002	166#												
RIT10	= 0020000	166#												
RIT11	= 0040000	166#												
RIT12	= 0060000	166#												
RIT13	= 0080000	166#												
RIT14	= 0100000	166#												
RIT15	= 0000000	166#												
RIT2	= 0000004	166#												
RIT4	= 0000010	166#												
RIT5	= 0000020	166#												
RIT6	= 0000040	166#												
RIT7	= 0002000	166#												
RIT8	= 0004000	166#												
RIT9	= 0010000	166#												
BREAKS	= 1044007	158#	385	386										
ERR1	= 0000172R	174#	379											
ERR2	= 0000142R	166#												
RTODS	= 104421	166#												
CDAAS	= 104417	166#												
CNT	= 000240R	174#	181*											
CONFIG	= 000000R	166#												
CSR	000232R	174#												
		236#												
		231#	185*	190*	195*	199	201	209*	210	212	213	221	223	224
		265	232	234	235*	242*	243	245	246	253*	254	256	257	264*
		305	267	268	275*	276	278	279	287*	291	294	295	302*	303
		338	306	313*	314	316	317	324*	325	327	328	333*	336	338
		338	348*	352*	358	362*	363	364	368*	371*	372			

PCSB0.P11 12-OCT-78 12:05		CROSS REFERENCE TABLE -- USER SYMBOLS											
GETPAS = 104415	166#												
GMBOF = 104415	166#												
HRDWR = 100044R	131#												
HRDRS = 104405	398#	218	228	239	250	261	272	283	299	310	321	332	343
	418												
HRDPAS = 000050R	143#												
IAR = 000230R	143#	183*	408	413	414								
ICRPR = 002140R	415	482#											
ICONT = 000036R	128#												
ICOUNT = 000040R	129#												
IDNUM = 000122R	158#												
INIT = 000030R	143#												
INITLG = 000235R	147#												
MA2 = 000130R	166#	380*	398	404*									
MA2S = 104415	178*												
MBITC = 002120R	318	470#											
MBITS = 002114R	307	467#											
MBR = 002444R	470	483#											
MBR0 = 002517R	473	536#											
MBR1 = 002563R	476	537#											
MBR2 = 002630R	479	544#											
MBR3 = 002666R	482	548#											
MBR4 = 002173R	484	549#											
MBR5 = 002423R	485	550#											
MBR6 = 002324R	486	550#											
MBR7 = 002350R	489	551#											
MBR8 = 002374R	492	551#											
MBR9 = 002421R	497	552#											
MODNAM = 000000R	112#												
MODSP = 000224R	126	164#											
MSGS = 104403	214	415	225	236	247	258	269	280	296	307	318	329	340
	395												
MSGSS = 104407	120#												
MSGSS = 104407	395	479#											
NOGWT = 002133R	166#	218	228	239	250	261	272	283	299	310	321	332	343
NULC = 000000	398	418											
	143	150	120	121	122	123	124	125	142	143	144	145	146
OPEN = 000000	166#												
OTOAS = 104420	127#												
PASCNT = 000034R	166#												
PIRQS = 000004R	166#												
POPSP = 005720	166#												
POPSP2 = 002620	166#												
PRIV = 000000	166#												
PRIV0 = 000000	166#	117	166#										
PRIV1 = 000040	166#												
PRIV2 = 000100	166#												
PRIV3 = 000140	166#												
PRIV4 = 000300	166#												
PRIV5 = 000340	166#												
PRIV6 = 000500	166#												
PRIV7 = 000340	166#												

XPCSB0.P11 12-OCT-78 12:05		CROSS REFERENCE TABLE -- USER SYMBOLS											
PS = 177776	166#												
PSW = 177776	166#												
PUSH = 005746	166#												
PUSH2 = 024447	166#												
RANDS = 104417	166#												
RANNUV = 000054R	135#												
RESTRT = 000274R	154	182#	437										
RES1 = 000056R	137#												
RES2 = 000060R	138#												
RETCCLR = 003092R	214	447#	443#										
RSTRT = 000117R	154#												
SRADR = 000162R	147#												
SOPCNT = 000042R	130#												
SOPERS = 104406	166#												
SOPPAS = 000046R	130#												
SPOINT = 000032R	126#												
SPSIZ = 000040	11	159											
SRI = 000016R	119#												
SR2 = 000020R	120#												
SR3 = 000024R	121#												
SR4 = 000028R	122#												
SR5 = 000032R	123#												
SR6 = 000036R	124#	178#											
SR7 = 000040R	125#												
SR8 = 000044R	126#												
SR9 = 000048R	127#												
SVR0 = 000062R	139#												
SVR1 = 000064R	140#												
SVR2 = 000066R	141#												
SVR3 = 000070R	142#												
SVR4 = 000074R	143#												
SVR5 = 000078R	144#												
SVR6 = 000076R	145#												
SYSCNT = 000052R	134#												
TBITC = 002106R	220	462#											
TBITS = 002102R	220	464#											
TEMP1 = 000234R	168#	381*	390*										
TEMP2 = 000236R	169#												
TEMP3 = 000230R	170#												
TRPDFD = 000022	166#												
VECTOR = 000010R	171#	376*	377	378*	379*								
VECT2 = 000012R	172#	377*											
WASADR = 000244R	148#												
WDFR = 000116R	156#	180*											
WDT0 = 000114R	155#	179*											
XFLAG = 000005R	113#												

ARS.

000000

000

002730

001

ERRORS DETECTED: 0

DEFAULT GLOBALS GENERATED: 0

XPCSB0,XPCSB0/SOL/CRF:SYM=DDXCOM,XPCSB0

RUN-TIME: 11.2 SECONDS

RUN-TIME RATIO: 26/3=6.7

PCSB DEC/X11 SYSTEM EXERCISER MODULE
XPCSB0.P11 12-OCT-78 12:05

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CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0016

CORE USED: 7K (13 PAGES)

.REM _

IDENTIFICATION

PRODUCT CODE: AC-E959B-MC
PRODUCT NAME: CXAABRO AA11-K MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT:

"AABR" IS A IOMOD THAT EXERCISES THE AA11-K SCOPE CONTROLLER. A CONFIDENCE LOGIC TEST IS EXECUTED ON THE CONTROL/STATUS (DAC0, DAC1, DAC2 AND DAC3 REGISTERS. ALL LOGIC ERRORS ARE REPORTED TO THE CONSOLE TELETYPE. THE MAJOR PORTION OF THIS MODULE IS DEFERRED TO LEVEL 0 SERVICE. A SIX LETTER MESSAGE (AA11-K) WILL BE PLOTTED ON THE SCREEN DURING EXECUTION. IF A 611/613 SCOPE IS CONNECTED, ALTERNATING CHARACTERS MAY APPEAR TO HAVE ALTERNATING INTENSITY LEVELS. WHEN A VRI4 SCOPE IS CONNECTED CHANGING THE CHANNEL SWITCH TO CHANNEL ONE ONLY WILL DISPLAY "A 1 - ". PLACING THE SWITCH TO CHANNEL TWO ONLY WILL DISPLAY "A 1 K".

2. REQUIREMENTS:

HARDWARE: AA11-K INTERFACE WITH A SCOPE DISPLAY INSTALLED
STORAGE:: AAB REQUIRES:
1. DECIMAL WORDS: 453
2. OCTAL WORDS: 0705
3. OCTAL BYTES: 1612

3. PASS DEFINITION:

ONE PASS OF THE AAB MODULE CONSISTS OF DISPLAYING 55,296 POINTS ON THE SCREEN. THIS MEANS THAT 55,296 DATA TRANSFERS OCCURED ON THE UNIBUS.

4. EXECUTION TIME:

VARIES WITH SCOPE DELAY BUT SHOULD TAKE AN AVERAGE OF SIXTY SECONDS TO COMPLETE ONE PASS. WHEN RUNNING ALONE ON AN 11/05.

5. CONFIGURATION PARAMETERS:

DEFAULT PARAMETERS:
DVA: 170416, VCT: 360, BRI: 4

REQUIRED PARAMETERS:
NONE
6. DEVICE OPTION SETUP:

A. TURN ON SCOPE POWER.
B. PLACE CHANNEL SW TO 1 & 2 (IF VR14)
7. MODULE OPERATION:

7.1 TEST SEQUENCE:
 - A. START: USING THE DEVICE ADDRESS, THIS SECTION OF CODE, DETERMINES THE CONTROL, X AND Y POSITION ADDRESSES, AND VECTORS.
 - B. TSTNRG: THIS SECTION OF CODE PERFORMS A CONFIDENCE REGISTER TEST OF THE CONTROL, DAC 0, DAC 1, DAC 2, AND DAC 3 REGISTERS.
 - C. PRIME: IN THIS SECTION, THE FOUR DAC REGISTERS AND CONTROL REGISTERS ARE LOADED. THE SCOPE IS ENABLED AND AN 'EXIT' RETURN TO THE MONITOR.
 - D. AA11K: UPON A SCOPE INTERRUPT, THE PROGRAM WILL RETURN TO THIS CODE. ENTER DEFERRED SERVICE MODE AND TEST FOR A MODE FLAG. IF NO MODE FLAG, REPORT IT AS AN ERROR.

- E. AA11XA: THRU CHAR13: THIS SECTION SELECTS
THE PROPER POINTS TO BE INTENSIFIED
ON THE SCREEN.
- F. CHAR11: IN THIS CODE, THE CHANNEL BIT ALTERNATED
TO DISPLAY EACH CHANNEL.
IF A 611/613 SCOPE IS CONNECTED
ALTERNATING CHARACTERS WILL HAVE
ALTERNATING INTENSITY LEVELS.
- G. CHAR20: IN THIS SECTION, THE PASS COUNT
IS DECREMENTED AND TESTED.
IF IT DID NOT BECOME ZERO, THEN
SELECT ANOTHER CLOCK RATE
AND RESUME COUNTING. UPON
A ZERO PASS COUNT THE CONTROL
AND PRESET REGISTER, ARE
CLEARED AND "ENDPAS" IS
REPORTED.

8. OPERATOR OPTIONS:

- A. LOCATION (VCPASS) CAN BE MODIFIED TO VARY THE NO.
LOOPS THRU TEST BEFORE END OF PASS IS REPORTED.

9. NON-STANDARD PRINTOUTS:

NONE: ALL PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED
IN THE DEC/X11 DOCUMENT


```
178 ;A11-K DEC/X11 EXERCISER MODULE
179 IDMOD <AARB> 170416,360,4,3000,123
180 MODULE 140000,AARB,170416,360,4,3000,123
181 TITLE AAHR DEC/X11 SYSTEM EXERCISER MODULE
182 DDJ70M VERSION 6 23-MAY-78
183 LIST BIN
184 *****
185 BEGIN:
186 MODNAM: -ASCII /AARB / ;MODULE NAME.
187 MODAG: RYTE OPEN ;USED TO KEEP TRACK OF WBOFF USAGE
188 ADDR: 170416+0 ;1ST DEVICE ADDR.
189 VECTUR: 360+0 ;1ST DEVICE VECTOR.
190 RPT: -RYTE PRTV+0 ;1ST RR LEVEL.
191 RPT: -RYTE PRTV+0 ;2ND RR LEVEL.
192 DVID1: +1 ;DEVICE INDICATOR 1.
193 SR1: OPEN ;SWITCH REGISTER 1
194 SR2: OPEN ;SWITCH REGISTER 2
195 SR3: OPEN ;SWITCH REGISTER 3
196 SR4: OPEN ;SWITCH REGISTER 4
197 *****
198 STAT: 140000 ;STATUS WORD.
199 LIMIT: START ;MODULE START ADDR.
200 SPOINT: 40DSP ;MODULE STACK POINTER.
201 PASCNT: 0 ;PASS COUNTER.
202 ICOUNT: 3000 ;# OF ITERATIONS PER PASS=3000
203 SOFCNT: 0 ;LOC TO COUNT ITERATIONS
204 HPDCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
205 SOFPAS: 0 ;LOC TO SAVE TOTAL HARD ERRORS
206 HRDPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
207 SVSCNT: 0 ;LOC TO SAVE HARD ERRORS PER PASS
208 RANNUM: 0 ;# OF SYS ERRORS ACCUMULATED
209 CTRPIC: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
210 RPT1: 0 ;RESERVED FOR MONITOR USE
211 RPT2: 0 ;RESERVED FOR MONITOR USE
212 RPT3: 0 ;LOC TO SAVE R0.
213 RPT4: 0 ;LOC TO SAVE R1.
214 RPT5: 0 ;LOC TO SAVE R2.
215 RPT6: 0 ;LOC TO SAVE R3.
216 RPT7: 0 ;LOC TO SAVE R4.
217 RPT8: 0 ;LOC TO SAVE R5.
218 RPT9: 0 ;LOC TO SAVE R6.
219 RPT10: 0 ;LOC TO SAVE R7.
220 RPT11: 0 ;LOC TO SAVE R8.
221 RPT12: 0 ;LOC TO SAVE R9.
222 CSR: OPEN ;ADDR OF CURRENT CSR.
223 SRADM: OPEN ;ADDR OF GOOD DATA, OR
224 ACSR: OPEN ;CONTENTS OF CSR.
225 MASADP: OPEN ;ADDR OF BAD DATA, OR
226 ASAT: OPEN ;STATUS REG CONTENTS.
227 ERRTP: OPEN ;TYPE OF ERROR.
228 ASR: OPEN ;EXPECTED DATA.
229 AAS: OPEN ;ACTUAL DATA.
230 RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
231 WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
232 WDT1: OPEN ;WORDS FROM MEMORY PER ITERATION
233 INT: OPEN ;# OF INTERRUPTS PER ITERATION
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234 IDNUM: 123 ;MODULE IDENTIFICATION NUMBER=123
235 RPT: SPSIZ ;MODULE STACK STARTS HERE.
236 NLST
237 WDT0 0
238 LIST
239 RNDK
240 WODSP:
241 *****
242
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243 ;A11-K OPERATOR CHANGEABLE LOCATIONS
244
245 000224* 000000 VCPAS: 0
246
247 ; COMMON A11-K DEVICE ADDRESSES
248
249 VCSTAT: 170416 ;SCOPE STATUS REGISTER
250 VCXREG: 170420 ;SCOPE X AXIS REGISTER
251 VCVRREG: 170422 ;SCOPE Y AXIS REGISTER
252 VCDAC2: 170424 ;DAC #2 ADDRESS
253 VCDAC3: 170426 ;DAC #3 ADDRESS
254
255 ;COMMON A11-K DEVICE VECTOR
256
257 VCIV: 360 ;SCOPE INTERRUPT VECTOR
258 VCIVS: 362
259
260 ;NOW SET UP THE ADDRESS AND VECTOR DISPATCH LOC.
261
262 000244* 016767 177566 177752 START: MOV ICNT,VCPAS ;SAVE ICNT
263 000245* 016767 177752 VCPAS ;MUST BE ONE LESS
264 000246* 016767 177634 MOV #245,INTR ;245- INTERRUPTS/ITERATION
265 000247* 016767 177622 MOV #245,WDT0 ;245- WORDS TO MEM/ITERATION
266 000248* 016767 177610 MOV #245,WDT1 ;245- WORDS FROM MEM/ITERATION
267 000249* 016767 177502 MOV ADDR,VCSTAT
268 000250* 016767 177714 ADD #7,VCXREG
269 000251* 016767 177706 ADD #7,VCVRREG
270 000252* 016767 177674 ADD #4,VCVRREG
271 000253* 016767 177670 ADD #6,VCDAC2
272 000254* 016767 177662 ADD #10,VCDAC3
273 000255* 016767 177644 MOV VECTOR,VCIV
274 000256* 016767 177640 MOV VECTOR,VCIVS
275 000257* 016767 177632 MOV VCSTAT,CSRA ;LOAD DEVICE ADDRESS
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283 ;A11-K LOGIC TEST
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285 RESTR: CLR VVCSTAT ;CLEAR STATUS
286 CLR VVCXREG ;CLEAR X
287 CLR VVCVRREG ;CLEAR Y
288 CLR VVCDC2 ;CLEAR DAC #2
289 CLR VVCDC3 ;CLEAR DAC #3
290 CLR FATAL ;CLEAR FATAL ERROR FLAG
291
292 ;NOW TEST DAC#0 REGISTER BITS
293
294 000446* 016767 177556 177424 TSTVRG: MOV VVCXREG,CSRA ;LOAD BUS ADDRESS
295 000447* 016767 010000 177422 MOV #RIT12,ASTAT ;LOAD EXPECTED HIT
296 000448* 006267 177416 1S: ASR ASTAT ;CHANGE THE DATA
297 000449* 001422 177410 9FQ 25 ;RIP IF DONE
298 000450* 016767 177410 177532 MOV ASTAT,VVCXREG ;LOAD DAC # 0
299 000451* 017767 177526 177376 MOV VVCXREG,ACSR ;READ DAC #
300 000452* 026767 177374 177370 CMP ASTAT,ACSR ;COMPARE RESULT
301 000453* 001763 000025 177364 15 ;RIP IF CORRECT
302 000454* 012767 000025 177364 MOV #25,ERRTYP ;BIT STUCK
303 ;*****
304 000522* 104405 000000* 000000 HRDRS,BEGIN,NULL ;DAC # 0 IN ERROR
305 ;*****
306 000530* 005267 001026 INC FATAL ;SET FATAL ERROR FLAG
307 000534* 000240 2S: NOP
308
309 ;NOW TEST DAC #1 REGISTER BITS
310
311 000536* 016767 177470 177334 TSTVRG: MOV VVCVRREG,CSRA ;LOAD BUS ADDRESS
312 000537* 012767 010000 177332 MOV #RIT12,ASTAT ;LOAD EXPECTED HIT
313 000538* 006267 177326 1S: ASR ASTAT ;CHANGE THE DATA
314 000539* 001422 177320 9FQ 25 ;RIP IF DONE
315 000540* 016767 177320 177444 MOV ASTAT,VVCVRREG ;LOAD DAC # 1
316 000541* 017767 177440 177386 MOV VVCVRREG,ACSR ;READ DAC #
317 000542* 026767 177380 177380 CMP ASTAT,ACSR ;COMPARE RESULT
318 000543* 001763 000025 177274 15 ;RIP IF CORRECT
319 000544* 012767 000025 177274 MOV #25,ERRTYP ;BIT STUCK
320 ;*****
321 000612* 104405 000000* 000000 HRDRS,BEGIN,NULL ;DAC # 1 IN ERROR
322 ;*****
323 000620* 005267 000736 INC FATAL ;SET FATAL ERROR FLAG
324 000624* 000240 2S: NOP
```

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325
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327
328 000626 016767 177402 177244 TST2RG: MOV VCDAC3,CSRA ;LOAD BUS ADDRESS
329 000634 012767 010000 177242 MOV #BIT12,ASTAT ;LOAD EXPECTED BIT
330 000642 006267 177236 1S: ASR ASTAT ;CHANGE THE DATA
331 000646 001422 177236 BRQ 25 ;RR IF DONE
332 000650 016777 177230 177356 MOV ASTAT,AVCDAC2 ;LOAD DAC # 2
333 000656 017767 177352 177216 MOV VCDAC2,ACSR ;READ DAC #
334 000664 016767 177214 177210 CMP ASTAT,ACSR ;COMPARE RESULT
335 000672 001763 177210 BRQ 15 ;RR IF CORRECT
336 000674 012767 000025 177204 MOV #25,ERRTYP ;BIT STUCK
337 ***** ;*****
338 000702 104405 000000 000000 HRDEFS,BEGIN,NULL ;DAC # 2 IN ERROR
339 ***** ;*****
340 000710 005267 000646 2S: INC FATAL ;SET FATAL ERROR FLAG
341 000714 000240 NOP
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;NOW TEST DAC #3 REGISTER BITS

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TST3RG: MOV VCDAC3,CSRA ;LOAD BUS ADDRESS
MOV #BIT12,ASTAT ;LOAD EXPECTED BIT
1S: ASR ASTAT ;CHANGE THE DATA
BRQ 25 ;RR IF DONE
MOV ASTAT,AVCDAC3 ;LOAD DAC # 3
MOV VCDAC3,ACSR ;READ DAC #
CMP ASTAT,ACSR ;COMPARE RESULT
BRQ 15 ;RR IF CORRECT
MOV #25,ERRTYP ;BIT STUCK
***** ;*****
HRDEFS,BEGIN,NULL ;DAC # 3 IN ERROR
***** ;*****
2S: INC FATAL ;SET FATAL ERROR FLAG
NOP

TST FATAL ;TEST IF ANY FATAL LOGIC ERRORS
BRQ,PRIME ;DROP MODULE DUE TO DAC REGISTER ERROR
ENDS,BEGIN

PRIME: CLR QVCSTAT ;ENSURE CLEAR STATUS
MOV #A11K,QVCIV ;SET UP LPSVC VECTOR
MOV #B11,QVCIVS ;SET UP
MOV #I01,QVCSTAT ;START DISPLAY AND INTERRUPT ENABLE
EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

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;DISPLAY "A11-K" ON THE SCOPE

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A11K:
PIROS,BEGIN,1S ; QUEUE UP TO CONTINUE AT 1S AND RTI
1S:
TSTR QVCSTAT
BVL A11K
MOV QVCSTAT,ACSR ;LOAD VALUE
CLR QVCSTAT
MOV #11,ERRTYP ;ILLEGAL INTERRUPT
***** ;*****
HRDEFS,BEGIN,NULL ;NO DISPLAY READY FLAG
***** ;*****
ENDS,BEGIN

A11KA: MOV #2000,XPOS ;LOAD X AXIS
MOV #5000,YPOS ;LOAD Y AXIS
MOV #6,CNTR ;SET UP FOR 6 CHARACTERS
TXT1: MOV #CHAR1,QVCIV ;LOAD INTERRUPT VECTOR
MOV #PNTR,AAR2 ;LOAD

;PLOT CHARACTER
CHAR: MOV YPOS,YPT ;INIT POINT
BIC #16,QVCSTAT
MOV XPOS,QVCXREG
MOV YPOS,QVCYREG
CHARS: JIS #10,QVCSTAT ;LOAD STATUS
MOV #5,AAR0 ;MATRIX COUNT <ROW>
CHAR1: MOV #7,AAR1 ;MATRIX COUNT <COLUMN>
MOV #AAR2,AAR3 ;GET CHARACTER
INC AAR2
ROLR AAR3
BPL CHAR13
MOV XPOS,QVCXREG ;LOAD X
MOV YPOS,QVCYREG
EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

CHAR3:
PIROS,BEGIN,1S ; QUEUE UP TO CONTINUE AT 1S AND RTI
1S:
TSTR QVCSTAT
BVL CHAR13
MOV QVCSTAT,ACSR ;LOAD VALUE
CLR QVCSTAT
MOV #11,ERRTYP ;ILLEGAL INTERRUPT
***** ;*****
HRDEFS,BEGIN,NULL ;NO DISPLAY READY
***** ;*****
ENDS,BEGIN

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423 001344- 062767 000070 000214 CHAR13: ADD #70,YPOS ;NEXT POINT
424 001352- 005267 000222 INC AAR1 ;ALL POINTS IN THE COLUMN
425 001356- 001334 BNE CHAR2 ;LOAD NEXT COLUMN
426 001360- 016767 000210 000290 MOV YPT,YPOS ;ADD SCALCE
427 001366- 062767 000070 000170 ADD #70,XPOS ;DONE ALL COLUMN
428 001374- 005267 000176 INC AAR0
429 001400- 001313 BNE CHAR1 ;YES, NEXT CHARACTER
430 001402- 062767 000070 000154 ADD #70,XPOS ;YES, NEXT CHARACTER
431 001410- 062767 000002 000124 ADD #2,CNTR
432 001416- 032777 001000 176602 CHAR11: BIT BIT9,@VCSTAT ;TEST CHANNEL
433 001424- 001004 BNE IS ;YES BRANCH
434 001426- 032777 001000 176572 BIT BIT9,@VCSTAT ;SET CHANNEL
435 001434- 000493 BNE CHAR20
436 001436- 042777 030000 176562 IS: BIT BIT9,@VCSTAT ;CLEAR CHANNEL
437 INC CNTR
438 001444- 005367 000120 CHAR20: DFC CNTR
439 001450- 001017 BNE IS
440 001452- 042777 000002 176546 BIT #2,@VCSTAT ;TIME FOR FINAL ITERATION ?
441 001460- 026767 176540 176352 CMP VCPAS,ICOUNT ;YES BRANCH
442 001466- 001404 BNE IS ;SIGNAL END OF ITERATION.
443 001470- 104413 000000- SEQ ENDITS,REGIN ;MONITOR SHALL TEST END OF PASS
444 INC CNTR
445 001474- 000167 177420 IS: JMP AALLKA
446 001500- 005077 176522 CLR @VCSTAT ;SIGNAL END OF ITERATION.
447 001504- 104413 000000- ENDITS,REGIN ;MONITOR SHALL TEST END OF PASS
448 INC CNTR
449
450 001510- 000167 177434 2S: JMP TXT1
  
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451 ;TEXT FOR THE AALL-K SCOPE OPTION
452 ;TEXT = "AALL-K"
453
454 001514- 001530- TEXT: A
455 001516- 001530- A
456 001520- 001554- N1
457 001522- 001554- N1
458 001524- 001547- DASH
459 001526- 001535- K
460
461 001530- 176 021 021 A: .RYTE 176,21,21,21,176
462 001533- 021 176 024 K: .RYTE 177,10,24,42,101
463 001535- 042 010 111 S: .RYTE 46,111,111,111,62
464 001540- 042 010 010 DASH: .RYTE 0,10,10,10,0
465 001542- 046 111 177 N1: .RYTE 0,102,177,100,0
466 001545- 111 062
467 001547- 000 010
468 001552- 010 000
469 001554- 000 102
470 001557- 100 000
471
472 001562- .EVEN
473
474 001562- 000000 FATAL: 0
475 001564- 000000 XPOS: 0
476 001566- 000000 YPOS: 0
477 001570- 000000 CNTR: 0
478 001572- 000000 YPT: 0
479 001574- 000000 YPT: 0
480 001576- 000000 AAR0: 0
481 001600- 000000 AAR1: 0
482 001602- 000000 AAR2: 0
483 001604- 000000 AAR3: 0
484 001606- 000000 TEMP1: 0
485 001610- 000000 TEMP2: 0
486
487 000001 .END
  
```

AAAR DEC/11 SYSTEM EXERCISER MODULE MACV11 30A(1052) 12-OCT-78 16:15 PAGE 16
XAAR00.P11 12-OCT-78 11:40 CROSS REFERENCE TABLE -- USER SYMBOLS

[illegible]

• ARS.	000000	000
	001612	001

```

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

XAARB0,XAARB0/SOL/CRF:SYM=DDXCOM,XAARB0
RUN-TIME: 1 1 .3 SECONDS
RUN-TIME RATIO: 11/3=3.1
CORE USED: 7K (13 PAGES)

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.REP -

IDENTIFICATION

PRODUCT CODE: AC-F004C-MC
 PRODUCT NAME: CXDRWCO DR11-W MODULE
 PRODUCT DATE: AUG 1982
 MAINTAINER: DEC/X11 SUPPORT GROUP
 AUTHOR: D. BUTENHOF

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1. ABSTRACT

DRW IS AN IOMOD THAT EXERCISES ONE DR11-W. THE DEVICE IS EXERCISED USING THE MAINTENANCE MODE TO TRANSFER A 16 WORD BUFFER. MAINTENANCE MODE IS INTERNAL WRAP-AROUND LOGIC WHICH ELIMINATES THE NEED FOR A USER DEVICE OR PHYSICAL CABLE.

2. REQUIREMENTS

HARDWARE: ONE DR11-W INTERFACE
 STORAGE: DRW REQUIRES:
 1. DECIMAL WORDS: 257
 2. OCTAL WORDS: 0437
 3. OCTAL BYTES: 1076

3. PASS DEFINITION

ONE PASS OF DRW CONSISTS OF TRANSFERRING ONE 16 WORD BLOCK OF DATA 77000(8) TIMES

4. EXECUTION TIME

ONE PASS OF DRW RUNNING ALONE ON A PDP11/04 PROCESSOR TAKES APPROXIMATELY 85 SECONDS.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:
 DEVADR: 172410, VECTOR: 124, BP1: 5, DEVCNT: 1
 REQUIRED PARAMETERS:
 NONE

6. DEVICE/OPTION SET-UP

INSTALL DR11-W (NO PHYSICAL CABLE IS NECESSARY)

7. MODULE OPERATION

TEST SEQUENCE:

- A. SET UP VECTOR AND DEVICE REGISTERS
- B. GENERATE READ-WRITE BUFFER
- C. GENERATE CHECK BUFFER
- D. TRANSFER 16 WORDS IN MAINT. MODE


```

E. COMPARE DATA INPUT - REPORT ERRORS
F. REPEAT 77000 TIMES
G. SIGNAL END OF PASS, RESTART AT A.

IF DEVICE FAILS TO INTERRUPT, AN ERROR MESSAGE WILL OCCUR AND THE
PROGRAM WILL REQUEST THAT THE MODULE BE DROPPED.

8. OPERATION OPTIONS
-----
NONE

9. NON STANDARD PRINTOUTS
-----
NONE: ALL PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN THE
DEC/X11 DOCUMENT.

10.0 READ-WRITE BUFFER DESCRIPTION
-----
SUBROUTINE LODBUF LOADS DRBUF WITH AN INCREMENTING PATTERN
(0,1,2,3,...,15). WHEN XFERS ARE COMPLETE, DRBUF WILL BE
MODIFIED(0,0,2,2,4,4,...,14,14) DUE TO THE ALTERNATING
DATA-DATA SEQUENCE CHARACTERISTIC OF DR11-W MAINTENANCE MODE.

11.0 CHECK BUFFER DESCRIPTION
-----
SUBROUTINE LODCHK LOADS CHKBUF WITH MODIFIED PATTERN
DESCRIBED IN 10.

12.0 BURST OPERATION
-----
CHARACTERISTIC OF DR11-W MAINTENANCE MODE OPERATION, THE XFERS
ARE MADE IN ALTERNATING SINGLE CYCLE & BURST MODE FASHION.
FOR THE FIRST FOUR XFERS THE DR11-W REQUESTS THE BUS ONLY
ONCE AND DOES NOT RELEASE IT UNTIL THE END OF THE FOURTH XFER.
THE NEXT FOUR XFERS SEEKS THE DR11-W REQUESTING & RELEASING THE BUS
FOR EACH XFER. THIS ALTERNATING ACTION CONTINUES FOR SUBSEQUENT XFERS.
  
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*****
*
* EDIT: BY: DATE: REASON:
*
* 001 M DENSMORE 10-NOV-80 FIX LOADING OF CSR'S
*
* 002 R GAUDIN 05-DEC-80 ADD EX11 & PIRG CALLS.
*
* CHANGE "IST" OF CSR TO "CLR"
*
* FIX LOAD OF "WAS" ERR PNTOUT.
*
*****
  
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000000 10000 <DRWC>,172410,124,5,0,0,77000,165 ;MJD001
000000 140000,DRWC,172410,124,5,0,0,77000,165,....
;
; .TITLE DRWC DEC/X11 SYSTEM EXERCISER MODULE
; DDXCUM VERSION 6.4 28-JAN-82
; *****
; BEGIN:
; MODNAM: ASCII /DRWC / :MODULE NAME.
;
; XFLAG: BYTE OPEN ;USED TO KEEP TRACK OF WBUF USAGE
; ADDR: 172410+0 ;1ST DEVICE ADDR.
; VECTOR: 124+0 ;1ST DEVICE VECTOR.
; BK1: BYTE PR15+0 ;1ST BP LEVEL.
; BK2: BYTE PR10+0 ;2ND BP LEVEL.
; DIV10: 0+1 ;DEVICE INDICATOR 1.
; SR1: OPEN ;SWITCH REGISTER 1
; SR2: OPEN ;SWITCH REGISTER 2
; SR3: OPEN ;SWITCH REGISTER 3
; SR4: OPEN ;SWITCH REGISTER 4
; *****
; STAT: 140000 ;STATUS WORD.
; INIT: START ;MODULE START ADDR.
; SPOINT: 400SP ;MODULE STACK POINTER.
; PASCNT: 0 ;PASS COUNTER.
; ICOUNT: 77000 ;# OF ITERATIONS PER PASS=77000
; LOC1: 0 ;LOC TO COUNT ITERATIONS
; SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
; HDRCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
; SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
; HDPPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
; SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
; RANNUM: 0 ;HOLDS RANUM # WHEN RAND MACRO IS CALLED
; COWFIG: 0 ;RESERVED FOR MONITOR USE
; RES1: 0 ;RESERVED FOR MONITOR USE
; RES2: 0 ;RESERVED FOR MONITOR USE
; SVR0: OPEN ;LOC TO SAVE R0.
; SVR1: OPEN ;LOC TO SAVE R1.
; SVR2: OPEN ;LOC TO SAVE R2.
; SVR3: OPEN ;LOC TO SAVE R3.
; SVR4: OPEN ;LOC TO SAVE R4.
; SVR5: OPEN ;LOC TO SAVE R5.
; SVR6: OPEN ;LOC TO SAVE R6.
; CSKA: OPEN ;ADDR OF CURRENT CSR.
; SBADR: ;ADDR OF GOOD DATA, OR
; ACSR: OPEN ;CONTENTS OF CSR.
; ASADR: ;ADDR OF BAD DATA, OR
; ASSTAT: OPEN ;STATUS REG CONTENTS.
; ERRTYP: ;TYPE OF ERROR.
; ASR: OPEN ;EXPECTED DATA.
; AWAS: OPEN ;ACTUAL DATA.
; RSTR1: RSTR1 ;RESTART ADDRESS AFTER END OF PASS
; WDTU: OPEN ;WORDS TO MEMORY PER ITERATION
; WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
; INIR: OPEN ;# OF INTERRUPTS PER ITERATION
; IDNUM: 165 ;MODULE IDENTIFICATION NUMBER=165
; .REPT SP51Z ;MODULE STACK STARTS HERE.
  
```

```

        .NLIST
        .WORD 0
        .LIST
        .ENDR

000224 MODSP:
;*****
175
176
177
178
179
180
181
182
183 000224 000001
184
185
;*****
;+
; THE NEXT LOCATION CONTAINS A NUMBER THAT CAN BE EXAMINED IN CONJ TO BE
; SURE THAT THE LISTING AND ACTUAL CODE ARE THE SAME. THIS LOCATION IS TO
; BE INCREMENTED EACH TIME THE SOURCE CODE IS UPDATED. LOCATION 'MODREV'
; IS NOT USED BY THE PROGRAM.
;=
MODREV: 1+0
;*****
    
```

```

187
188
189
190
191
192
193
194 000226
195 000226 016705 177554
196 000232 010567 000560
197 000236 005725
198 000240 010567 000554
199 000244 005725
200 000246 010567 000550
201 000252 005025
202 000254 010567 000544
203 000260 005715
204 000262 016705 177522
205 000266 010567 000534
206 000272 005725
207 000274 010567 000530
208
209
210
211 000300 016767 077000 000472
212 000306
213 000312 104407 000000
214 000316 012777 177760 000472
215 000324 012767 001032 000450
216 000332 104415 000000 001010
217 000340 016777 000446 000452
218 000346 005067 000362
219 000352 016777 000356 000442
220 000360 012777 177777 000436
221 000366 056767 000422 000340
222 000374 016777 000334 000420
223 000402 012777 000542 000416
224 000410 116777 177376 000412
225 000416 005067 000360
226 000422 004767 000260
227 000426 004767 000216
228 000432 012767 000010 000346
229 000440 005067 000340
230 000444 052767 010000 000262
231 000452 052767 000501 000254
232 000460 016777 000250 000334
233
234
235
236 000466 104400 000000
237
238 000472
239 000476 104407 000000
240 000482 005767 000274
; * * * START OF MODULE CODE * * *
; THIS MODULE TESTS THE DR-11W DIRECT MEMORY ACCESS INTERFACE
; INITIALIZATION FOR (DMA) DR-11W
START:
MOV ADDW,R5 ;GET 1ST ADDRESS
MOV R5,ACR ;SET UP WORD COUNT REG. POINTER
TST (R5)+ ;MAKE CERTAIN REG. CAN BE ACCESSED
MOV R5,BAR ;SET UP BUS ADDRESS REG. POINTER
TST (R5)+ ;ACCESS REG.
MOV R5,CSR ;SET UP CONTROL STATUS REG. POINTER
CLR (R5)+ ;ACCESS REGISTER
MOV R5,DHR ;SET UP DATA BUFFER REG. POINTER
TST (R5) ;ACCESS REG.
MOV VECTOR,R5 ;LOAD DEVICE VECTOR
MOV R5,DRVECU ;SETUP DEV VECTOR POINTER
MOV R5,DRVEC2 ;ACCESS REGISTER
; SETUP PRIORITY POINTER
; DEVICE SERVICE CODE
RESTRT: MOV #77000,INTPSC ;TIMER FOR WHEN TO END PASS
CONT:
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
MOV #20,MACR ;SIXTEEN WORD TRANSFER
MOV DRBUF,VA ;SETUP BASE ADDRESS
GETPAS,BEGIN, VA ;GET PHYSICAL ADDRESS FROM 16-BIT VA
MOV PA,BBAR ;SET UP REAL ADDR.
CLR SCSR ;CLEAR THE SOFTWARE CSR
MOV SCSR,BCSR ;LOAD REAL CSR
MOV #1,DRBP ;SET UP DATA
RIS EA,SCSR ;SET EXTENDED MEM. BITS
MOV SCSR,BCSR ;PUT IN REAL CSR
MOV LDRIN,DRVECU ;SET VECTOR TO SERVICE ROUTINE
MOV BBI,DRVEC2 ;SET PRIORITY
CLR INTFLG ;CLEAR INTERRUPT FLAG
JSR PC,LODRUP ;GENERATE READ AND WRITE BUFFER
JSR PC,LODRUP ;GENERATE CHECKING BUFFER
MOV #10,IMRCN1 ;SET UP INTERRUPT LOOP
CLR IMR ;SETUP TIMER COUNTER
BIS #10000,SCSP ;MAINTENANCE MODE
BIS #501,SCSR ;16 CYCLE AND GO
MOV SCSR,BCSR ;LOAD REAL CSR
; WAIT FOR INTERRUPT CODE
; ADD EXIT MONITOR CALL
EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
TIMER:
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
TST INTFLG ;DID INTERRUPT OCCUR?
    
```

```

240 000506 001402          HEQ      1S          ;IF NOT,CONTINUE IN TIMER
241 000510 000167 000044    JMP      CHECK      ;IF YES,GO CHEC DATA
242 000514 000367 000264    DEC      IMR       ;COUNT MORE TIME;IS TMH FINISHED
243 000520 001364          HNE      TIMER      ;IF NOT,BREAK AGAIN
244 000522 000367 000260    DEC      IMRCNT    ;DECREASE OVRALL COUNT
245 000526 000361          HPL      TIMER      ;GO AGAIN IF MORE TIME
246 000530 004403 000000' 000736' MSGNS,BEGIN,HUNG ;ASCII MESSAGE CALL WITH COMMON HEADER
247 000536 004410 000000'          ENDS,BEGIN
248
249
250
251 000542 000267 000234    DMR:     INC      INTFLG    ;INTERRUPT HAS OCCURRED
252 000546 000577 000250    CLR      RCSR      ;CLR CSH REGISTER
253
254
255 000552 000004 000000' 000472' ; CHANGE RTI TO FIRQ MONITOR CALL
;-----
;PIRQS,BEGIN,TIMER ; QUEUE UP TO CONTINUE AT TIMER AND RTI
;-----
;DATA CHECK CODE
256
257
258
259 000560 012701 001032'    CHECK:  MOV      #DRBUF,R1      ; GET THE BUFFER ADDRESS
260 000564 012702 000010    MOV      #R,R2       ; LOAD THE COUNTER
261 000568 012703 001072'    MOV      #CHKBUF,R3     ; GET CHECK BUFFER ADDRESS
262 000574 022123          1S:     CMP      (R1),(R3)+    ; ARE THE TWO SEQUENTIAL WORDS EQUAL ?
263 000576 001003          BNE      ZS              ; NO, REPORT AN ERROR
264 000600 005302          DEC      R2              ; DONE THE WHOLE BUFFER ?
265 000602 003374          BGT      1S              ; NO, KEEP CHECKING
266 000604 000415          HNE      DONE           ; YES,CHECK FOR ENDPASS
267 000606 016767 000219 177266 2S:     MOV      CSH,ACSR    ;ADDRESS OF CONTROL STATUS REGISTER
268 000614 014367 177270    MOV      -(R3),A#AS    ;ACTUAL DATA
269 000620 010367 177260    MOV      R3,#ASADH    ;ADDRESS OF ACTUAL DATA
270 000624 014167 177256    MOV      -(R1),ASB    ;CORRECT DATA
271 000630 010167 177246    MOV      R1,SHADR    ;ADDRESS OF CORRECT DATA
272 000634 014404 000000'    ;*****
;DATERS,BEGIN ;DATA ERROR!!!
;*****
273 000640
274 000640 014413 000000'    DONE:   ENDS,BEGIN    ;SIGNAL END OF ITERATION.
275 000644 000167 177436    JMP      COUNT    ;MONITOR SHALL TEST END OF PASS
276
277
278 000650 012702 001072'    ;LODCHK: MOV      #CHKBUF,R2      ;LOAD CHKBUF WITH INCREMENTING
279 000654 005001          CLR      R1          ;MODIFIED PATTERN
280 000656 005003          1S:     CLR      R3
281 000660 010422          MOV      R3,(R2)+
282 000662 010322          MOV      R3,(R2)+
283 000664 022701 000002    ADD      #2,R1
284 000670 020127 000020    CMP      R1,#20      ;ARE WE FINISHED?
285 000674 001001          BNE      ZS              ;NO
286 000676 000207          RIS      PC              ;YES
287 000700 062703 000002    2S:     ADD      #2,R3
288 000704 000765          BR      1S
289
290
291 000706 012701 001032'    ;LODBUF: MOV      #DRBUF,R1      ;LOAD DRBUF WITH INCREMENTING PATTERN

```

```

292 000712 005002          CLP      R2
293 000714 005021          CLR      (R1)+
294 000716 005202          1S:     INC      R2
295 000720 020227 000070    CMP      R2,#20      ;ARE WE DONE?
296 000724 001402          BEQ      LDEXIT     ;YES
297 000726 010221          MOV      R2,(R1)+    ;NO,CONTINUE
298 000730 000772          BR      1S
299 000732 000207          LDEXIT: RIS      PC
300
301 000734 000000          SCSH:     0
302 000736 000742'          HUNG:     FAIL      ;SOFTWARE CSH
303 000740 177777          FAIL:     177777
304 000742 045          104          105
000745 126          111          103
000750 105          040          106
000753 101          111          114
000756 105          104          040
000761 124          117          040
000764 111          116          124
000767 105          122          122
000772 125          120          124
000775 045          000
305
306 001000 000000          INTFSC: OPEN
307 001002 000000          INTFLG: OPEN      ;COUNTS FOR WHEN TO ENDPASS
308 001004 000000          IMR:     OPEN
309 001006 000000          IMRCNT: OPEN
310 001010 000000          VA:     OPEN
311 001012 000000          PA:     OPEN
312 001014 000000          EA:     OPEN
313 001016 000000          WCR:     OPEN
314 001020 000000          BAR:     OPEN
315 001022 000000          CSR:     OPEN
316 001024 000000          DBR:     OPEN
317 001026 000000          DRVECU: OPEN
318 001030 000000          DRVEC2: OPEN
319 001032          DRBUF:     .BLKW 20
320 001072          CHKBUF:     .BLKW 20
321
322 000001          .END

```

DRWC DEC/X11 SYSTEM EXERCISER M MACRO M1200 02-SEP-82 14:16 PAGE 4-3
SYMBOL TABLE

ACSR	000102R	CHECK	000560R	ICUNIT	000040R	PDP70	= 010000	SUFERS	= 104406
ADDR	000006R	CHKHUF	001072R	IDNUM	000122R	PIRQS	= 000004	SJFPAS	000046R
ADDR22	= 001000	CKHNGS	= 000001	INDPAP	= 000040	PUPSP	= 005726	SPUNT	000032R
APTHPE	= 000200	CLKPHH	= 000001	INIT	000030R	PUPSP2	= 022626	SPSIZ	= 000040
ASR	000106R	CLKSPS	= 104422	INTFLG	001002R	PRHSS	= 000002	SR1	000016R
ASTAT	000104R	CONEIG	000056R	INTPSC	001000R	PTY	= 000000	SR2	000020R
AUTO	= 000010	CUNT	000306R	INTH	000120R	PTY0	= 000000	SP3	000022R
AWAS	000110R	CSR	001022R	KIPRES	= 000400	PTY1	= 000040	SH4	000024R
BAR	001020R	CSRA	000100R	KXTXND	= 040000	PTY2	= 000100	STAKT	000226R
BEGIN	000000R	DATCKS	= 104411	LDXIT	000732R	PTY3	= 000140	STAT	000026R
BIT0	= 000001	DATERS	= 104404	LODBUF	000706R	PTY4	= 000200	SVR0	000062R
BIT1	= 000002	DBR	001024R	LODBCK	000650R	PTY5	= 000240	SVR1	000064R
BIT10	= 002000	DONE	000640R	MAP22S	= 104416	PTY6	= 000300	SVR2	000066R
BIT11	= 004000	DRHUF	001032R	MODNAM	000000R	PTY7	= 000340	SVR3	000070R
BIT12	= 010000	DRIR	000542R	MODREV	000224R	PS	= 177776	SVR4	000072R
BIT13	= 020000	DRVECO	001026R	MUUSP	000224R	PSW	= 177776	SVR5	000074R
BIT14	= 040000	DRVEC2	001030R	MSGNS	= 104403	PUSH	= 005746	SVR6	000076R
BIT15	= 100000	DVIDI	000014R	MSGSS	= 104402	PUSH2	= 024646	SYSCNT	000052R
BIT2	= 000004	EA	001014R	MSGU	= 104401	PWFLG	= 000002	TIME	000440R
BIT3	= 000010	ECCMEM	= 000100	NCPUDP	= 000020	QMNJ22	= 000010	TIMER	000472R
BIT4	= 000020	ENTITS	= 104413	NCAPTY	= 000002	RANDS	= 104417	TMR	001004R
BIT5	= 000040	ENDS	= 104410	NULL	= 000000	RANUM	000054R	TMRCNT	001006R
BIT6	= 000100	ERRTYP	000106R	OPEN	= 000000	KESTPT	000300R	TRPDFD	= 000023
BIT7	= 000200	EXITS	= 104400	OTOS	= 104420	RES1	000056R	USTACK	= 000001
BIT8	= 000400	FAIL	000742R	PA	001012R	RES2	000060R	VA	001010R
BIT9	= 001000	GETPAS	= 104415	PARPPE	= 002000	RH70	= 001000	VECTOR	000010R
BREAKS	= 104407	GHUFS	= 104414	PASCT	000034R	RSTRT	000112R	WASADR	000104R
BR1	000012R	HRDCNT	000044R	PATCH	001132R	R6	= 0000006	WCR	001016R
BR2	000013R	HRDERS	= 104405	PDPF1	= 000002	R7	= 0000007	WDRP	000116R
BTODS	= 104421	HROPAS	000050R	PDPESI	= 020000	SRADP	000102R	WTD	000114R
CAPRES	= 000004	HUNG	000736R	PDP44	= 100000	SCSH	000734R	XFLAG	000005R
CDATAS	= 104412	ICUNIT	000036R	PDP60	= 004000	SOFNT	000042R		

. AFS. 000000 000
001202 001
ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 13158 WORDS (52 PAGES)
DYNAMIC MEMORY: 19748 WORDS (75 PAGES)
ELAPSED TIME: 00:00:24
XDRWCO, XDRWCO/CR/-SP=DDXC04, XDRWCO

XDRWCO CREATED BY MACRO ON 2-SEP-82 AT 14:16 PAGE 1
SYMBOL CROSS REFERENCE CREF V01

SYMBOL	VALUE	REFERENCES							
ACSR	000102 R	#3-174	#4-267						
ADDR	000006 R	#3-174	4-195						
ADDR22	= 001000	#3-174							
APTHPE	= 000200	#3-174							
ASR	000106 R	#3-174	#4-270						
ASTAT	000104 R	#3-174							
AUTO	= 000010	#3-174							
AWAS	000110 R	#3-174	#4-268						
BAR	001020 R	#4-198	4-216	#4-314					
BEGIN	000000 R	#3-174	4-212	4-212	4-215	4-236	4-238	4-238	4-246
		#4-255	4-272	4-274					
BIT0	= 000001	#3-174							
BIT1	= 000002	#3-174							
BIT10	= 002000	#3-174							
BIT11	= 004000	#3-174							
BIT12	= 010000	#3-174							
BIT13	= 020000	#3-174							
BIT14	= 040000	#3-174							
BIT15	= 100000	#3-174							
BIT2	= 000004	#3-174							
BIT3	= 000010	#3-174							
BIT4	= 000020	#3-174							
BIT5	= 000040	#3-174							
BIT6	= 000100	#3-174							
BIT7	= 000200	#3-174							
BIT8	= 000400	#3-174							
BIT9	= 001000	#3-174							
BREAKS	= 104407	#3-174	4-212	4-212	4-238	4-238			
BR1	000012 R	#3-174	4-223						
BR2	000013 R	#3-174							
BTODS	= 104421	#3-174							
CAPRES	= 000004	#3-174							
CDATAS	= 104412	#3-174							
CHECK	000560 R	#4-241	#4-259						
CHKHUF	001072 R	#4-261	4-278	#4-320					
CKHNGS	= 000001	#3-174							
CLKPHH	= 000001	#3-174							
CLKSPS	= 104422	#3-174							
CONEIG	000056 R	#3-174							
CUNT	000306 R	#4-212	4-275						
CSR	001022 R	#4-200	4-218	4-221	4-231	4-252	4-267	#4-315	
CSRA	000100 R	#3-174							
DATCKS	= 104411	#3-174							
DATERS	= 104404	#3-174	4-272						
DBR	001024 R	#4-202	4-219	#4-316					
DONE	000640 R	#4-266	4-273						
DRHUF	001032 R	4-214	4-259	4-291	#4-319				
DRIR	000542 R	4-222	4-251						
DRVECO	001026 R	#4-205	4-222	#4-317					
DRVEC2	001030 R	#4-207	4-223	#4-318					
DVIDI	000014 R	#3-174							
EA	001014 R	4-220	#4-312						

SYMBOL CROSS REFERENCE

CREF V01

SYMBOL	VALUE	REFERENCES
FCCMEM	= 000100	#3-174
ENDITS	= 104413	#3-174 4-274
ENDS	= 104410	#3-174 4-247
ERRTYP	= 000106 R	#3-174
EXITS	= 104400	#3-174 4-236
FAIL	= 000742 R	#3-174 4-302
GETPAS	= 104415	#3-174 4-215
GWRUFS	= 104414	#3-174
HRCENT	= 000044 R	#3-174
HRDEFS	= 104405	#3-174
HRDPAS	= 000050 R	#3-174
HUNG	= 000736 R	4-246 #4-302
ICUNT	= 000036 R	#3-174
ICOUNT	= 000040 R	#3-174
IDNUM	= 000122 R	#3-174
INDPAR	= 000040	#3-174
INIT	= 000030 R	#3-174
IVFLG	= 001002 R	#3-174 4-239
INTPSC	= 001000 R	#3-174 4-306
INTP	= 000120 R	#3-174
KTPRES	= 000400	#3-174
KXTEND	= 040000	#3-174
LDXILI	= 000732 R	4-296 #4-299
LODRUF	= 000706 R	4-225 #4-291
LODRCHK	= 000650 R	4-226 #4-278
MAP225	= 104416	#3-174
MODNAM	= 000000 R	#3-174
MODREV	= 000224 R	#3-183
MODSP	= 000224 R	#3-174
MSGMS	= 104403	#3-174 4-246
MSGSS	= 104403	#3-174
MSG	= 104401	#3-174
NCPUPP	= 000020	#3-174
NOAPTY	= 000002	#3-174
NULL	= 000000	#3-174
OPEN	= 000000	3-174 3-174 3-174 3-174 3-174 3-174 3-174 3-174
		3-174 3-174 3-174 3-174 3-174 3-174 3-174 3-174
		3-174 3-174 3-174 3-174 3-174 3-174 3-174 3-174
		4-313 4-314 4-315 4-316 4-317 4-318 4-310 4-311
OTOAS	= 104420	#3-174
PA	= 001012 R	4-216 #4-311
PAPREF	= 002000	#3-174
PASCHT	= 000034 R	#3-174
PATCH	= 001132 R	4-321
PDPF11	= 000007	#3-174
PDPF15	= 020000	#3-174
PDP44	= 100000	#3-174
PDP60	= 004000	#3-174
PDP70	= 010000	#3-174
PIRUS	= 000004	#3-174 4-255
POSP	= 005726	#3-174
POSPD2	= 022626	#3-174

SYMBOL CROSS REFERENCE

CREF V01

SYMBOL	VALUE	REFERENCES
PRHMS	= 000002	#3-174
PRTY	= 000000	#3-174
PRTY0	= 000000	#3-174
PRTY1	= 000040	#3-174
PRTY2	= 000100	#3-174
PRTY3	= 000140	#3-174
PRTY4	= 000200	#3-174
PRTY5	= 000240	#3-174
PRTY6	= 000300	#3-174
PRTY7	= 000340	#3-174
PS	= 177776	#3-174
PS*	= 177776	#3-174
PUSH	= 005746	#3-174
PUSH2	= 024646	#3-174
PWFILG	= 000002	#3-174
QMDN22	= 000010	#3-174
RANDS	= 104417	#3-174
RANNUM	= 000054 R	#3-174
RESIRT	= 000300 R	#3-174
RES1	= 000056 R	#3-174
RES2	= 000060 R	#3-174
RM70	= 001000	#3-174
RSTRT	= 000112 R	#3-174
R6	= 000000	#3-174
R7	= 000007	#3-174
SHADP	= 000102 R	#3-174
SCSK	= 000734 R	4-217 #4-220 4-221 #4-229 #4-230 4-231 #4-301
SOFENT	= 000042 R	#3-174
SOFERS	= 104404	#3-174
SOPPAS	= 000046 R	#3-174
SPDINT	= 000032 R	#3-174
SPSIZ	= 000040	#3-174 3-174
SR1	= 000016 R	#3-174
SR2	= 000020 R	#3-174
SR3	= 000022 R	#3-174
SR4	= 000024 R	#3-174
START	= 000026 R	#3-174
STAI	= 000026 R	#3-174
SVR0	= 000062 R	#3-174
SVR1	= 000064 R	#3-174
SVR2	= 000066 R	#3-174
SVR3	= 000070 R	#3-174
SVR4	= 000072 R	#3-174
SVR5	= 000074 R	#3-174
SVR6	= 000076 R	#3-174
SYSCT	= 000052 R	#3-174
TIME	= 000440 R	4-228
TIMER	= 000472 R	4-238 4-243 4-245 4-255
TMR	= 001004 R	4-228 #4-242 #4-244 #4-309
TMRCNT	= 001006 R	4-227 3-174 3-174 #3-174 3-174 3-174 #3-174 3-174
TRPDFD	= 000023	#3-174 3-174 3-174 #3-174 3-174 3-174 #3-174 3-174

SYMBOL CROSS REFERENCE

CREF V01

SYMBOL	VALUE	REFERENCES
HSTACK	= 000001	#3-174 3-174 3-174 #3-174 3-174 3-174 #3-174 3-174 3-174
VA	001010 R	#3-174 3-174 3-174 #3-174 3-174 3-174 #3-174 3-174 3-174
VECTUM	000010 H	#3-174 3-174 3-174 #3-174 3-174 3-174 #3-174 3-174 3-174
WASADR	000104 K	#3-174 3-174 3-174 #3-174 3-174 3-174 #3-174 3-174 3-174
WCH	001016 H	#3-174 3-174 3-174 #3-174 3-174 3-174 #3-174 3-174 3-174
WDFR	000116 R	#3-174 3-174 3-174 #3-174 3-174 3-174 #3-174 3-174 3-174
WDFD	000114 R	#3-174 3-174 3-174 #3-174 3-174 3-174 #3-174 3-174 3-174
XFLAG	000005 R	#3-174 3-174 3-174 #3-174 3-174 3-174 #3-174 3-174 3-174

MACRO CROSS REFERENCE

CREF V01

MACRO NAME	REFERENCES
RRMOD	#1-124
BREAK	#1-222 4-212 4-238
BTOD	#1-246
CKDATA	#1-282
CLKSP	#1-149
DATACK	#1-291
DATERK	#1-175 4-272
DFSEVN	#1-314 3-174 3-174 3-174 3-174 3-174 3-174 3-174 3-174
DSEVNT	#1-324 3-174 3-174 3-174 3-174 3-174 3-174 3-174 3-174
END	#1-212 4-247
ENDIT	#1-203 4-274
ENDMOD	#1-208
EQUATS	#1-330 3-174
EXIT	#1-157 4-236
GETPA	#1-273 4-215
GWBUFF	#1-261
HLFRRK	#1-227
HRDEK	#1-165
IOMOD	#1-120 3-174
IOMODP	#1-144
IOMODR	#1-140
IOMODX	#1-136
MAP22	#1-277
MODULE	#1-29 3-174
ASG	#1-191
MSGN	#1-195 4-246
MSGS	#1-199
NBKMOD	#1-132
JOA	#1-232
PIRQ	#1-216 4-255
RAND	#1-161
SRMOD	#1-128

.REM _

IDENTIFICATION

PRODUCT CODE: AC-F001B-MC
PRODUCT NAME: CXXWFB0 GROSS TMNG MOD
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1.0 ABSTRACT

"KWF" IS A GROSS TIMING ANALYSIS MODULE THAT USES THE KW11-L LINE CLOCK OPTION TO PERFORM AN OVERALL TIMING ANALYSIS ON THE 2040 PDP11 CONSOLE PROCESSOR SYSTEM OR A DN2X SECONDARY P.E. IT WILL BE CONFIGURED IN THE EXERCISER TO BE THE FIRST MODULE INITIALIZED BY THE MONITOR AND SINCE IT IS A "NBKMOD" IT WILL RUN ONLY ONE PASS AND NEVER RUN AGAIN UNTIL THE EXERCISER IS RESTARTED. ITS PURPOSE IS TO ESTABLISH CONFIDENCE IN THE HARDWARE AFFECTING OVERALL SYSTEM TIMING. IF THE TIMING IS FOUND TO BE WITHIN A PRE-ESTABLISHED, EMPIRICALLY DETERMINED LIMIT, THE MODULE WILL ENABLE USE OF THE SYSTEM TIMER MODULE. IF NOT IT WILL REPORT THE ERROR AND DISABLE USE OF THE SYSTEM TIMER MODULE.

2.0 REQUIREMENTS

HARDWARE: ANY PDP11/40 OR 11/34A SYSTEM WITH CORE MEMORY AND A KW11-L LINE CLOCK.

STORAGE:: KWF REQUIRES:

1. DECIMAL WORDS: 355
2. OCTAL WORDS: 0543
3. OCTAL BYTES: 1306

3.0 PASS DEFINITION

ONE PASS OF KWF CONSISTS OF A SINGLE PASS THROUGH THE MODULE CODE TO PERFORM THE REQUIRED DIAGNOSIS AND ANALYSIS.

4.0 EXECUTION TIME

ONE PASS OF KWF RUNNING ON AN 11/40 CPU TAKES APPROXIMATELY ONE MINUTE.

NOTE: THE TIMING INFORMATION IN THE "ENDPAS" MESSAGE FOR THE "KWF" MODULE IS MEANINGLESS SINCE THE TIMER MODULE DOES NOT GET STARTED UNTIL "KWF" REPORTS ITS END OF PASS.

5.0 CONFIGURATION PARAMETERS

DEFAULT PARAMETERS:

DVA: 177546 VCT: 100 BR1: 6 BR2: 0 DVC: 1 SP1: 000037

REQUIRED PARAMETERS:

FOR 50 CYCLE SYSTEMS THE TIMING PARAMETER IN SR1 SHOULD BE SET TO 000030. (APPROX)

FOR CPU'S OTHER THAN THE KD11-A, THE VALUE OF SR1 WILL HAVE TO BE EMPIRACALLY DETERMINED BY RUNNING THIS MODULE ALONE AND NOTING THE INFORMATION PROVIDED IN THE ERROR PRINTOUT.

FOR AN 1134A 60 CYCLE SYSTEM THE TIMING PARAMETER IN SR1 SHOULD BE SET TO 000031.

6.0 DEVICE OPTION SET-UP

NONE REQUIRED

7.0 MODULE OPERATION

- A. VERIFY THAT THE KW11-L MONITOR BIT CAN BE SET BY THE POWER SUPPLY SIGNAL. IF A FAILURE IS DETECTED, REPORT THE ERROR AND DROP THE MODULE.
- B. VERIFY THAT THE KW11-L CAN GENERATE AN INTERRUPT TO THE PROPER VECTOR WHEN ENABLED. IF A FAILURE IS DETECTED REPORT THE ERROR AND DROP THE MODULE.
- C. PERFORM THE TIMING ANALYSIS:
 - 1. SYNC THE LINE CLOCK
 - 2. COUNT THE NO. OF ITERATIONS THROUGH A MONITOR "BREAK" LOOP WHILE WAITING FOR AN INTERRUPT.
 - 3. STORE THE COUNT IN A TABLE
 - 4. REPEAT (1) THRU (3) SIXTEEN TIMES TO STORE 16 COUNTS IN THE TABLE
 - 5. SUM ALL ENTRIES IN THE TABLE AND DIVIDE BY 16 TO GET THE AVERAGE.
 - 6. CHECK THAT THE AVERAGE IDS WITHIN LIMITS
 - 7. IF WITHIN TOLERANCE, ENABLE SYSTEM TIMER TO BE ABLE TO RUN AND REPORT END OF PASS.
 - 8. IF NOT WITHIN TOLERANCE, REPORT THE ERROR AND DROP THE MODULE. (SYSTEM TIMER NOT ENABLED TO RUN)

8.0 OPERATOR OPTIONS

"SR1" AND THE CONTENTS OF LOCATION "TOL" MAY HAVE TO BE MODIFIED FOR UNUSAL CASES TO COMPENSATE FOR A PARTICULAR SYSTEM INSTALLATION. (SR1) IS THE AVERAGE TIMER COUNT AND IF FOUND TO BE 000000 THE MODULE ASSUMES A DEFAULT OF "000037" (KD11-A CPU). IF NOT 000000, THE MODULE TAKES THE CONTENTS OF "SR1" AS THE AVERAGE TIME VALUE. THE LOCATION TAGGED "TOL" IS THE ALLOWABLE TOLERANCE AND IS PROGRAM LOADED AS A "3".

9.0 NON-STANDARD PRINTOUTS

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SEQ 0004

IF A TIMING ANALYSIS ERROR IS DETECTED IT IS REPORTED
VIA AN EXTENDED ERROR PRINTOUT OF FOUR OCTAL NUMBERS:

WWWWW SSSSS HHHHH LLLLL

WHERE: WWWWW IS THE WAS AVERAGE
SSSSS IS WHAT THE AVERAGE SHOULD HAVE BEEN
HHHHH IS THE HIGH LIMIT
LLLLL IS THE LOW LIMIT


```

000000 NBRMOD <KWFB>,177546,100,6,1,162
000000 MODULE 41000,KWFB,177546,100,6,1,162
; TITLE KWFB DEC/X11 SYSTEM EXERCISER MODULE
; DOXCOM VERSION 6 23 MAY-78
; ***** LIST *****
000000 BEGIN:
000000 MODNAM: ASCII / KWFB / ;MODULE NAME.
000000 RFLAS: BYTE OPEN ;USED TO KEEP TRACK OF WBUF USAGE
000000 ADDR: 177546+0 ;1ST DEVICE ADDR.
000000 VECTOR: 100+0 ;1ST DEVICE VECTOR.
000000 BR1: -BYTE PRTY6+0 ;1ST BR LEVEL.
000000 BR2: -BYTE PRTY+0 ;2ND BR LEVEL.
000000 DVID1: +1 ;DEVICE INDICATOR 1.
000000 SR1: OPEN ;SWITCH REGISTER 1
000000 SR2: OPEN ;SWITCH REGISTER 2
000000 SR3: OPEN ;SWITCH REGISTER 3
000000 SR4: OPEN ;SWITCH REGISTER 4
000000 *****
000000 STAT: 41000 ;STATUS WORD
000000 INIT: START ;MODULE START ADDR.
000000 SPOINT: MODSP ;MODULE STACK POINTER.
000000 PASCNT: 0 ;PASS COUNTER
000000 ICONF: 0 ;# OF ITERATIONS PER PASS=1
000000 ICONF: 0 ;LOC TO COUNT ITERATIONS
000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
000000 RANMOD: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
000000 CONFIG: ;RESERVED FOR MONITOR USE
000000 RES1: 0 ;RESERVED FOR MONITOR USE
000000 RES2: 0 ;RESERVED FOR MONITOR USE
000000 SVR0: OPEN ;LOC TO SAVE R0.
000000 SVR1: OPEN ;LOC TO SAVE R1.
000000 SVR2: OPEN ;LOC TO SAVE R2.
000000 SVR3: OPEN ;LOC TO SAVE R3.
000000 SVR4: OPEN ;LOC TO SAVE R4.
000000 SVR5: OPEN ;LOC TO SAVE R5.
000000 SVR6: OPEN ;LOC TO SAVE R6.
000000 CSRA: OPEN ;ADDR OF CURRENT CSP.
000000 SBADR: ;ADDR OF GOOD DATA, OR
000000 ACSR: OPEN ;CONTENTS OF CSR.
000000 BADADR: ;ADDR OF BAD DATA, OR
000000 ASTAT: OPEN ;STATUS REG CONTENTS.
000000 ERRTP: ;TYPE OF ERROR
000000 ASB: OPEN ;EXPECTED DATA.
000000 AMAS: OPEN ;ACTUAL DATA.
000000 RESTR: RESTR ;RESTART ADDRESS AFTER END OF PASS
000000 WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
000000 IDNUM: 162 ;MODULE IDENTIFICATION NUMBER=162
;REPT SPSIZ
;NLST
;MODULE STACK STARTS HERE.

```

```

;WORD 0
;LIST
;ENDR
000224 MODSP:
;*****
;GLOBL CLOCK,CLOCKL,CLOCKP,LCLEAR,PCLEAR ;MONITOR AND TIMER MODULE LOCATIONS
000224 START: BITB #BIT0,CLOCK ;USING A KW11-L TIMER ?
000224 BNE IS ;BRR IF YES
000224 BITB #BIT1,CLOCK ;USING THE KW11-P TIMER ?
000224 BNE IS ;BRR IF YES
000224 ENDS,BEGIN
000224 MOV CLOCKL,R0 ;GET POINTER TO TIMING INFORMATION
000224 MOV #LCLEAR,TCLEAR ;SET PROPER JSR ADDR
000224 BR 3S ;CONTINUE
000224 MOV CLOCKP,R0 ;GET POINTER TO TIMING INFORMATION
000224 MOV #PCLEAR,TCLEAR ;SET PROPER JSR ADDR
000224 TST (R0)+ ;POINT TO LOCATION CONTAINING "TIME"
000224 CLZ ;INIT RUN TIME = 000000
000224 JSR PC,TCLEAR ;CLEAR MODULE PASSTIME TABLE IN CLOCK MODULE
000224 SR1 ;DOES SR1 SPECIFY A TIME ?
000224 BNE RESTR ;BRR IF IT DOES
000224 #3,SR1 ;DEFAULT TO 37(9) - KD11-A CPU
000224 MOV ADDR,CSRA ;SAVE KW11-L ADDRESS
000224 MOV VECTOR,R0 ;GET VECTOR ADDRESS
000224 MOV #KWINT,(R0)+ ;STEER KW INTR'S TO KWINT
000224 MOVB BR1,(R0)
000224 CLR @ADDR
;VERIFY THAT KW MONITOR BIT (BIT07) CAN BE SET BY PCWER SUPPLY SIGNAL
;*****
000350 KWD1: CLR R5 ;INIT TIMER
000350 IS: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
000350 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
000350 DEC R5 ;COUNT TIMER
000350 CNE 15 ;BRR UNTIL TIMER = 0
000350 CMP #00,@ADDR ;DID MONITOR BIT SET?
000350 BEQ KWD2 ;BRR IF YES
000350 MOV @ADDR,ACSR ;SAVE CONTENTS OF LKCSR
000350 MOV #25,BRTVP
;*****
000412 HDRS,BEGIN,NULL ;KW11-L MONITOR BIT WON'T SET
000412 CLRB CLOCK ;DISABLE ALL TIMING CHECKS
000412 ENDS,BEGIN
;VERIFY THAT KW11-L CAN GENERATE AN INTERRUPT PROPERLY
;*****
000430 KWD2: CLR KWFLG1 ;CLEAR SOFTWARE INTR. FLAG
000430 CLR R5 ;INIT TIMER
000430 BIS #100,@ADDR ;SET LKCSR INTR ENAB

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283 000444 042777 000200 177334 1$: BIC #200, @ADDR ;CLEAR MONITOR BIT
284 000452 005767 000544 TST #WFLG1 ;DID KW11-L INTR OCCUR
285 000456 100425 BNE #25 ;BR IF YES
286 000460 104407 000000 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
287 000464 104407 000000 ;THEN CONTINUE AT NEXT INSTRUCTION.
288 000470 005305 DEC R5 ;COUNT TIMER
289 000472 001367 BNE #15 ;BR IF NO TIMEOUT
290 000476 017730 MOV @ADDR,ACSR ;SAVE CONTENTS OF LKCSR
291 000502 017730 CLR @ADDR ;CLEAR INTR ENAR
292 000506 012767 000023 177372 MOV #23,ERRTYP
293 *****
294 000514 104405 000000 000000 HRDRS,BEGIN,NULL ;KW11-L FAILED TO INTR ON TIME
295 *****
296 000522 105067 000000G CLRB CLOCK ;DISABLE ALL TIMING CHECKS
297 000526 104410 000000 ENDS,BEGIN
298 000532 032777 000100 177246 2$: BIT #100, @ADDR ;DID INTR SERVICE CLEAR BIT 06?
299 000536 005767 000372 BNE #R0 ;BR IF YES
300 000540 017724 MOV @ADDR,ACSR ;SAVE CONTENTS OF LKCSR
301 000544 005077 177232 CLR @ADDR ;CLEAR LKCSR
302 000554 012767 000025 177324 MOV #25,ERRTYP
303 *****
304 000562 104405 000000 000000 HRDRS,BEGIN,NULL ;KW11 INTR SERVICE FAILED TO CLR BIT06
305 *****
306 000570 105067 000000G CLRB CLOCK ;DISABLE ALL TIMING CHECKS
307 000574 104410 000000 ENDS,BEGIN
308 *****
309 ;GROSS TIMING ANALYSIS ROUTINE - PERFORMS GROSS CPU/MEMORY TIMING ANALYSIS
310 ;USING THE KW11-L
311 *****
312 000600 004767 000332 KWDT3: JSR PC,CLRTAB ;GO CLEAR LOOP COUNTERS TABLE
313 000604 005067 000332 KWSYMC: MOV #CTR1AB,R1 ;R1 POINTS TO LOOP COUNTERS TABLE
314 000608 005067 000406 CLR #WFLG1 ;INIT SOFTWARE INTR. FLAG
315 000612 005000 CLR R0 ;INIT TIMER
316 000616 052777 000100 177162 BIS #100, @ADDR ;ENABLE INTR
317 000620 005767 000372 1$: TST #WFLG1 ;INTR OCCUR YET?
318 000624 100425 BNE #25 ;BR IF YES
319 000628 104407 000000 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
320 000632 104407 000000 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
321 000636 104407 000000 DEC R5 ;COUNT TIMER
322 000640 005305 BNE #15 ;BR IF NO TIMEOUT
323 000644 017730 MOV @ADDR,ACSR ;SAVE CONTENTS OF LKCSR
324 000648 005077 177226 CLR @ADDR ;CLEAR OUT LKCSR
325 000652 012767 000023 177220 MOV #23,ERRTYP
326 *****
327 000666 104405 000000 000000 HRDRS,BEGIN,NULL ;KW11-L TIMEOUT
328 *****
329 000674 105067 000000G CLRB CLOCK ;DISABLE ALL TIMING CHECKS
330 000678 104410 000000 ENDS,BEGIN
331 000682 005067 000312 2$: CLR #WFLG1 ;INIT SOFTWARE INTR FLAG
332 000686 005000 CLR R0 ;INIT LOOP COUNTER
333 000690 005000 000100 177066 BIS #100, @ADDR ;ENABLE KW11-L INTR
334 000694 005767 000372 3$: TST #WFLG1 ;INTR OCCUR YET?
335 000698 100425 BNE #45 ;BR IF YES
336 000702 104407 000000 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....

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337 000732 104407 000000 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
338 000736 005200 INC R0 ;COUNT ONE PASS THRU BREAK LOOP
339 000740 001367 BNE #30, @ADDR,ACSR ;BR IF NO TIMEOUT
340 000744 001367 177040 177132 MOV @ADDR,ACSR ;SAVE CONTENTS OF LKCSR
341 000748 005077 177032 CLR @ADDR ;CLEAR OUT LKCSR
342 000752 012767 000023 177124 MOV #23,ERRTYP
343 *****
344 000762 104405 000000 000000 HRDRS,BEGIN,NULL ;KW11-L TIMEOUT
345 *****
346 000770 105067 000000G CLRB CLOCK ;DISABLE ALL TIMING CHECKS
347 000774 104410 000000 ENDS,BEGIN
348 000778 005067 000130 4$: CMP #CTR1AB+40,R1 ;SAVE LOOP COUNT IN TABLE
349 000782 005067 000130 001306 001306 001306 ;END OF TABLE ??
350 000786 005067 000130 001306 001306 001306 ;GO DO IT AGAIN
351 000790 005067 000130 001306 001306 001306 ;GO COMPUTE TABLE AVERAGE
352 000794 005067 000130 001306 001306 001306 ;R3 CONTAINS AVERAGE COUNT
353 000798 005067 000130 001306 001306 001306 ;GET WHAT AVERAGE COUNT SHOULD BE
354 000802 005067 000130 001306 001306 001306 ;PUT IT IN R2 TOO
355 000806 005067 000130 001306 001306 001306 ;R1 CONTAINS HIGH LIMIT COUNT
356 000810 005067 000130 001306 001306 001306 ;R2 CONTAINS LOW LIMIT COUNT
357 000814 005067 000130 001306 001306 001306 ;LESS THAN HIGH LIMIT?
358 000818 005067 000130 001306 001306 001306 ;BR IF NOT
359 000822 005067 000130 001306 001306 001306 ;GREATER THAN LOW LIMIT?
360 000826 005067 000130 001306 001306 001306 ;BR IF NOT
361 000830 005067 000130 001306 001306 001306 ;SIGNAL END OF ITERATION.
362 000834 005067 000130 001306 001306 001306 ;MONITOR SHALL TEST END OF PASS
363 *****
364 000842 005067 000130 001306 001306 001306 5$: JMP RESTART ;DISABLE USING THE TIMER MODULE
365 000846 005067 000130 001306 001306 001306 ;RESTORE HIGH LIMIT
366 000850 005067 000130 001306 001306 001306 ;POINT TO SAVE AREA
367 000854 005067 000130 001306 001306 001306 ;SAVE WAS COUNTER AVERAGE
368 000858 005067 000130 001306 001306 001306 ;SAVE SHOULD BE COUNTER AVERAGE
369 000862 005067 000130 001306 001306 001306 ;SAVE HIGH LIMIT
370 000866 005067 000130 001306 001306 001306 ;SAVE LOW LIMIT
371 000870 005067 000130 001306 001306 001306 ;SAVE CONTENTS OF LKCSR
372 000874 005067 000130 001306 001306 001306 CLR ERRTYP
373 *****
374 001112 104405 000000 001224 HRDRS,BEGIN,KWMSG ;PRINT COUNTER INFORMATION
375 *****
376 001120 104410 000000 ENDS,BEGIN
377 *****
378 ;GENERAL PUPOSE UTILITY ROUTINES
379 *****
380 ;INTERRUPT SERVICE ROUTINE
381 *****
382 001124 005077 176656 KWINT: CLR @ADDR ;CLEAR OUT LKCSR
383 001130 005167 000066 COM #WFLG1 ;SET SOFTWARE INTR. FLAG
384 001134 000002 RTI ;RETURN TO MAINLINE
385 *****
386 ;ROUTINE TO CLEAR LOOP COUNTER TABLE
387 *****
388 001136 012701 001246 CLRTAB: MOV #CTR1AB,R1 ;POINT TO FIRST TABLE ENTRY
389 001142 005021 CLR (R1)+ ;CLEAR ONE WORD

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395	001144*	022701	001306*
396	001152*	0001374	
397	001152*	000207	
398			
399			
400			
401			
402	001154*	012701	001246*
403	001162*	005002	
404	001162*	005003	
405	001162*	0624102	
406	001162*	000000	
407	001162*	022701	001306*
408	001172*	001173	
409	001172*	005000	
410	001172*	005000	000020
411	001172*	005000	
412	001172*	103402	
413	001172*	005000	
414	001172*	005000	
415	001172*	000207	

```

CMPB     #CTRTAB+40,R1      ;DOONE 16 WORDS?
BNE      #IF NOT
RTS      #RETURN TO TIMING ANALYSIS ROUTINE
;-----
;ROUTINE TO COMPUTE AVERAGE LOOP COUNT
;-----
TIMAVG:  MOV     #CTRTAB,R1      ;R1 POINTS TO 1ST TABLE ENTRY
          CLR     R0              ;INIT 32 BIT ACCUMULATOR
1$:      ADD     (R1)+,R2          ;ADD TABLE ENTRY TO LOW ACCUM.
          ADC     R3              ;ADD IN ANY CARRIES TO HI ACCUM.
          CMP     #CTRTAB+40,R1   ;SUMMED ALL 16 ENTRIES?
          BNE     R0              ;BR IF NOT
          INIT    R0 TO ACCUMULATE AVERAGE
          SUB     #16,R2          ;SUBTRACT CONSTANT 16(10)
          BCC     R0              ;BORROW IF REQUIRED
          BR      R0              ;BR IF OVER SUBTRACT - (WE'RE DONE)
2$:      INC     R0              ;COUNT NO. OF TIMES 16 WENT IN
          GD      SUBTRACT ANOTHER 16(10)
          RTS     R0              ;RETURN - AVERAGE IN R0
;-----
;CONSTANTS, VARIABLES, AND MISCELLANEOUS DATA AREAS
;-----
TCLEAR:  C        ;HOLDS SUBROUTINE ADDR.
TOL:     0         ;TOLERANCE FOR LOOP COUNTER AVG.
RWFLG1:  0         ;SOFTWARE INTERRUPT FLAG
RWMSG:   0         ;POINTS TO WMS AVERAGE
          TIMES+2   ;POINTS TO SHOULD BE AVERAGE
          TIMES+4   ;POINTS TO HIGH LIMIT
          TIMES+6   ;POINTS TO LOW LIMIT
          177777    ;TABLE TERMINATOR
TIMES:   .BLKW     4.      ;RESERVE FOUR WORDS FOR ERROR INFO.
;-----
;16. WORD TABLE TO STORE SIXTEEN LOOP COUNTS
;-----
CTRTAB:  .BLKW     16.
.END

```

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CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0010

[illegible][illegible]

.9EM

IDENTIFICATION

PRODUCT CODE: AC-F998R-MC
PRODUCT NAME: CYOL890 DL11-E MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1.0

ABSTRACT

DXDL89 IS AN IOMOD THAT EXERCISES ONE DL11-E ASYNCHRONOUS COMMUNICATIONS INTERFACE (47880). THE PROGRAM CONSISTS OF TWO MAJOR SECTIONS AS DESCRIBED BELOW:

SECTION ONE:

THE FIRST SECTION CONSISTS OF A LOGICALLY SEQUENCED SET OF STATIC REGISTER TESTS TO VERIFY THE DL11-E HARDWARE EQUIPPED TO PERFORM INPUT/OUTPUT DATA TRANSFERS IN INTERRUPT MODE. ERRORS DETECTED IN THIS SECTION THAT ARE DETERMINED TO BE FATAL ARE REPORTED VIA THE STANDARD DEC/X11 ERROR PRINTOUT AND THEN THE MODULE IS DROPPED FROM THE EXERCISE. NON-FATAL ERRORS ARE SIMPLY REPORTED AND THEN THE PROGRAM CONTINUES IN NORMAL SEQUENCE.

SECTION TWO:

THE SECOND SECTION TRANSFERS 256. BYTE BLOCKS OF DATA USING THE MAINTENANCE MODE TO TURN THE DATA AROUND. THE 256. BYTES OUTPUT ARE COMPARED WITH THE 256. BYTES INPUT FOR DATA COMPARISON ERRORS. ALL DATA COMPARISON ERRORS ARE REPORTED ON THE CONSOLE DEVICE. THE 256. BYTE TRANSFER IS REPEATED FOR FOUR DIFFERENT DATA BIT PATTERNS AS DESCRIBED BELOW:

A. NULL-BIT-NULL SEQUENCE (000,377,000.....000,377)
B. BINARY UP-COUNT SEQUENCE (000,001,002.....376,377)
C. BINARY DOWN COUNT SEQUENCE (377,376,375.....001,000)
D. WORST CASE PATTERN (376,377,001,000.....000,200)

2.0

REQUIREMENTS

HARDWARE: A PDP11 COMPUTER WITH A DL11-E INTERFACE
STORAGE: DL89 REQUIRES:
1. DECIMAL WORDS: 1354
2. OCTAL WORDS: 02512
3. OCTAL BYTES: 5224

3.0

PASS DEFINITION

ONE PASS OF "DXDL89" CONSISTS OF TWO ITERATIONS OF SECTION TWO OF THE MODULE CODE WHICH RESULTS IN 2048(10) BYTES TRANSFERRED.

4.0

EXECUTION TIME

AT 300 BAUD RUNNING ALONE ON A PDP11/40 A SINGLE ERROR FREE

PASS TAKES APPROXIMATELY 40. SECONDS THIS TIME WILL VARY
DEPENDENT UPON THE BAUD RATE AND CPU TYPE.

5.0 CONFIGURATION PARAMETERS

DEFAULT PARAMETERS:

DVA: 175610 VCT: 300 SRI: 4 BR2: 0
DVC: 1 SRI: 0

REQUIRED PARAMETERS:

SRI TO EXERCISE THOSE STATIC TESTS REQUIRING THE USE
OF THE H315 MODEM TEST CONNECTOR (MODERN CONTROL LOGIC)
BIT 15 OF SRI MUST BE SET TO A "1". IF SRI=100000.

NOTE: IF SRI BIT 15=1 AND THE MODERN TEST CONNECTOR
IS NOT INSTALLED, FALSE ERRORS WILL BE REPORTED.

6.0 DEVICE/OPTION SETUP

IF THE MODERN CONTROL LOGIC IS TO BE TESTED, THE USER MUST
DISCONNECT THE MODERN AND CONNECT THE H315 TEST CONNECTOR TO
THE DLT11-C DEVICE CABLE. SRI MUST BE SETUP AS DESCRIBED IN
(5.0) OR THE TESTS WILL BE SKIPPED.

7.0 MODULE OPERATION

7.1 TEST SEQUENCES

A. STATIC REGISTER TESTS

- DLT01: TEST THAT ALL BITS IN THE RCSR ARE CLEAR WHEN
THE MODULE IS INITIALIZED TO RUN.
- DLT02: TEST THAT ONLY THE "RDY" BIT IS SET
IN THE XCSR WHEN THE MODULE IS INITIALIZED TO RUN.
- DLT03: TEST THAT THE "MAINT" BIT IN THE XCSR CAN BE
SET AND CLEARED.
- DLT04: TEST THAT THE "INTR ENAB" BIT IN THE XCSR CAN
CAUSE AN INTERRUPT TO THE PROPER VECTOR WHEN
SET AND ALSO THAT "INTR ENAB" CLEARS PROPERLY.
- DLT05: TEST THAT A RECEIVER INTERRUPT OCCURS TO THE
PROPER VECTOR WHEN "DONE" GETS SET WITH THE
"INTR ENAB" BIT IN THE RCSR SET TO A ONE.
ALSO TEST THAT THE CORRECT DATA IS RECEIVED.

TESTS DLT06 THRU DLT13 ASSUME THAT THE H315 MODERN

TEST CONNECTOR IS INSTALLED. THE USER INDICATES
THIS BY SETTING BIT 15 IN SRI. THE MODULE LOOKS AT
SRI AND WILL SKIP AROUND DLT06 THRU DLT13 IF BIT15=0.

- DLT06: TEST THAT "REQ TO SEND" CAN ASSERT "RING"
WHEN SET AND THAT BOTH "REQ TO SEND" AND "RING"
CAN BE CLEARED PROPERLY.
- DLT07: TEST THAT "SEC XMIT" WHEN SET ASSERTS
"SEC REC" WHICH SETS "DATA SET INT" AND THAT
READING THE RCSR CLEARS "DATA SET INT".
ALSO TESTS THAT CLEARING "SEC XMIT" NEGATES
"SEC REC" WHICH ALSO CAUSES "DATA SET INT" TO
SET.
- DLT10: TEST THAT "DTR" ASSERTS "CLR TO SEND" AND
"CAR DET" WHICH IN TURN SET "DATA SET INT".
ALSO TESTS THAT "CLR TO SEND" AND "CAR DET"
CLEAR WHEN "DTR" IS CLEARED.
- DLT11: TEST THAT "DATA SET INTR ENABLE" CAN BE SET
AND CLEARED.
- DLT12: TEST THAT "DATA SET INTR ENABLE" IN THE XCSR
CAUSES AN INTR. WHEN ENABLED.
- DLT13: TEST THAT THE BREAK BIT IN THE XCSR CAN BE SET
AND CLEARED.

NOTE: BASIC TESTS DLT01 THRU DLT13 ARE EXECUTED
ONLY ONCE WHEN THE MODULE IS FIRST INITIALIZED.
IF ANY FATAL ERRORS ARE DETECTED THE MODULE IS DROPPED
PRIOR TO THE DATA TRANSFER TESTS. AFTER PASS 1 THE
MODULE IS RESTARTED AT THE ENTRY POINT TO THE DATA
TRANSFER TESTS.

B. DATA TRANSFER TESTS

AFTER THE BASIC TESTS ARE RUN, FOUR 256(10)
BYTE DATA TRANSFERS ARE EXECUTED IN THE MAINTENANCE
MODE. EACH 256(10) BYTE BLOCK TRANSFER IS DIFFERENT
IN THAT FOUR DIFFERENT DATA PATTERNS ARE XMITTED AND
RECEIVED AS DESCRIBED IN PARA. 1-0.

THE TEST SEQUENCE FOR THE DATA TRANSFER TESTS IS AS
FOLLOWS:

- 1.) CLEAR BOTH THE INPUT AND OUTPUT BUFFERS IN CORE
(256(10) BYTES EACH).
- 2.) LOAD THE OUTPUT BUFFER WITH THE APPROPRIATE DATA
PATTERN.
- 3.) ENABLE BOTH THE XMIT AND RCVR INTERRUPTS AND
INITIATE THE DATA TRANSFERS.

- 4.) AFTER 256(10) BYTES HAVE BEEN OUTPUT AND INPUT COMPARE THE OUTPUT AND INPUT BUFFERS. BYTE BY BYTE FOR DATA COMPARE ERRORS. REPORT ALL DATA ERRORS ON THE CONSOLE DEVICE.
- 5.) IF ALL FOUR DATA PATTERNS HAVE BEEN TRANSFERRED, GO TO (6) BELOW - IF NOT REPEAT (1) THRU (4) FOR THE NEXT PATTERN.
- 6.) DECREMENT A PASS COUNTER (INITIALIZED TO 2.) AND TEST FOR ZERO. IF ZERO GO TO (7) - IF NOT REPEAT (1) THRU (5) AGAIN.
- 7.) REPORT END OF PASS TO THE MONITOR AND RESTART AT (1) WITH THE FIRST DATA PATTERN.

NOTES:

- (1) ON EACH "XMIT" INTERRUPT THE "READY" FLAG IS TESTED AND IF NOT SET, THE ERROR IS REPORTED AND THE MODULE IS DROPPED. (FALSE INTERRUPTS ARE CLASSIFIED AS FATAL ERRORS).
- (2) ON EACH "RCVR" INTERRUPT THE "DONE" FLAG IS TESTED AND IF NOT SET THE MODULE IS DROPPED THE SAME AS FOR A "XMIT FALSE INTERRUPT".
- (3) IF A SOFT ERROR (PARITY-FRAMING-OVERRUN) IS DETECTED IN RCVR INT. SERVICE, THE OFFENDING BLOCK TRANSFER IS RESTARTED FROM THE BEGINNING OF THE BLOCK. IF AFTER THREE RETRIES THE ERROR PERSISTS TRANSFER OF THE OFFENDING DATA PATTERN IS ABORTED AND THE PROGRAM GOES ON TO THE NEXT DATA PATTERN. ALL SOFT ERRORS ARE REPORTED ON THE CONSOLE DEVICE.

7.2 SUBROUTINE ABSTRACTS

SEGX: THIS SUBROUTINE SERVES AS A MINI-MONITOR THAT CONTROLS THE SEQUENCING OF THE FOUR DIFFERENT 256(10) BYTE BLOCK TRANSFERS. IT IS CALLED AFTER THE BASIC TESTS AND PERFORMS THE FOLLOWING FUNCTIONS:

1. CALLS A SUBROUTINE TO CLEAR THE DATA BUFFERS.
2. CALLS THE APPROPRIATE SUBROUTINE TO SET UP THE OUTPUT BUFFER WITH THE REQUIRED DATA PATTERN.
3. CALLS A SUBROUTINE TO ENABLE INTERRUPTS AND INITIATE THE DATA TRANSFER.
4. SERVICES RETRIES REQUESTED BY SOFT ERRORS.
5. PERFORMS "BREAK" CALLS TO THE MONITOR TO PREVENT TIMEOUTS FROM HANGING THE MODULE.
6. CALLS THE SUBROUTINE TO CHECK THE DATA BUFFERS WHEN THE BLOCK TRANSFER IS COMPLETE.

KICKOF: THIS SUBROUTINE IS CALLED FROM "SEGX" AND CONTAINS THE CODE TO ENABLE INTERRUPTS AND INITIATE THE BLOCK TRANSFER FOR EACH 256(10) BYTE BLOCK TRANSFER.

CHKDAT: THIS SUBROUTINE IS CALLED FROM "SEGX" AND CHECKS FOR DATA COMPARISON ERRORS AFTER EACH BLOCK TRANSFER.

STATK: THIS SUBROUTINE IS CALLED FROM THE BASIC TESTS AND SETS UP THE ERROR INFORMATION FOR ALL ERRORS RELATING TO THE RECEIVER CSR.

STATX: THIS ROUTINE IS CALLED FROM THE BASIC TESTS AND SETS UP THE ERROR INFORMATION FOR ALL ERRORS RELATING TO THE TRANSMITTER CSR.

CLEDBF: THIS ROUTINE IS CALLED FROM "SEGX" AND CLEARS BOTH THE OUTPUT AND INPUT DATA BUFFERS IN CORE.

LDOUT1: THIS ROUTINE IS CALLED FROM "SEGX" AND IS USED TO LOAD THE OUTPUT BUFFER WITH THE NULL-DEL-NULL PATTERN.

LDOUT7: THIS ROUTINE IS CALLED FROM "SEGX" AND IS USED TO LOAD THE OUTPUT BUFFER WITH A BINARY UP-COUNT PATTERN.

LDOUT3: THIS ROUTINE IS CALLED FROM "SEGX" AND IS USED TO LOAD THE OUTPUT BUFFER WITH A BINARY DOWN-COUNT PATTERN.

LDOUT4: THIS ROUTINE IS CALLED FROM "SEGX" AND IS USED TO LOAD THE OUTPUT BUFFER WITH THE MONITOR'S WORST CASE PATTERN.

8.0 OPERATOR OPTIONS

- A. USE THE MOD COMMAND TO MODIFY LOCATION "DLR 16" TO CHANGE SRI. REFER TO PARA. 5.0.
- B. MODIFYING THE CONTENTS OF MODULE LOCATION "RESTRY +2" ALLOWS THE USER TO VARY THE TOTAL NO. OF BYTES TRANSFERRED PER PASS. THIS IS DEFAULTED AT LOAD TIME TO 2 WHICH RESULTS IN 2048. BYTES TRANSFERRED.

9.0 NON-STANDARD ERROR PRINTOUTS

- A. IF ANY ONE OF THE FOUR DATA PATTERNS OUTPUT CANNOT BE SUCCESSFULLY COMPLETED DUE TO SOFT ERRORS (3 RETRIES ATTEMPTED) OR A MONITOR "BREAK" TIMEOUT ONE OF THE FOLLOWING APPROPRIATE PRINTOUTS WILL OCCUR:

MSG1: "NULL-DEL-NULL SEQUENCE ABORTED"
 MSG2: "BINARY UP-COUNT SEQUENCE ABORTED"
 MSG3: "BINARY DOWN-COUNT SEQUENCE ABORTED"

MSG4: "WORST CASE SEQUENCE ABORTED"

B. IF ANY FATAL ERROR DETECTED IN SECTION ONE RESULTS IN A
DECISION TO DROP THE MODULE THE FOLLOWING MESSAGE IS
PRINTED:

"FATAL ERROR DETECTED IN THE STATIC REGISTER TESTS"
AND THE "END" CALL IS EXECUTED TO DROP THE MODULE.

```

;DL11-E DEC/X11 EXERCISER MODULE
000000 IOMOD <DLBR> 175610,300,4,66,1024,161
000000 MODULE 140000,DLBR,175610,300,4,66,1024,161
; TITLE DLBR DEC/X11 SYSTEM EXERCISER MODULE
; DDRCOM VERSION 6 23-MAY-78
;*****LIST BIN*****
000000 BEGIN:
000000 046104 041102 040 MODNAM: .ASCII /DLBR / ;MODULE NAME
000005 XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
000006 175610 ADDR: 175610+0 ;1ST DEVICE ADDR
000010 000300 VECTOR: 300+0 ;1ST DEVICE VECTOR
000012 200 RRI: .BYTE PRTV4+0 ;1ST RR LEVEL
000013 000 BP2: .BYTE PRTV+0 ;2ND RR LEVEL
000014 000001 DVID1: +1 ;DEVICE INDICATOR 1
000016 000000 SR1: OPEN ;SWITCH REGISTER 1
000020 000000 SR2: OPEN ;SWITCH REGISTER 2
000022 000000 SR3: OPEN ;SWITCH REGISTER 3
000024 000000 SR4: OPEN ;SWITCH REGISTER 4
;*****
000026 140000 STAT: 140000 ;STATUS WORD
000030 000224 INIT: START ;MODULE START ADDR
000032 000224 SPOINT: MODSP ;MODULE STACK POINTER
000034 000000 PASCNT: 0 ;PASS COUNTER
000036 002000 ICOUNT: 1024 ;# OF ITERATIONS PER PASS=1024
000040 000000 SOFCNT: 0 ;LOC TO COUNT ITERATIONS
000042 000000 HRPCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000044 000000 SOFPAS: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000046 000000 HRDPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000050 000000 SYSCNT: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000052 000000 RANNUM: 0 ;# OF SYS ERRORS ACCUMULATED
000054 000000 CONFIC: 0 ;HOLDS RANNUM # WHEN RAND MACRO IS CALLED
000056 000000 RES1: 0 ;RESERVED FOR MONITOR USE
000060 000000 RES2: 0 ;RESERVED FOR MONITOR USE
000062 000000 SVR0: OPEN ;LOC TO SAVE R0
000064 000000 SVR1: OPEN ;LOC TO SAVE R1
000066 000000 SVR2: OPEN ;LOC TO SAVE R2
000070 000000 SVR3: OPEN ;LOC TO SAVE R3
000072 000000 SVR4: OPEN ;LOC TO SAVE R4
000074 000000 SVR5: OPEN ;LOC TO SAVE R5
000076 000000 SVR6: OPEN ;LOC TO SAVE R6
000100 000000 CSRA: OPEN ;ADDR OF CURRENT CSR
000102 000000 SRADR: OPEN ;ADDR OF GOOD DATA, OR
000104 000000 ASADR: OPEN ;ADDR OF BAD DATA, OR
000106 000000 ASSTAT: OPEN ;CONTENTS OF CSR
000108 000000 ERRTYP: OPEN ;STATUS REG CONTENTS
000110 000000 ASB: OPEN ;TYPE OF ERROR
000112 002334 AMAS: OPEN ;EXPECTED DATA
000114 000000 RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
000116 000000 WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
000120 000000 WDRP: OPEN ;WORDS FROM MEMORY PER ITERATION
000122 000161 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 161 ;MODULE IDENTIFICATION NUMBER=161

```



```
464 000434 104405 000000 000000 HDRPRS,BEGIN,NULL ;MAINT. BIT WON'T SET OR IT CLEARED READY
465 000434 104405 000000 000000 MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
466 000447 104403 000000 003640 ENDS,BEGIN
467 000450 104410 000000 003640 1S: BIC #4,ADLXCSR ;NOW CLR THE MAINT BIT
468 000454 042777 000004 003110 CMP #200,ADLXCSR ;DID IT CLEAR ??
469 000454 042777 000004 003110 BIC #100,ADLXCSR ;SET I.E. IN RCVR CSR
470 000470 001415 000000 003102 BIC #100,ADLXCSR ;DID IT SET
471 000472 004767 002706 177402 BNE #5 ;RR IF IT DID
472 000476 012767 000025 177402 JSR PC,STATR ;GO SET UP ERROR INFO
473 000476 012767 000025 177402 MOV #25,ERRTYP
474 000504 104405 000000 000000 HDRPRS,BEGIN,NULL ;MAINT. BIT WON'T SET OR IT CLEARED READY
475 000504 104405 000000 000000 MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
476 000512 104403 000000 003640 ENDS,BEGIN
477 000520 104410 000000 003640 ;TEST THAT BIT 06 IN XCSR CAN CAUSE AN INTERRUPT
478 000520 104410 000000 003640
479 000524 005067 003060 DLTO4: CLR INTPLG ;INIT THE SOFTWARE INTR. FLAG
480 000530 016700 177254 MOV VECTOR,R0 ;GET BASE VECTOR ADDRESS
481 000534 062700 000004 ADD #4,R0 ;GENERATE ADDR OF XMIT VECTOR
482 000540 012770 000034 MOV #25,(R0) ;GO TO 25 ON XMIT INTERRUPT
483 000544 012770 177242 MOV #R1,(R0) ;PRIORITY LEVEL = R1
484 000550 005001 000000 CLR R1 ;INIT BREAK TIMER
485 000552 052777 000100 003012 BIS #100,ADLXCSR ;SET INTR. ENAB
486 000560 104407 000000 000000 1S: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
487 000564 104407 000000 000000 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
488 000570 005767 003014 TST INTPLG ;DID XMIT INTR OCCUR YET ??
489 000574 001025 000000 BNE #5 ;RR IF IT DID
490 000576 005767 000000 BIC #5 ;COUNT BREAK TIMER
491 000600 001367 000000 BNE #5 ;RR IF NO TIMEOUT
492 000602 004767 002576 JSR PC,STATR ;GO SET UP ERROR INFO
493 000606 012767 000023 177272 MOV #23,ERRTYP
494 000614 104405 000000 000000 HDRPRS,BEGIN,NULL ;XMITTER FAILED TO GENERATE INTERRUPT
495 000614 104405 000000 000000 MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
496 000622 104403 000000 003640 ENDS,BEGIN
497 000630 016700 177254 DLTO5: CLR INTPLG ;INIT SOFTWARE INTR. FLAG
498 000634 042777 000100 002730 2S: BIC #100,ADLXCSR ;DISABLE XMITTR INTR ENABLE
499 000642 005167 002742 COM INTPLG ;SET THE INTR. FLAG
500 000646 000002 000000 RTI ;RETURN CONTROL TO OTHER GUY
501 000650 022777 000200 002714 3S: CMP #200,ADLXCSR ;DID I.E. GET CLEARED IN INTR. SERVICE
502 000654 001415 000000 BIC #100,ADLXCSR ;SET I.E. IN RCVR CSR
503 000658 004767 002520 JSR PC,STATR ;GO SET UP ERROR INFO
504 000662 012767 000027 177214 MOV #27,ERRTYP
505 000672 104405 000000 000000 HDRPRS,BEGIN,NULL ;"DONE" OR RCVR INTR. ENAB FAILED TO CLEAR
506 000672 104405 000000 000000 MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
507 000700 104403 000000 003640 ENDS,BEGIN
508 000706 104410 000000 003640 ;TEST THAT A RCVR INTR CAN OCCUR WHEN "DONE" GETS SET
509 000712 005067 002672 DLTO5: CLR INTPLG ;INIT SOFTWARE INTR. FLAG
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520 000716 016700 177066 MOV VECTOR,R0 ;GET THE BASE VECTOR ADDRESS
521 000722 016700 177066 MOV #45,(R0) ;GO TO 45 ON RCVR INTERRUPT
522 000726 116710 177060 MOV #R1,(R0) ;SET PRIORITY
523 000732 052777 000100 002626 BIS #100,ADLXCSR ;SET I.E. IN RCVR CSR
524 000740 032777 000100 002620 BIT #100,ADLXCSR ;DID IT SET
525 000746 001415 000000 BNE #5 ;RR IF IT DID
526 000750 001767 002412 JSR PC,STATR ;GO SET UP ERROR INFO
527 000754 012767 000033 177124 MOV #33,ERRTYP
528 000762 104405 000000 000000 HDRPRS,BEGIN,NULL ;CAN'T SET BIT 06 IN RCVR I.E.
529 000762 104405 000000 000000 MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
530 000770 104403 000000 003640 ENDS,BEGIN
531 000776 104410 000000 003640 1S: MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
532 000782 042777 000100 002556 BIC #100,ADLXCSR ;NOW CLEAR THE I.E. BIT
533 000786 042777 000100 002550 BIT #100,ADLXCSR ;DID I.E. BIT GET CLEARED ??
534 000790 001415 000000 BIC #100,ADLXCSR ;SET I.E. IN RCVR CSR
535 000794 004767 002342 JSR PC,STATR ;GO SET UP ERROR INFO
536 000798 012767 000023 177054 MOV #23,ERRTYP
537 000802 104405 000000 000000 HDRPRS,BEGIN,NULL ;CAN'T CLEAR RCVR INTR. ENAB. BIT
538 000802 104405 000000 000000 MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
539 000810 104403 000000 003640 ENDS,BEGIN
540 000816 042777 000100 002506 2S: BIS #100,ADLXCSR ;NOW TURN IT ON FOR REAL
541 000820 052777 000004 002504 BIS #4,ADLXCSR ;TURN ON MAINT. MODE
542 000824 005001 000252 002476 CLR R1 ;INIT BREAK TIMER
543 000828 112777 000252 002476 MOV #252,ADLXCSR ;LOAD THE XMITTR OUTPUT DATA BUFFER
544 000832 104407 000000 000000 3S: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
545 000836 104407 000000 000000 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
546 000840 005767 002476 TST INTPLG ;DID RCVR INTR. YET ??
547 000844 001025 000000 BNE #5 ;RR IF IT DID
548 000848 005767 000000 BIC #5 ;COUNT BREAK TIMER
549 000852 001367 000000 BNE #5 ;RR IF NO TIMEOUT
550 000856 004767 002436 JSR PC,STATR ;GO SET UP ERROR INFO
551 000860 012767 000023 176744 MOV #23,ERRTYP
552 000864 104405 000000 000000 HDRPRS,BEGIN,NULL ;RCVR FAILED TO INTR. ON TIME
553 000864 104405 000000 000000 MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
554 000872 104403 000000 003640 ENDS,BEGIN
555 000878 016700 177254 DLTO6: CLR INTPLG ;INIT SOFTWARE INTR. FLAG
556 000882 042777 000100 002370 4S: BIC #100,ADLXCSR ;TURN OFF THE RECEIVED DATA
557 000886 005077 002370 CLW ADLXCSR ;TURN OFF MAINTENANCE MODE
558 000890 005167 002402 COM INTPLG ;SET SOFTWARE INTR. FLAG
559 000894 005777 002352 RTI ;RETURN TO OTHER GUY
560 000898 001421 002352 TST ADLXCSR ;DID INTR SERVICE CLEAR THE RCVR CSR ??
561 000902 001421 002352 BNE #5 ;RR IF IT DID
562 000906 004767 002344 JSR PC,STATR ;GO SET UP ERROR INFO
563 000910 005077 002340 CLR ADLXCSR ;CLEAR BOTH CSRS
564 000914 005077 002340 CLR ADLXCSR
565 000918 012767 000025 176646 MOV #25,ERRTYP
566 000922 104405 000000 000000 HDRPRS,BEGIN,NULL ;RCVR INTR SERVICE FAILED TO CLEAR I.E. AND DONE
```

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SEQ 0014

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6302 001506* 012767 000025 176372      MOV     #25,ERRTYP
6303                                     ;*****
6304 HRDPRS,REGIN,NULL      ;DATA SET INT FAILED TO SET
6305                                     ;*****
6306 CMP     #010,DLRCSR    ;SEC XMIT AND REC SET - DATA SET INT CLEAR
6307 BPO     2S              ;RR IF YES
6308 JSR     PC,STATR        ;GO SET UP ERROR INFO
6309 MOV     #22,ERRTYP
6310                                     ;*****
6311 HRDPRS,REGIN,NULL      ;SEC XMIT OR SEC REC FAILED TO SET - DATA SET INT FAILED
6312                                     ;*****
6313 011552* 042777 000010 002006      DTC     #0,DLRCSR    ;TURN OFF SEC XMIT
6314 3S:    BPO     1S        ;DID SEC INT GOING OFF SET DATA SET INT ?
6315 BVI     3S              ;RR IF YES
6316 JSR     PC,STATR        ;GO SET UP ERROR INFO
6317 MOV     #22,ERRTYP
6318                                     ;*****
6319 001544* 104405 000000* 000000      HRDPRS,REGIN,NULL    ;CLEARING SEC XMIT FAILED TO SET DATA SET INT
6320                                     ;*****
6321 011560* 005777 001754      3S:    BPO     1S        ;ALL BITS NOW CLEAR
6322 BPO     1S              ;RR IF YES
6323 JSR     PC,STATR        ;GO SET UP ERROR INFO
6324 MOV     #20,ERRTYP
6325                                     ;*****
6326 001566* 004767 000020 176306      HRDPRS,REGIN,NULL    ;SEC XMIT,SEC REC, OR DATA SET INT FAILED TO CLEAR
6327 011572* 012767 000020 176306      ;*****
6328                                     ;*****
6329                                     ;*****
6330 ;TEST THAT "DTR" ASSERTS "CAR DET", "CLR TO SEND", AND "DATA SET INT"
6331 ;-----
6332 001634* 005077 001726      DLT10: CLR     DLRCSR    ;CLR THE RCVR CSR
6333 011640* 012777 000020 001720      BPS     DLRCSR    ;SET "DATA SET" READY
6334 011646* 005777 001714      BPT     DLRCSR    ;DID DATA SET INT SET ??
6335 011652* 100407 000000      BPO     1S        ;RR IF YES
6336 011654* 004767 001506      JSR     PC,STATR    ;GO SET UP ERROR INFO
6337 011660* 005067 176222      CLR     ERRTYP
6338                                     ;*****
6339 001664* 104405 000000* 000000      HRDPRS,REGIN,NULL    ;"DTR" FAILED TO SET "DATA SET INT"
6340                                     ;*****
6341 011672* 022777 030002 001666      CMP     #30002,DLRCSR;DTR,CLR TO SEND, CAR DET, SET AND DATA SET INT CLEAR ?
6342 1S:    BPO     2S        ;RR IF YES
6343 BPO     2S              ;GO SET UP ERROR INFO
6344 JSR     PC,STATR
6345 CLR     ERRTYP
6346                                     ;*****
6347 001712* 104405 000000* 000000      HRDPRS,REGIN,NULL    ;DTR,CAR DET, OR CLR TO SEND FAILED TO SET OR DATA SET I
6348                                     ;*****
6349 011720* 042777 000002 001640      DTC     #0,DLRCSR    ;TURN OFF DTR
6350 2S:    BPO     1S        ;DATA SET INT SHOULD HAVE SET
6351 BVI     3S              ;RR IF IT DID
6352 JSR     PC,STATR        ;GO SET UP ERROR INFO
6353 MOV     #20,ERRTYP
6354                                     ;*****
6355 001744* 104405 000000* 000000      HRDPRS,REGIN,NULL    ;DATA SET INT FAILED TO SET ON CLEARING DTR
6356                                     ;*****
6357 011752* 005777 001610      3S:    BPO     1S        ;ALL BITS NOW CLEAR ??
6358 011756* 001407 000000      BPO     1S        ;RR IF YES
6359                                     ;*****

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688 001760* 004767 001402
689 001764* 005067 176116
690
691 001770* 104405 000000* 000000
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693
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696
697 001776* 052777 000040 001562 DLT11: BIS #40,MDLRCSR ;SET DATA SET I.E.
698 002004* 032777 000040 001554 BIT #40,MDLRCSR ;DID IT SET ??
699 002012* 001010 ;RR IF YES
700 002014* 004767 001346 JSR PC,STATR ;GO SET UP ERROR INFO
701 002020* 012767 000027 176060 MOV #25,ERRTYP
702
703 002026* 104405 000000* 000000
704
705 002034* 042777 000040 001524 1S: BIC #40,MDLRCSR ;CLEAR DATA SET I.E.
706 002042* 005777 001520 TST MDLRCSR ;DID IT CLEAR ??
707 002046* 001411 ;RR IF YES
708 002050* 004767 001312 JSR PC,STATR ;GO SET UP ERROR INFO
709 002054* 012767 000027 176024 MOV #25,ERRTYP
710
711 002062* 104405 000000* 000000
712
713 002070* 000463 BR DLT13 ;SKIP NEXT TEST
714
715
716
717
718 002072* 005067 001512 DLT12: CLR INTFLG ;INIT SOFTWARE INTR. FLAG
719 002076* 016700 175706 MOV VECTOR,R0 ;GET RAS VECTOR ADDR.
720 002102* 012720 002174* MOV #25,(R0)+ ;GO TO 25 ON DATA SET INTERRUPT
721 002106* 095001 CLW R1 ;INIT BREAK TIMER
722 002110* 052777 000040 001450 BVS #40,MDLRCSR ;ENABLE DATA SET INTR.
723 002116* 052777 000010 001442 BVS #10,MDLRCSR ;SET SEC XMIT
724
725 002124* 104407 000000* ;TEMPORARY RETURN TO MONITOR....
726 002130* 104407 000000* ;THEN CONTINUE AT NEXT INSTRUCTION.
727 002134* 005767 001450 TST INTFLG ;DID INTR OCCUR YET ??
728 002140* 001023 JNE JS ;RR IF YES
729 002142* 095301 OFC R1 ;COMMIT WUP BREAK TIMER
730 002144* 001367 JNE JS ;RR IF NO TIMEOUT
731 002146* 004767 001214 JSR PC,STATR ;GO SET UP ERROR INFO
732 002152* 095077 001410 CLR MDLRCSR ;TURN OFF THE I.E. BIT
733 002156* 012767 000023 175722 MOV #25,ERRTYP
734
735 002164* 104405 000000* 000000
736
737 002172* 000422 BR DLT13 ;GO TO NEXT TEST
738 002174* 042777 000040 001364 2S: BIC #40,MDLRCSR ;TURN OFF DATA SET I.E.
739 002202* 005167 001402 COM INTFLG ;SET SOFTWARE INTR. FLAG
740 002206* 000002 RTI ;RETURN CONTROL TO OTHER GOV
741 002210* 032777 000040 001350 3S: BIT #40,MDLRCSR ;DID INTR SERVICE TURN OFF I.E. ?
742 002216* 001410 ;RR IF YES
743 002220* 004767 001142 JSR PC,STATR ;GO SET UP ERROR INFO
```

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744 002224* 012767 000023 175654 MOV #23,ERRTYP
745
746 002232* 104405 000000* 000000
747
748
749
750
751
752 002240* 052777 000001 001324 DLT13: BIS #1,MDLRCSR ;SET BREAK BIT
753 002246* 032777 000001 001316 BIT #1,MDLRCSR ;DID IT SET ??
754 002254* 001010 ;RR IF YES
755 002256* 004767 001122 JSR PC,STATR ;GO SET UP ERROR INFO
756 002262* 012767 000025 175616 MOV #25,ERRTYP
757
758 002270* 104405 000000* 000000
759
760 002276* 042777 000001 001266 1S: BIC #1,MDLRCSR ;CLEAR THE BREAK BIT
761 002304* 032777 000001 001260 BIT #1,MDLRCSR ;DID IT CLEAR ?
762 002312* 001410 ;RR IF YES
763 002314* 004767 001064 JSR PC,STATR ;GO SET UP ERROR INFO
764 002320* 012767 000025 175560 MOV #25,ERRTYP
765
766 002326* 104405 000000* 000000
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772 002334* 005067 001242
773 002334* 005077 001226
774 002344* 005077 001216
775 002350* 016700 175434
776 002354* 012720 002522*
777 002360* 116710 175426
778 002364* 005720
779 002364* 012720 002452*
780 002372* 116710 175414
781 002376* 012703 003612*
782 002402* 012704 003612*
783 002402* 005067 001142
784 002412* 005777 001152
785 002416* 005777 001146
786 002422* 012367 001204
787 002426* 012467 001202
788 002432* 004767 000436
789 002436* 022703 003622*
790 002442* 001361
791
792
793
794
795
796
797
798 002444* 104411 000000*
799
800 002450* 000752
801
802
803

```

;
; *****
; * SECTION TWO
; *****
;
RESTRT:  CLR      XEND      ;CLEAR FND FLAGS
          CLR      @DLXCSR  ;CLEAR THE DL11 CONTROL REGS
          CLR      @DLRCSR  ;JUST IN CASE
          MOV      VECTOP,R0 ;GET START VECTOR ADDRESS
          MOV      @RINT,(R0)+ ;SET UP THE RCVR AND XMIT VECTORS
          MOV      @R1,(R0)
          TST      (R0)+
          MOV      @RINT,(R0)+
          MOV      @R1,(R0)
          DOACIN:  MOV      @LDTR,R3 ;POINT TO TABLE OF LOAD SUBR. POINTERS
                  MOV      @MTAB,R4 ;POINT TO TABLE OF MESSAGE POINTERS
                  CLR      R7C      ;CLEAR RETRY FLAGS
                  TST      @DLRDDR  ;FLUSH RCVR INPUT BUFFER REG
          IS:      TST      @DLRDDR  ;
                  MOV      (R3)+,DOOUT ;SET UP CORRECT LOAD BUF ADDRESS POINTER
                  MOV      (R4)+,MESS  ;SET UP MESSAGE POINTER
                  JSR      PC,SEGX   ;GO UP A SEGMENT
                  CVP      @TAB,R3   ;DONE ALL FOUR SEGMENTS ??
                  BNE      IS        ;BR IF NOT

          ENDTIS,REGIN ;SIGNAL END OF ITERATION.
          BR      DOACIN ;MONITOR SHALL TEST END UP PASS
```

904
905
906
907 002452* 105777 001114
908 002456* 100403
909
910 002460* 000004 000000* 002516*
911
912 002466* 022767 004522* 001104
913 002474* 001405
914 002476* 117777 001076 001070
915 002504* 005267 001070
916 002510*
917
918 002510* 000004 000000* 002610*
919
920
921 002516* 105767 001061
922 002522* 001025
923 002524* 016767 001042 175346
924 002532* 017767 001034 175342
925 002540* 042777 000104 001024
926 002546* 105167 001030
927
928 002552* 012767 000011 175326
929
930 002560* 104405 000000* 000000
931
932
933 002566* 005077 000774
934 002572* 104410 000000*
935
936 002576* 042777 000100 000766
937 002604* 104400 000000*
938
939 002610* 105767 000767
940 002614* 001370
941 002616* 104400 000000*
942
943
944
945
946 002622* 105777 000740
947 002626* 100403
948
949 002630* 000004 000000* 002702*
950
951 002636* 005777 000726
952 002642* 100003
953
954 002644* 000004 000000* 002774*
955
956 002652* 022767 005122* 000716
957 002660* 001405
958 002662* 117777 000702 000706
959 002670* 005267 000702

```

;THIS ROUTINE SERVICES ALL XMITTR INTERRUPTS. FOR ALL 256. BYTE XFRS
;
XINT:  TSTR      @DLXCSR      ;XMIT READY SPT ??
        BML      IS          ;BR IF YES
        PIRQS,BEGIN,45      ;QUEUE UP TO CONTINUE AT 45 AND RTI
        1S:  CMP      @DLRUF1,DPTX ;OUTPUT 256. BYTES YET ??
              BFO      3S      ;BR IF YES
              MOV      @DPTX,@DLXDDR ;OUTPUT A CHARACTER
              INC      DPTX      ;POINT TO NEXT CHAR. IN BUFFER
              3S:
              PIRQS,BEGIN,65      ;QUEUE UP TO CONTINUE AT 65 AND RTI
              4S:  TSTR      XEND+1 ;ANY FATAL RCVR. ERRORS PENDING ??
                    BNE      5S      ;BR IF YES - STOP XMITTING
                    MOV      @DLXCSR,CSRA ;SAVE THE CSR ADDRESS
                    MOV      @DLXCSR,ACSR ;SAVE THE CONTENTS OF THE CSR
                    BIC      #104,@DLXCSR ;DISABLE XMITTR INTERRUPTS
                    COMR      XEND      ;SET XMIT END FLAG
                    MOV      #11,BPKTYP
                    *****
                    HDRFS,REGIN,NULL ;XMITTR FALSE INTERRUPT - FATAL ERROR
                    *****
                    CLR      @DLRCSR      ;TURN OFF RCVR INTR.
                    ENDS,REGIN
                    5S:  BIC      #100,@DLXCSR ;DISABLE XMITTR. INTERRUPTS
                          EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
                          6S:  TSTR      XEND+1 ;ANY FATAL RCVR. ERRORS PENDING ??
                                BNE      5S      ;BR IF YES
                                EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

;THIS ROUTINE SERVICES RECEIVER INTERRUPTS FOR ALL 256. BYTE XFRS
;
RINT:  TSTR      @DLRCSR      ;RCVR DONE SET ??
        BML      IS          ;BR IF YES
        PIRQS,BEGIN,35      ;QUEUE UP TO CONTINUE AT 35 AND RTI
        1S:  TST      @DLRDDR  ;OVERRUN/PARITY/FRAMING ERRORS ??
              BPL      2S      ;BR IF NONE
              PIRQS,BEGIN,55      ;QUEUE UP TO CONTINUE AT 55 AND RTI
              2S:  CMP      @RUPEND,IPTR ;INPUT BUFFER FULL ??
                    BFO      7S      ;BR IF YES
                    MOV      @DLRDDR,@IPTR ;READ THE DL INPUT BUFFER REG.
                    INC      IPTR      ;POINT TO NEXT CHAR. POSITION
```

```
002674* 75:
002674* 000004 000000 003062*
002702* 105767 000674 35:
002705* 001025
002710* 016767 000652 175162
002716* 017767 000644 175162
002724* 042777 000100 000634
002732* 105167 000645
002736* 012767 000011 175142
002744* 104405 000000 000000
002752* 005077 000614
002756* 104410 000000
002762* 042777 000100 000576 45:
002770* 104400 000000
002774* 105767 000602 55:
003000* 001777
003002* 016767 000560 175070
003010* 017767 000552 175064
003016* 017767 000546 175060
003024* 042777 000100 000534
003032* 012767 000017 175046
003040* 104405 000000 000000
003046* 005077 000520
003052* 105267 000510
003056* 104400 000000
003062* 105767 000514 65:
003066* 001335
003070* 174400 000000
```

;TRQS,BEGIN,65 ; QUEUE UP TO CONTINUE AT 65 AND RTT

;TSTR XEND ;ANY FATAL XMITTR ERROR PENDING
;BNE 4S ;RR IF YES
;MOV DLRCSP,CSRA ;SAVE THE RCVR. CSR ADDRESS
;MOV DLRCSP,ACSR ;SAVE CONTENTS OF CSR
;BIC #100,DLRCSP ;TURN OFF THE RCVR.
;COMB XEND+1 ;SET FATAL RCVR ERROR FLAG

;MOV #11,ERRTYP
;*****
;RDPRS,BEGIN,NULL ;RECEIVER FALSE INTERRUPT - FATAL ERROR
;*****

;CLR #DLXCSR ;DISABLE XMITTR TOO
;ENDS,BEGIN ;

;BIC #100,DLRCSP ;DISABLE RCVR INTERRUPTS
;EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

;TSTR XEND ;ANY FATAL XMITTR ERRORS PENDING ??
;BNE 4S ;RR IF YES
;MOV DLRCSP,CSRA ;SAVE CSR ADDRESS
;MOV DLRCSP,ACSR ;SAVE CONTENTS OF CSR
;MOV #DLRDRR,ASTAT ;SAVE THE ERROR FLAGS
;BIC #100,DLRCSP ;DISABLE RCVR INTR.

;MOV #17,ERRTYP
;*****
;RDPRS,BEGIN,NULL ;OVERDUN - PARITY - FRAMING ERROR
;*****

;CLR #DLXCSR ;DISABLE XMITTR TOO
;INCR RTCV ;SET RETRY FLAG
;EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

;TSTR XEND ;ANY FATAL XMITTR ERRORS PENDING ??
;BNE 4S ;RR IF YES
;EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

```
003074* 000240
003076* 004767 000320
003102* 004777 000544
003106* 004767 000106
003112* 005002
003114*
003116* 104407 000000
003120* 104407 000000
003124* 005767 000452
003130* 001407
003132* 104400 000000
003136* 105767 000444
003142* 001411
003144* 105067 000436
003150* 105267 000434
003154* 122767 000003 000425
003162* 001345
003164* 000406
003166* 022767 005122 000402 45:
003174* 001406
003176* 005302
003200* 001345
003202*
003204* 104403 000000 003634 55:
003210* 000207
003212* 004767 000034 65:
003216* 000207 75:
;THIS ROUTINE KICKS OFF ALL 256. BYTE TRANSFERS
;-----
;KICKOFF: MOV #DLBUFO,OPTR ;POINT TO BEGINNING OF OUTPUT BUFFER
;MOV #DLBUFI,IPTR ;POINT TO BEGINNING OF INPUT BUFFER
;BIC #104,DLXCSR ;TURN ON XMITTR
;BIC #100,DLRCSP ;TURN ON RCVR
;RTS PC ;RETURN TO CALLING SEGMENT
;-----
;THIS ROUTINE CHECKS FOR AND REPORTS DATA COMPARE ERRORS
;-----
;CHKDAT: BIC #100,DLXCSR ;DISABLE XMITTR INTR.
;BIC #100,DLRCSP ;DISABLE RCVR INTR.
;MOV #DLBUFO,R0 ;R0 POINTS TO OUTPUT BUFFER
;MOV #DLBUFI,R1 ;R1 POINTS TO INPUT BUFFER
;CMPR (R0),+(R1)+ ;COMPARE INPUT WITH OUTPUT
;BNE 2S ;RR IF NOT EQUAL
;CMP #BUPEND,R1 ;END OF THE BUFFERS ??
;BNE 1S ;RR IF NOT
;RTS PC ;RETURN TO CALLER
;-----
;MOV DLRCSP,CSRA ;SAVE THE CSR ADDRESS
;MOV #174400,ASB ;SAVE THE SHOULD BE DATA
;BIC #174400,ASB ;ZERO HI BYTE
;MOV #0,SBADR ;SAVE THE SHOULD BE ADDRESS
```

```

958 003336* 114167 174546      MOV      -(R1),AWAS      ;SAVE THE WAS DATA
959 003342* 042767 177400      RLC      #177400,AWAS    ;CLEAR OUT HI BYTE
960 003350* 010167 174530      MOV      R1,WASADR      ;SAVE THE WAS ADDRESS
961
962
963 003354* 104404 000000*      ;*****
964 JATPRS,REGIN                  ;DATA ERROR!!!
965 ;*****
966
967 003360* 105720      TSTR      (R0)+          ;POINT TO NEXT BYTE IN THE BUFFERS
968 003362* 105721      TSTR      (R1)+          ;GO CHECK NEXT BYTE
969 003364* 000746
970
971 ;THESE ROUTINES SET UP THE ERROR INFORMATION FOR THE BASIC TESTS
972 ;-----
973
974 003366* 016767 000174 174504 STAT:  MOV      DLRCR,CSRA      ;SAVE THE CSR ADDRESS
975 003374* 017167 000166 174500      MOV      #DLRCR,ACSR      ;SAVE THE CONTENTS OF THE CSR
976 003402* 000207      RTS                      ;RETURN TO BASIC TESTS
977
978 003404* 016767 000162 174466 STAT:  MOV      DLXCSR,CSRA      ;SAVE THE CSR ADDRESS
979 003412* 017167 000154 174462      MOV      #DLXCSR,ACSR      ;SAVE THE CONTENTS OF THE CSR
980 003420* 000207      RTS                      ;RETURN TO THE BASIC TESTS
981
982 ;THIS ROUTINE IS USED TO CLEAR THE INPUT/OUTPUT BUFFERS
983 ;-----
984
985 003422* 012700 004122*      CLOLBP: MOV      #DLRUPO,R0      ;POINT R0 TO BEGINNING OF BUFFERS
986 003426* 005020      1S:  CLR      (R0)+          ;CLEAR ONE WORD - UPDATE POINTER
987 003430* 022700 005122*      CMP      #RUPEMD,R0      ;DONE 256 BYTES ??
988 003434* 001374      BNE      IS          ;RR IF NOT
989 003436* 000207      RTS                      ;RETURN TO CALLING SEGMENT
990
991 ;THIS ROUTINE LOADS THE OUTPUT BUFFER WITH A NULL-DEL-NULL PATTERN
992 ;-----
993
994 003440* 012700 004122*      LDOHT1: MOV      #DLRUPO,R0      ;SET UP POINTER
995 003444* 105020      1S:  CLR      (R0)+          ;MOV A NULL CHAR
996 003446* 112720 000377      MOV      #177,(R0)+      ;MOV A DELETE CHAR.
997 003452* 022700 004522*      CMP      #DLRUFI,R0      ;BUFFER FULL ???
998 003456* 001374      BNE      IS          ;RR IF NOT
999 003460* 000207      RTS                      ;RETURN TO CALLING SEGMENT
1000
1001 ;THIS ROUTINE IS USED TO LOAD AN ASCENDING BINARY COUNT PATTERN
1002 ;-----
1003
1004 003462* 012700 004122*      LDOHT2: MOV      #DLRUPO,R0      ;SET UP POINTER
1005 003466* 005001      1S:  CLR      R1                      ;USE R1 TO GENERATE THE PATTERN
1006 003470* 110120 004522*      MOV      R1,(R0)+      ;LOAD ONE CHAR.
1007 003472* 022700      CMP      #DLRUFI,R0      ;BUFFER FULL ???
1008 003476* 001402      BFC      25          ;RR IF YES
1009 003500* 105201      INCR      R1          ;GENERATE NEXT CHAR.
1010 003502* 000772      BR      IS          ;GO MOVE IT
1011 003504* 000207      RTS                      ;RETURN TO CALLING SEGMENT
1012
1013 ;THIS ROUTINE IS USED TO LOAD THE DESCENDING BINARY COUNT PATTERN

```

```

1014
1015 003506* 012700 004122*      LDOHT3: MOV      #DLRUPO,R0      ;SET UP POINTER
1016 003512* 012701 000377      1S:  MOV      #177,R1          ;START R1 AT 177
1017 003516* 110120      MOV      R1,(R0)+      ;LOAD ONE CHAR.
1018 003520* 022700 004522*      CMP      #DLRUFI,R0      ;AT END OF THE BUFFER ??
1019 003524* 001402      BFC      25          ;RR IF YES
1020 003526* 105101      INCR      R1          ;GENERATE NEXT CHAR.
1021 003530* 000772      BR      IS          ;GO MOVE IT
1022 003532* 000207      RTS                      ;RETURN TO CALLING SEGMENT
1023
1024 ;THIS ROUTINE LOADS THE WORST CASE PATTERN
1025 ;-----
1026
1027 003534* 012700 004122*      LDOHT4: MOV      #DLRUPO,R0      ;SET UP POINTERS
1028 003540* 012701 005124*      1S:  MOV      #WCASE,R1      ;POINT TO MONITOR'S WORST CASE PATTERN
1029 003544* 012120      2S:  MOV      (R1)+(R0)+      ;LOAD ONE WORD
1030 003546* 022700      CMP      #DLRUFI,R0      ;BUFFER FULL ???
1031 003550* 001404      BFC      35          ;RR IF YES
1032 003552* 001767      MOV      #WCASE,R1      ;END OF WORST CASE PATTERN ??
1033 003554* 022701      INCR      R1          ;GO RESET R1
1034 003560* 000774      BR      2S          ;GO MOVE IT
1035 003562* 000774      BR      2S          ;GO MOVE IT
1036 003564* 000207      RTS                      ;RETURN TO CALLING SEGMENT

```

```
1037
1038
1039
1040 003566* 000000
1041 003570* 000000
1042 003572* 000000
1043 003574* 000000
1044
1045 003576* 000000
1046 003600* 000000
1047
1048 003602* 000000
1049 003604* 000000
1050 003606* 000000
1051 003610* 000000
1052
1053 003612* 003440*
1054 003614* 003462*
1055 003616* 003506*
1056 003620* 003534*
1057 003622* 003644*
1058 003624* 003705*
1059 003626* 003750*
1060 003630* 004015*
1061
1062 003632* 000000
1063 003634* 003644*
1064 003636* 177777
1065 003640* 004053*
1066 003642* 177777
1067 003644* 047045 046125 026514
1068 003652* 042504 026514 052516
1069 003650* 046114 051440 050505
1070 003666* 042525 041516 020105
1071 003674* 041101 051117 042524
1072 003702* 022504 000 000
1073 003705* 000 044502 040516
1074 003712* 054522 052440 020120
1075 003720* 047503 047125 020124
1076 003726* 042523 052523 047105
1077 003734* 042503 040440 047502
1078 003742* 052122 042105 000045
1079 003750* 041045 047111 051101
1080 003756* 020131 047504 047127
1081 003760* 041440 052517 052118
1082 003772* 051440 050505 042518
1083 004000* 041516 020105 041101
1084 004006* 051117 042524 022504
1085 004014* 000 000
1086 004015* 045 047527 051522
1087 004022* 020124 040503 042523
1088 004030* 051440 050505 042525
1089 004036* 051516 020105 041101
1090 004044* 051 042524 022504
1091 004052* 000 000
1092 004053* 045 040506 040524

;VARIABLES,PLACS,MESSAGES,AND BUFFERS
;-----
DLRCSR: OPEN ;CONTAINS ADDRESS OF RCVR CSR
DLRDBR: OPEN ;CONTAINS ADDRESS OF RCVR DBR
DLKCSR: OPEN ;CONTAINS ADDRESS OF XMITR CSR
DLKDBR: OPEN ;CONTAINS ADDRESS OF XMITR DBR
IPTR: OPEN ;CONTAINS POINTER TO INPUT BUFFER
OPTR: OPEN ;CONTAINS POINTER TO OUTPUT BUFFER
XFND: OPEN ;FATAL ERROR FND PLACS
EPCRR: OPEN ;END OF PASS COUNTER
RTRY: OPEN ;RETRY FLAG AND COUNTER
INTPLC: OPEN ;SOFTWARE INTR. FLAG USED BY BASIC TESTS
LDBTAB: LDBTAB1 ;POINTER TO 1ST LOAD BUFFER SUBR.
LDBTAB2 ;POINTER TO 2ND LOAD BUFFER ROUTINE
LDBTAB3 ;POINTER TO 3RD LOAD BUFFER ROUTINE
LDBTAB4 ;POINTER TO 4TH LOAD BUFFER ROUTINE
MTAB: MSG1 ;POINTER TO MESSAGE 1
MSG2 ;POINTER TO MESSAGE 2
MSG3 ;POINTER TO MESSAGE 3
MSG4 ;POINTER TO MESSAGE 4
LDBTAB: OPEN ;CONTAINS POINTER TO LOAD BUFFER SUBR.
AMESS: MSG1 ;MESSAGE POINTERS
DRPMS: MSG5 ;TERMINATOR
MSG1: .ASCIZ /%NULL-DEL-NULL SEQUENCE ABORTED%/
MSG2: .ASCIZ /%BINARY UP COUNT SEQUENCE ABORTED%/
MSG3: .ASCIZ /%BINARY DOWN COUNT SEQUENCE ABORTED%/
MSG4: .ASCIZ /%WORST CASE SEQUENCE ABORTED%/
MSG5: .ASCIZ /%FATAL ERROR IN STATIC REGISTER TESTS%/
```

```
1093 004060* 020114 051105 047522
1094 004066* 020122 047111 051440
1095 004074* 040524 044524 020103
1096 004102* 042522 044507 052118
1097 004110* 051105 052540 051505
1098 004116* 051524 000045
1099
1100 .RVFN
1101 ;512 WORDS RESERVED FOR TWO 256. BYTE BUFFERS
1102 ;-----
1103
1104 004122* 000400
1105 004555* 000400
1106 005122* 000000
1107
1108 005124* 177776 000001 177775
1109 005132* 000002 177773 000004
1110 005140* 177767 000010
1111 005144* 177757 000020 177737
1112 005152* 000049 177677 000100
1113 005160* 177577 000200
1114 005164* 177377 000400 176777
1115 005172* 001000 175777 002000
1116 005200* 173777 004000
1117 005204* 167777 010000 157777
1118 005212* 020000 137777 040000
1119 005220* 077777 100000
1120 005224*
1121
1122 000001
1123

WCASE: .WORD 177776,1,177775,2,177773,4,177767,10
.WORD 177757,20,177737,40,177677,100,177577,200
.WORD 177377,400,176777,1000,175777,2000,173777,4000
.WORD 167777,10000,157777,20000,137777,40000,77777,100000

WCASE:
.END
```

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CROSS REFERENCE TABLE -- USER SYMBOLS

ACSP	000102H	389#	582*	874*	868*	896*	974*	978*											
ADDR	000006H	355#	409																
ADDR22=	001000H	409#																	
AMSS	003634H	788*	927	1063#															
ASH	000106H	393#	583*	955*	956*														
ASTAT	000104H	391#	587*																
AWAS	000113H	394#	583*																
BEGIN	000000H	355#	437	579	958*	959*													
		490	491	440	450	450	452	453	464	466	467	474	476	477					
		542	548	501	502	511	512	513	514	529	531	532	539	541					
		614	641	549	561	562	575	576	578	587	589	591	594	597					
		746	758	766	768	810	818	830	834	837	841	849	854	862					
		874	881	892	897	901	910	911	914	927									
HIT0	= 000001	407#																	
HIT1	= 000002	407#																	
HIT10	= 002000	407#																	
HIT11	= 004000	407#																	
HIT12	= 010000	407#																	
HIT13	= 020000	407#																	
HIT14	= 040000	407#																	
HIT15	= 100000	407#																	
HIT2	= 000004	407#																	
HIT3	= 000010	407#																	
HIT4	= 000020	407#																	
HIT5	= 000040	407#																	
HIT6	= 000100	407#																	
HIT7	= 000200	407#																	
HIT8	= 000400	407#																	
HIT9	= 001000	407#																	
BRKAS	= 104407	407#	490	491	548	549	725	726	910	911									
BR1	= 000112H	486	522	778	781														
BR2	= 000113H	355#																	
RT0DS	= 104421	407#																	
BRFPND	= 005122H	856	922	950	986	1106*													
CDATAS	= 104412	407#																	
CMKAT	= 003252H	929	944#																
CLDLBP	= 003425H	905	984#																
CONPIL	= 000056H	377#																	
CSRA	= 000100H	387#	581*	823*	867*	885*	954*	973*	977*										
DATCKS	= 104411	407#																	
DATERS	= 104404	407#	587	963															
DLBHPF	= 004522H	812	936	947	996	1007	1019	1031	1105#										
DLBHPF	= 004122H	946	984	993	1004	1016	1028	1104#											
DLWIT	= 003564H	410*	423*	424*	432	523*	524	533*	534	543*	555*	564*	568	571*					
DLRCSO	= 003566H	581	582	601*	602	609	616*	617	628*	629	636	643*	644	651*					
		665*	663*	674	671	678*	679	686	697*	698	705*	706	722*	723*					
		732*	738*	741	743*	746	753*	754	768*	769	785	786	851	858	887	1041#			
		938*	945*	954	973	974	1040#												
		412*	427	563	584														
DLRDRP	= 003570H	412*																	
DLT01	= 000102H	432*																	
DLT02	= 000106H	446	445#																
DLT03	= 000404H	446	458#																
DLT04	= 000524H	470	482#																
DLT05	= 000712H	507	519#																
DLT06	= 001344H	595	601#																

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CROSS REFERENCE TABLE -- USER SYMBOLS

DLT07	001466H	613	628#																
DLT10	001634H	659	652#																
DLT11	001776H	689	697#																
DLT12	002076H	707	718#																
DLT13	002240H	713	719	742	752#														
DLRCSO	= 003572H	411*	425*	445	458*	459	468*	469	488*	503*	506	544*	556*	565*					
		572*	575*	576	760*	741	774*	807	823	824	825*	836*	877*	895*					
		931*	946*	977	978	1042#													
		416*	546*	585	814*	1043#													
DLKDRP	= 003574H	782	800																
DJACIN	= 002376H	433	452	466	476	501	513	531	541	561	577	1065#							
DRPMS	= 003640H	355																	
DVDM1	= 000016H	407#																	
ENDITS	= 104411	440																	
ENDS	= 104410	453	467	477	502	514	532	542	562	578				878					
EPCTH	= 003604H	1049#	435*	448*	462*	472*	497*	509*	527*	537*	557*	573*	605*	612*					
ERRTP	= 000106H	392#	620*	632*	639*	647*	654*	657*	682*	689*	701*	709*	733*	744*					
		756*	764*	828*	828*	890*													
		837	841	891	897	901	914												
EXITS	= 104400	407#																	
FCPDAS	= 104415	407#																	
GWBNP	= 104414	407#																	
HRDCNT	= 000044H	372#																	
HRDCNS	= 104405	407#	437	450	464	474	499	511	529	539	559	575	607	614					
		422	614	641	649	656	669	676	684	691	703	711	735	746					
		758	766	830	874	892													
HRDPAS	= 000050H	374#																	
ICONT	= 000036H	369#																	
ICOUNT	= 000040H	370#																	
IDNMT	= 000122H	399#																	
INIT	= 000030H	366#																	
INTPLG	= 003610H	482*	492	504*	519*	550	566*	718*	727	739*	1051#								
IVTP	= 000120H	398#																	
IPTR	= 003576H	856	858*	859*	922	936*	1045#												
ITCUP	= 003220H	907	935#																
LDOHT	= 003632H	787*	906																
LDOHT1	= 003440H	993#	1053																
LDOHT2	= 003460H	1004#	1054																
LDOHT3	= 003506H	1016#	1055																
LDOHT4	= 003534H	1028#	1056																
LDTAB	= 003612H	789	1053#																
MAP22S	= 104416	407#																	
MODPM	= 001332H	580	594#																
MODVAM	= 000000H	353#																	
MODSP	= 000724H	367	405#																
MSGCS	= 104403	407#	439	452	466	476	501	513	531	541	561	577	927						
MSGCS	= 104402	407#																	
MSGCS	= 104401	407#																	
MSG1	= 001644H	1057	1063	1067#															
MSG2	= 001705H	1063	1073#																
MSG3	= 001750H	1059	1079#																

UPEV = 000000	354	360	361	362	363	380	381	382	383	384	385	386	387
	389	391	393	394	396	397	398	407#	1040	1041	1042	1043	1045
	1046	1048	1049	1050	1051	1062							
	814	815*	815*	835*	1046#								
OPTD = 003600R	407#												
OTDAS = 104420R	407#												
PASCNT = 000034R	358#												
PTRPS = 000000R	407#	810	818	849	854	862							
PDPSP = 005725	407#												
PDPSP2 = 022626	407#												
PRTV = 000000R	358#												
PRTV0 = 000000R	407#												
PRTV1 = 000040R	407#												
PRTV2 = 000100R	407#												
PRTV3 = 000140R	407#												
PRTV4 = 000200R	407#	407#											
PRTV5 = 000240R	407#												
PRTV6 = 000300R	407#												
PRTV7 = 000340R	407#												
PS = 177776	407#												
PSW = 177776	407#												
PUSH = 005746	407#												
PUSH2 = 024646	407#												
RANDS = 104417	407#												
RANWJW = 000054W	376#												
RSTRT = 002334R	395#	596	762	772#									
RFS1 = 000056W	378#												
RFS2 = 000050R	472#												
RFT = 002622R	376#	846#											
RSTRT = 000112R	395#												
RTRV = 003606R	784*	896*	915*	917*	918*	919	1050#						
SRADR = 000102R	388#												
SECT = 00374W	389#	904#											
SOPCNT = 000042R	371#												
SOPFRS = 104406	407#												
SOPPAS = 000046R	373#												
SPOINT = 000042R	367#												
SPSTZ = 000040R	1#	400											
SR1 = 000016R	360#	594											
SR2 = 000020R	361#												
SR3 = 000024R	362#												
SR4 = 000024R	363#												
START = 000224R	366#	409#											
STAT = 000026R	365#												
STATR = 00366R	434	576	576	554	570	604	611	619	631	638	646	653	666
	447	681	688	700	708	731	743	973#					
	461	471	496	508	755	763	977#						
STATX = 003404R	380#												
SVR0 = 000062R	381#												
SVR1 = 000064R	382#												
SVR2 = 000066R	383#												
SVR3 = 000070R	384#												
SVR4 = 000072R	385#												
SVR5 = 000074R	386#												
SVR6 = 000076R	375#												
SVSCNT = 000052R	407#												
TSPHFI = 000027	356#	483	520	719	776								
VECTDR = 000010R													

ASADP = 000104R	390#	584*											
WCASE = 005124R	1029	1109#											
WCASEP = 005224R	1033	1121#											
WDRP = 000115R	397#												
WDTN = 000114R	396#												
WEND = 003602R	773*	821	876*	839	865	870*	883	899	912	1048#			
XFLAG = 000005R	354#												
XINT = 002452R	780	807#											
	1104#	1105#											

. AQS. 000000 000
005224 001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

XDLRBO, XDLRBO/SOL/CRF:SYN=DDXCON, XDLRBO
RUN-TIME: 2 3 SECONDS
RUN-TIME RATIO: 31/6=4.9
CORE USED: 7K (13 PAGES)

DIAGNOSTIC ENGINEERING

digital

DECO ☐ DEPO ☒ SUBMISSION ☐☐ NEW

FOR RELEASE ENG. USE

☐ CHANGE ☐ DELETE

PRODUCT IDENTIFICATION

LIBRARY	PRODUCT NUMBER	REV	PATCH	ECO TALLY	PRODUCT DATE DD MMM YY	STATUS	DISTRIBUTION	1ST COPY - RIGHT YEAR	LAST COPY - RIGHT YEAR
ZZ	CXDLB	B	1	31	22 Jan 79	OBSOLETE	XX G	R	1976 1979

TITLE CXDLBB1 DL11-E MODULE

AUTHOR D. RUTENHOF MAINTAINER DEC/5001 SPT GRP MAINTAINER D. BUTENHOF SUBMITTING ENGINEER D. BUTENHOF

PRODUCT COMPONENTS

CK	DESCRIPTION	PRODUCT NO.	REV	CK	DESCRIPTION	PRODUCT NO.	REV
	DOCUMENT				INDEX		
	LISTING				SOURCE MEDIA		
	OBJECT MEDIA				TEST MEDIA		
X	DEPO	AF-E998B-M1					

PRODUCTS OBSOLETE (other than previous version)

LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV
MD			MD			MD		

PRODUCT CHARACTERISTICS

PROCESSORS PRODUCT OPERATES WITH (Enter all applicable 2-digit codes representing the Processor the product operates with. See separate instructions.)

03 04 05 10 20 21 34 35 40 45 50 55 60 70

OPERATIONAL CODES (Enter all applicable 2-digit codes that describe the product. See separate instructions.)

02 03 04 06 50

ACT/APT/XXDP	EXT	ACT SEQ NUMBER	ACT/XXDP COMPATIBLE?	APT COMPATIBLE?	1ST PASS RUN TIME	SUBSEQUENT PASS RUN TIME
INFORMATION FIELD			<input checked="" type="checkbox"/> Y <input type="checkbox"/> N	<input checked="" type="checkbox"/> Y <input type="checkbox"/> N	10 SECONDS	10 SECONDS

DECO/DEPO INFORMATION

LEM REPORTS CLOSED:							
DECO AFFECTED DEC/X11			MULTIMEDIA AFFECTED?	<input checked="" type="checkbox"/> YES	<input type="checkbox"/> NO		

KIT NUMBERS	ZJ129-RZ	ZJ129-FR	ZJ130-RB				
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PROBLEM:
Module is intended for 2/40 FRONT-END interface only, but documentation does not say so.

SOLUTION:
State in module header that this module is not intended for use on standalone PDP11 SYSTEMS.

DEPO PATCH AREA

CHANGE LOC	FROM	TO	CHANGE LOC	FROM	TO

SUBMITTING ENGINEER	MANUFACTURING ENGINEER	SUPPORT ENGINEER	CHARGE DECO/DEPO TO DISCRETE PROJECT NUMBER
<i>[Signature]</i>	<i>[Signature]</i>		Q99-05460
DATE: 23-Jan-79	DATE: 7-Feb-79	DATE:	
MAINTAINER	FIELD SERVICE	WAIVERING MANAGER	COORDINATION NO.
<i>[Signature]</i>			MC# 2838
DATE: 23-Jan-79	DATE:	DATE:	

7.0 MODULE OPERATION

BASIC TEST SEQUENCE:

DT01: VERIFY THAT ALL ZEROES CAN BE WRITTEN AND READ FROM THE "DELAY COUNTER" REGISTER

DT02: VERIFY THAT ALL ZEROES CAN BE WRITTEN AND READ FROM ALL ACTIVE "RAM" LOCATIONS

DT03: VERIFY THAT ALL ONES CAN BE WRITTEN AND READ FROM THE "DELAY COUNTER" REGISTER

DT04: VERIFY THAT ALL ONES CAN BE WRITTEN AND READ FROM ALL ACTIVE "RAM" LOCATIONS

DT05: VERIFY THAT EACH "RAM" LOCATION IS UNIQUELY ADDRESSABLE

DT06: VERIFY THAT THE "RMF=0" BIT DOES NOT SET WHEN A FLOATING ONE IS READ OUT OF THE "RAM"

DT07: VERIFY THAT THE "TO10 DONE" BIT CAN CAUSE AN INTERRUPT TO THE PROPER VECTOR

DT10: VERIFY THAT THE "IO REQ INT" BIT CAN CAUSE AN INTERRUPT TO THE PROPER VECTOR

DT11: VERIFY THAT THE "TO11 DONE" BIT CAN CAUSE AN INTERRUPT TO THE PROPER VECTOR

DT12: VERIFY THAT THE "TO10 ER" CAN CAUSE AN INTERRUPT TO THE PROPER VECTOR

DT13: VERIFY THAT THE "TO11 ER" BIT CAN CAUSE AN INTERRUPT TO THE PROPER VECTOR

DT14: VERIFY THAT "MSTK CLR" CAN CLEAR THE "TO11 BC" REG.

DT15: VERIFY THAT THE "ABC" REGISTER INCREMENTS DURING A "TO11 TRANSFER"

DT16: VERIFY THAT THE "TO11 DONE" BIT SETS PROPERLY

DT17: VERIFY THAT THE "STST NULL" FLOP CAN BE SET PROPERLY

DT20: VERIFY THAT THE "ABC" REGISTER INCREMENTS DURING A "TO10" E-BUFF FILL

8.0 OPERATOR OPTIONS

RELATIVE LOCATION "DTA 14" (DVID1) MUST BE MODIFIED TO EXERCISE MORE THAN ONE DTE20 AS SHOWN BELOW:

DVID1	BIT00=1	DTE20 #0	(DEFAULT)
DVID1	BIT01=1	DTE20 #1	
DVID1	BIT02=1	DTE20 #2	
DVID1	BIT03=1	DTE20 #3	

TO DESELECT A DTE20 THE APPROPRIATE BIT IN "DVID1" MUST BE SET TO A ZERO. IF THE PROGRAM FINDS ALL FOUR BITS = "0" THE MODULE WILL BE DROPPED.

9.0 NON STANDARD PRINTOUTS

DTA USES THE DATA ERROR PRINTOUT IN SOME CASES TO REPORT OTHER THAN NORMAL DATA ERRORS. REFER TO THE ACTUAL ERROR CALL "APC" TO LOCATE THE CALL IN THE LISTING AND ANALYZE THE INSTRUCTIONS PRECEDING THE "DATAERS" CALL TO OBTAIN THE INTERPRETION OF THE INFORMATION PRINTED.

.REM _

IDENTIFICATION

PRODUCT CODE: AC-E995D-MC
PRODUCT NAME: CXDTADO DTE20 MODULE
PRODUCT DATE: JANUARY 1981
MAINTAINER: DEC/X11 SUPPORT GROUP

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1.0 ABSTRACT

"DTA" IS AN IOMOD THAT WILL EXERCISE UP TO FOUR DTE20'S SEQUENTIALLY. IT USES THE DIAGNOSTIC MODE TO VERIFY THE DTE20 UNIBUS INTERFACE AND THE LOGIC CONTROLLING THIS INTERFACE. IT PERFORMS SIMPLE RAM READ/WRITE AND ADDRESSING TESTS ALONG WITH VERIFICATION OF THE VECTORED INTERRUPT AND "NPR" FUNCTIONS. IT MAKES NO ATTEMPT TO COMMUNICATE WITH THE KL10 SIDE OF THE DEVICE.

2.0 REQUIREMENTS

HARDWARE: A PDP11 COMPUTER SYSTEM WITH AT LEAST ONE DTE20 KL10 INTERFACE.

STORAGE: DTA REQUIRES:
1. DECIMAL WORDS: 852
2. OCTAL WORDS: 1524
3. OCTAL BYTES: 3250

3.0 PASS DEFINITION

THE FIRST PASS OF "DXDTA" CONSISTS OF EXECUTING EACH SUB-TEST ONE TIME. SUBSEQUENT PASSES CONSIST OF 100(8) ITERATIONS OF THE TEST SEQUENCE FOR EACH DTE20 FOUND.

4.0 EXECUTION TIME

PASS TIME WILL VARY DEPENDING UPON THE NO. OF DTE20'S SELECTED AND THE CONFIGURATION BEING EXERCISED.

5.0 CONFIGURATION PARAMETERS

DEFAULT PARAMETERS:

DVA: 174400 VCT: 774 BR1: 4 BR2: 0 DVC: 1

REQUIRED PARAMETERS:

TO EXERCISE MORE THAN ONE DTE20 "DVC" MUST BE SET UP AS DESCRIBED IN PARA. 8.0 BELOW.

6.0 DEVICE OPTION SET-UP

NONE REQUIRED

7.0 MODULE OPERATION

BASIC TEST SEQUENCE:

DT01: VERIFY THAT ALL ZEROES CAN BE WRITTEN AND READ FROM
THE "DELAY COUNTER" REGISTER

DT02: VERIFY THAT ALL ZEROES CAN BE WRITTEN AND READ FROM
ALL ACTIVE "RAM" LOCATIONS

DT03: VERIFY THAT ALL ONES CAN BE WRITTEN AND READ FROM THE
"DELAY COUNTER" REGISTER

DT04: VERIFY THAT ALL ONES CAN BE WRITTEN AND READ FROM
ALL ACTIVE "RAM" LOCATIONS

DT05: VERIFY THAT EACH "RAM" LOCATION IS UNIQUELY ADDRESSABLE

DT06: VERIFY THAT THE "RMF=0" BIT DOES NOT SET WHEN A FLOATING
ONE IS READ OUT OF THE "RAM"

DT07: VERIFY THAT THE "TO10 DONE" BIT CAN CAUSE AN INTERRUPT
TO THE PROPER VECTOR

DT10: VERIFY THAT THE "10 REQ INT" BIT CAN CAUSE AN INTERRUPT
TO THE PROPER VECTOR

DT11: VERIFY THAT THE "TO11 DONE" BIT CAN CAUSE AN INTERRUPT
TO THE PROPER VECTOR

DT12: VERIFY THAT THE "TO10 ER" CAN CAUSE AN INTERRUPT
TO THE PROPER VECTOR

DT13: VERIFY THAT THE "TO11 ER" BIT CAN CAUSE AN INTERRUPT
TO THE PROPER VECTOR

DT14: VERIFY THAT "MSTR CLR" CAN CLEAR THE "TO11 BC" REG.

DT15: VERIFY THAT THE "ABC" REGISTER INCREMENTS DURING
A "TO11 TRANSFER"

DT16: VERIFY THAT THE "TO11 DONE" BIT SETS PROPERLY

DT17: VERIFY THAT THE "STST NULL" FLOP CAN BE SET PROPERLY

DT20: VERIFY THAT THE "ABC" REGISTER INCREMENTS DURING
A "TO10" E-BUFF FILL

8.0 OPERATOR OPTIONS

RELATIVE LOCATION "DTA 14" (DVID1) MUST BE MODIFIED TO EXERCISE
MORE THAN ONE DTE20 AS SHOWN BELOW:

DVID1	BIT00=1	DTE20 #0	(DEFAULT)
DVID1	BIT01=1	DTE20 #1	
DVID1	BIT02=1	DTE20 #2	
DVID1	BIT03=1	DTE20 #3	

TO DESELECT A DTE20 THE APPROPRIATE BIT IN "DVID1" MUST
BE SET TO A ZERO. IF THE PROGRAM FINDS ALL FOUR BITS = "0"
THE MODULE WILL BE DROPPED.

9.0 NON STANDARD PRINTOUTS

DTA USES THE DATA ERROR PRINTOUT IN SOME CASES
TO REPORT OTHER THAN NORMAL DATA ERRORS. REFER TO THE ACTUAL
ERROR CALL "APC" TO LOCATE THE CALL IN THE LISTING AND ANALYZE
THE INSTRUCTIONS PRECEDING THE "DATAER" CALL TO OBTAIN THE
INTERPRETION OF THE INFORMATION PRINTED.

```

000000' IOMOD <DTAD>,174400,774,4,0,0,4,157
000000' MODULE 140000,DTAD,174400,774,4,0,0,4,157
; .TITLE DTAD DEC/X11 SYSTEM EXERCISER MODULE
; DDXCOM VERSION 6 23-MAY-78
; .LIST BIN
;*****
BEGIN:
000000' 052104 042101 040 MODNAM: .ASCII /DTAD / ;MODULE NAME.
000005' 000 XFLAG: .BYTE UPEN ;USED TO KEEP TRACK OF WBUFF USAGE
000006' 174400 ADDR: 174400+0 ;1ST DEVICE ADDR.
000010' 000774 VECTOR: 774+0 ;1ST DEVICE VECTOR.
000012' 200 BR1: .BYTE PRTY4+0 ;1ST BR LEVEL.
000013' 000 BR2: .BYTE PRTY0+0 ;2ND BR LEVEL.
000014' 000001 DVID1: 0+1 ;DEVICE INDICATOR 1.
000016' 000000 SR1: UPEN ;SWITCH REGISTER 1
000020' 000000 SR2: UPEN ;SWITCH REGISTER 2
000022' 000000 SR3: UPEN ;SWITCH REGISTER 3
000024' 000000 SR4: UPEN ;SWITCH REGISTER 4
;*****
000026' 140000 STAT: 140000 ;STATUS WORD.
000030' 000224' INIT: START ;MODULE START ADDR.
000032' 000224' SPOINT: MODDSP ;MODULE STACK POINTER.
000034' 000000 PASCNT: 0 ;PASS COUNTER.
000036' 000004 ICONT: 4 ;# OF ITERATIONS PER PASS=4
000040' 000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
000042' 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000044' 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000046' 000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000050' 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000052' 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
000054' 000000 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
000056' 000000 CONFIG: ;RESERVED FOR MONITOR USE
000060' 000000 RES1: 0 ;RESERVED FOR MONITOR USE
000062' 000000 RES2: 0 ;RESERVED FOR MONITOR USE
000064' 000000 SVR0: UPEN ;LOC TO SAVE R0.
000066' 000000 SVR1: UPEN ;LOC TO SAVE R1.
000068' 000000 SVR2: UPEN ;LOC TO SAVE R2.
000070' 000000 SVR3: UPEN ;LOC TO SAVE R3.
000072' 000000 SVR4: UPEN ;LOC TO SAVE R4.
000074' 000000 SVR5: UPEN ;LOC TO SAVE R5.
000076' 000000 SVR6: UPEN ;LOC TO SAVE R6.
000100' 000000 CSRA: UPEN ;ADDR OF CURRENT CSR.
000102' 000000 SBADR: ;ADDR OF GOOD DATA, OR
000104' 000000 ACSR: OPEN ;CONTENTS OF CSR.
000106' 000000 WASADR: ;ADDR OF BAD DATA, OR
000108' 000000 ASTAT: OPEN ;STATUS REG CONTENTS.
000110' 000000 ERRTYP: ;TYPE OF ERROR
000112' 000000 ASB: OPEN ;EXPECTED DATA.
000114' 000000 AWAS: OPEN ;ACTUAL DATA.
000116' 000246' RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
000118' 000000 WDTO: UPEN ;WORDS TO MEMORY PER ITERATION
000120' 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
000122' 000157 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
000040 IDNUM: 157 ;MODULE IDENTIFICATION NUMBER=157
;REPT SPS12 ;MODULE STACK STARTS HERE.

```

```

; .NLIST
; .WORD 0
; .LIST
; .ENDR
000224' MODDSP:
;*****
239
240
241 000040 DTESIZ= 000040 ;16 REGISTERS PER DTE20
242 000004 DTEMAX= 000004 ;UP TO FOUR DTE'S CAN BE TESTED
243 020000 T01BM= BIT13 ;TO=11 BYTE MODE
244 002000 DS05= BIT10 ;DIAGNOSTIC STATUS
245 000060 PULSE= BIT4:BIT5 ;SINGLE PULSE THE 10/11 CLOCK
246 000040 D1011= BIT5 ;10/11 INTERFACE DIAGNOSTIC MODE
247 000200 T010= BIT7 ;INTERFACE MAJOR STATE - T010 TRANSFER
248 040000 EDONES= BIT14 ;SET E-BUS DONE
249 000100 DRESET= BIT6 ;PERFORM DIAGNOSTIC CLEAR
250 000040 INTRON= BIT5 ;ENABLE DTE20 TO INTR. THE 11
251 000002 ERR11S= BIT1 ;SET T011 ERROR
252 100000 T010DN= BIT15 ;T010 DONE
253 010000 HAMIS0= BIT12 ;OUTPUT READ FROM RAM IS ALL ZEROES
254 000400 T010DB= BIT8 ;REQ 10 INTERRUPT - DUGRBELL FROM 11
255 000040 NULSTP= BIT5 ;NULL STOP
256 020000 ERR10S= BIT13 ;SET TO 10 ERROR
257 100000 DON10S= BIT15 ;SET TO 10 DONE
258 000200 DON11S= BIT7 ;SET TO 11 DONE
259 000100 T011= BIT6 ;INTERFACE MAJOR STATE - T011 XFR
260 004000 T011DB= BIT11
261 000200 T011DN= BIT7
262 040000 ZSTOP= BIT14 ;STOP ON NULL (ZERO) CHAR
263
264 000224' 016767 177564 003000 START: MOV DVID1,TDVD1 ;GET DEVICE SELECT PARAMETER
265 000232' 042767 177760 002772 BIC #177760,TDVD1 ;CLEAR OUT UNUSED BITS
266 000240' 001002 BNE RSTRT ;BR IF ANY DTE20'S SELECTED
267 000242' 104410 000000' ENDS,BEGIN ;
268
269 000246' 016767 002760 002760 RSTRT: MOV TDVD1,TDVD2 ;SAVE THE SELECT BITS
270 000254' 016705 177526 MOV ADDR,R5 ;GET THE FIRST DTE20 ADDRESS
271 000260' 016700 177524 MOV VECTOR,R0 ;GET THE FIRST VECTOR ADDRESS
272 000264' 000257 1$ CCC ;CLEAR OUT THE "C" BIT
273 000266' 006067 002742 ROR TDVD2 ;"C" WILL SET IF A DTE IS SELECTED
274 000272' 103002 BCC 2$ ;BR IF SELECT BIT = 0
275 000274' 004767 000016 JSR PC,GOEXDT ;GO EXERCISE SELECTED DTE
276 000300' 062705 000040 2$ ADD #DTESIZ,R5 ;GENERATE NEXT DTE START ADDR.
277 000304' 162700 000004 SUB #DTEMAX,R0 ;GENERATE NEXT DTE VECTOR ADDR.
278 000310' 104413 000000' ENDTLS,BEGIN ;SIGNAL END OF ITERATION.
279 ;MONITOR SHALL TEST END OF PASS
280 000314' 000763 BR 1$
281
282 000316' 012702 003172' GOEXDT: MOV #DLYCNT,R2 ;POINT TO FIRST TABLE ENTRY
283 000322' 005003 CLR R3 ;INIT R3 TO COUNT B1 +2
284 000324' 010512 1$ MOV R5,(R2) ;STORE A DTE ADDRESS
285 000326' 060322 ADD R3,(R2)+ ;MAKE IT THE RIGHT ADDRESS
286 000330' 005723 TST (R3)+ ;ADD +2 TO R3
287 000332' 022703 000040 CMP #DTESIZ,R3 ;STORED ALL DTE ADDRESSES ??
288 000336' 001372 BNE 1$ ;BR IF NOT

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289 000340' 016767 002676 002670 MOV ITCNT,ICOUN ;INITIALIZE ITERATION COUNTER
290 000346' 005367 002664 DOAGIN: DEC ICOUN ;COUNT ONE ITERATION
291
292 ;ZER0ES TEST TO DELAY COUNTER
293 ;-----
294
295 000352' 005067 177530 DT01: CLR ASB ;RESULT S/B = 000000
296 000356' 004767 002356 JSR PC,DIAGRT ;GO DO DIAGNOSTIC RESET
297 000362' 004767 002366 JSR PC,DIAGME ;GO SET UP DIAGNOSTIC MODE
298 000366' 012777 000000 002576 MOV #0,DLYCNT ;ZERO THE DELAY COUNT REG.
299 000374' 017767 002572 177506 MOV #DLYCNT,AWAS ;GET THE DELAY COUNT REG.
300 000402' 001412 BEQ DT02 ;BR IF DATA CORRECT
301 000404' 010567 177470 MOV R5,CSRA ;SAVE THE ERROR INFO
302 000410' 016767 002556 177466 MOV DLYCNT,WASADR
303 000416' 012767 000106' 177456 MOV #ASB,SBADR
304 ;*****
305 000424' 104404 000000' DATERS,BEGIN ;DATA ERROR!!!
306 ;*****
307
308 ;TEST TO WRITE ZER0ES IN ALL ACTIVE RAM LOCATIONS
309 ;-----
310
311 000430' 005067 177452 DT02: CLR ASB ;RESULT S/B = 000000
312 000434' 016703 002532 MOV DLYCNT,R3 ;POINT R3 TO FIRST RAM LUC.
313 000440' 004767 002274 18: JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
314 000444' 004767 002304 JSR PC,DIAGME ;GO SET IT IN DIAGNOSTIC MODE
315 000450' 005023 CLR (R3)+ ;ZERO A RAM LOCATION
316 000452' 020367 002544 CMP R3,DIAG1 ;DONE ALL ACTIVE LOCATIONS ??
317 000456' 001370 BNE 18 ;BR IF NOT
318 000460' 004767 002254 28: JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
319 000464' 004767 002264 JSR PC,DIAGME ;GO SET DIAGNOSTIC MODE
320 000470' 014367 177414 MOV ~(R3),AWAS ;GET CONTENTS OF RAM
321 000474' 001411 BEQ 38 ;BR IF IT WAS 000000
322 000476' 010367 177402 MOV R3,WASADR ;SAVE THE ERROR INFO
323 000502' 012767 000106' 177372 MOV #ASB,SBADR
324 000510' 010567 177364 MOV R5,CSRA
325 ;*****
326 000514' 104404 000000' DATERS,BEGIN ;DATA ERROR!!!
327 ;*****
328 000520' 032777 010000 002500 38: BIT #RAMIS0,0STATUS ;DID RMF=0 BIT SET
329 000526' 001012 BNE 48 ;BR IF IT DID
330 000530' 010567 177344 MOV R5,CSRA ;SAVE THE ERROR INFO
331 000534' 017767 002466 177342 MOV 0STATUS,ASTAT
332 000542' 005067 177340 CLR ERRRTYP
333 ;*****
334 000546' 104405 000000' 000000 HRDRS,BEGIN,NULL ;RMF=0 FAILED TO SET
335 ;*****
336 000554' 020367 002412 48: CMP R3,DLYCNT ;CHECKED ALL LOCATIONS ??
337 000560' 003337 BGT 28 ;BR IF NOT
338
339 ;TEST TO WRITE ALL 1'S INTO DELAY COUNTER
340 ;-----
341
342 000562' 012767 177777 177316 DT03: MOV #-1,ASB ;RESULT S/B = 177777
343 000570' 004767 002144 JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
344 000574' 004767 002154 JSR PC,DIAGME ;GO SET UP DIAGNOSTIC MODE
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345 000600' 012777 177777 002364 MOV #-1,DLYCNT ;ALL 1'S TO DELAY COUNT REG.
346 000606' 017767 002360 177274 MOV #DLYCNT,AWAS ;GET THE CONTENTS OF DELAY COUNT
347 000614' 026767 177266 177266 CMP ASB,AWAS ;WAS IT ALL ONES ??
348 000622' 001412 BEQ DT04 ;BR IF YES
349 000624' 016767 002342 177252 MOV DLYCNT,WASADR ;SAVE THE ERROR INFO
350 000632' 012767 000106' 177242 MOV #ASB,SBADR
351 000640' 010567 177234 MOV R5,CSRA
352 ;*****
353 000644' 104404 000000' DATERS,BEGIN ;DATA ERROR!!!
354 ;*****
355
356 ;TEST TO WRITE ALL 1'S INTO ACTIVE RAM LOCATIONS
357 ;-----
358
359 000650' 016703 002316 DT04: MOV DLYCNT,R3 ;R3 POINTS TO 1ST ADDRESS
360 000654' 004767 002060 18: JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
361 000660' 004767 002070 JSR PC,DIAGME ;GO SET DIAGNOSTIC MODE
362 000664' 012723 177777 MOV #-1,(R3)+ ;LOAD ALL 1'S INTO RAM
363 000670' 020367 002326 CMP R3,DIAG1 ;LOADED ALL LOCATIONS ??
364 000674' 001367 BNE 18 ;BR IF NOT
365 000676' 012767 177777 28: MOV #-1,ASB ;RESULT S/B = 177777
366 000704' 004767 002030 JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
367 000710' 004767 002040 JSR PC,DIAGME ;GO SET DIAGNOSTIC MODE
368 000714' 014367 177170 MOV ~(R3),AWAS ;READ A RAM LOCATION
369 000720' 026767 177162 177162 CMP ASB,AWAS ;WAS IT 177777 ??
370 000726' 001411 BEQ 38 ;BR IF YES
371 000730' 010367 177150 MOV R3,WASADR ;SAVE THE ERROR INFO
372 000734' 012767 000106' 177140 MOV #ASB,SBADR
373 000742' 010567 177132 MOV R5,CSRA
374 ;*****
375 000746' 104404 000000' DATERS,BEGIN ;DATA ERROR!!!
376 ;*****
377 000752' 020367 002214 38: CMP R3,DLYCNT ;CHECKED THEM ALL YET ??
378 000756' 003347 BGT 28 ;BR IF NOT
379
380 ;RAM MEMORY ADDRESS TEST
381 ;-----
382
383 000760' 016703 002206 DT05: MOV DLYCNT,R3 ;START WITH 1ST RAM LOC
384 000764' 005004 R4 ;R4 CONTAINS FOUR ZER0BYTES (4 BITS EACH)
385 000766' 004767 001746 JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
386 000772' 004767 001756 JSR PC,DIAGME ;GO SET DIAGNOSTIC MODE
387 000776' 010423 18: MOV R4,(R3)+ ;LOAD A RAM LOCATION-UPDATE POINTER
388 001000' 062704 021042 ADD #21042,R4 ;ADD +2 TO EACH DATA BYTE
389 001004' 020367 002212 CMP R3,DIAG1 ;LOADED THEM ALL ??
390 001010' 001372 BNE 18 ;BR IF NOT
391 001012' 162704 021042 28: SUB #21042,R4 ;INIT R4 TO START CHECKING DATA
392 001016' 010467 177064 MOV R4,ASB ;SAVE S/B DATA
393 001022' 016367 177776 177060 MOV ~(R3),AWAS ;GET CONTENTS OF RAM
394 001030' 020443 CMP R4,~(R3) ;CORRECT CONTENTS ??
395 001032' 001411 BEQ 38 ;BR IF YES
396 001034' 010367 177044 MOV R3,WASADR ;SAVE THE ERROR DATA
397 001040' 012767 000106' 177034 MOV #ASB,SBADR
398 001046' 010567 177026 MOV R5,CSRA
399 ;*****
400 001052' 104404 000000' DATERS,BEGIN ;DATA ERROR!!!
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401 ;*****
402 001056' 005704 3S: TST R4 ;BACK TO 000000 DATA
403 001060' 001354 BNE 2S ;BR IF NOT
404
405 ;TEST THAT RMF=0 DOES NOT SET WITH FLOATING ONE OUT OF RAM
406 ;-----
407
408 001062' 004767 001652 DT06: JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
409 001066' 005067 177014 CLR ASB ;RESULT S/B = 000000
410 001072' 012703 000001 MOV #BIT0,R3 ;INIT R3 TO LOAD DLYCNT
411 001076' 010377 002070 1S: MOV R3,DLYCNT ;LOAD FLOATING ONE INTO DLYCNT
412 001102' 017767 002064 177000 MOV DLYCNT,AWAS ;READ IT BACK OUT
413 001110' 032777 010000 002110 BIT #RAMISO,#STATUS ;DID RMF=0 GET SET ??
414 001116' 001415 BEQ 2S ;BR IF NOT -- ITS OK
415 001120' 016767 176764 176754 MOV AWAS,ACSK ;[CSRC] = DATA READ
416 001126' 010567 176746 MOV R5,CSRA ;SAVE ERROR INFO
417 001132' 017767 002070 176744 MOV #STATUS,ASTAT
418 001140' 005067 176742 CLR ERRTP
419 ;*****
420 001144' 104405 000000' 000000 HRDERS,BEGIN,NULL ;RMF=0 BIT FAILED TO GET CLEARED
421 ;*****
422 001152' 000241 2S: CLC ;PUT 0'S IN ON RIGHT SIDE
423 001154' 006103 RUL R3 ;ROTATE THE FLOATING ONE
424 001156' 001347 BNE 1S ;BR TILL IT COMES OUT ON LEFT
425
426 ;TEST THAT "TO10 DONE" CAUSES A VECTORED INTERRUPT
427 ;-----
428
429 001160' 004767 001554 DT07: JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
430 001164' 005067 002050 CLR INTFLG ;CLEAR SOFTWARE FLAG
431 001170' 012720 003164' MOV #DTINT,(R0)+ ;GO TO DTINT ON INTERRUPT
432 001174' 116710 176612 MOVB BRT,(R0)
433 001200' 005740 TST ~(R0) ;RESET VECTOR POINTER
434 001202' 012777 100040 002016 MOV #DON10S!INTRON,#STATUS ;ENABLE "TO10" TO CAUSE INTR.
435 001210' 000240 NOP ;DELAY A LITTLE
436 001212' 000240 NOP ;TO ALLOW INTERRUPT
437 001214' 000240 NOP
438 001216' 005767 002016 TST INTFLG ;DID THE INTERRUPT OCCUR ??
439 001222' 001015 BNE DT10 ;BR IF IT DID
440 001224' 010567 176650 MOV R5,CSRA ;SAVE THE ERROR INFO
441 001230' 012767 100040 176646 MOV #DON10S!INTRON,ASTAT
442 001236' 004767 001476 JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
443 001242' 012767 000023 176636 MOV #23,ERRTP
444 ;*****
445 001250' 104405 000000' 000000 HRDERS,BEGIN,NULL ;"TO10 DONE" FAILED TO CAUSE INTERRUPT
446 ;*****
447
448 ;TEST THAT "10 REQ INT" CAN GENERATE VECTORED INTERRUPT
449 ;-----
450
451 001256' 004767 001456 DT10: JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
452 001262' 005067 001752 CLR INTFLG ;CLEAR SOFTWARE FLAG
453 001266' 012777 004040 001732 MOV #TO10DB!INTRON,#STATUS ;ENABLE "10 REQ INT" TO CAUSE INTR
454 001274' 000240 NOP ;A LITTLE STALL
455 001276' 000240 NOP
456 001300' 000240 NOP
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457 001302' 005767 001732 TST INTFLG ;DID THE INTR. OCCUR ??
458 001306' 001015 BNE DT11 ;BR IF YES
459 001310' 010567 176564 MOV R5,CSRA ;SAVE THE ERROR INFO
460 001314' 012767 004040 176562 MOV #TO10DB!INTRON,ASTAT
461 001322' 004767 001412 JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
462 001326' 012767 000023 176552 MOV #23,ERRTP
463 ;*****
464 001334' 104405 000000' 000000 HRDERS,BEGIN,NULL ;"10 REQ INT" FAILED TO GENERATE INTERRUPT
465 ;*****
466
467 ;TEST THAT "TO11 DONE" CAN GENERATE A VECTORED INTERRUPT
468 ;-----
469
470 001342' 004767 001372 DT11: JSR PC,DIAGRT ;GO DO DIAGNOSTIC RESET
471 001346' 005067 001666 CLR INTFLG ;CLEAR SOFTWARE INTR. FLAG
472 001352' 012777 000240 001646 MOV #DON11S!INTRON,#STATUS ;ENABLE "TO11 DONE" TO CAUSE INTR
473 001360' 000240 NOP ;WAIT A LITTLE
474 001362' 000240 NOP
475 001364' 000240 NOP
476 001366' 005767 001646 TST INTFLG ;DID INTR. OCCUR ??
477 001372' 001015 BNE DT12 ;BR IF IT DID
478 001374' 010567 176500 MOV R5,CSRA ;SAVE THE ERROR INFO
479 001400' 012767 000240 176476 MOV #DON11S!INTRON,ASTAT
480 001406' 004767 001326 JSR PC,DIAGRT ;GO DO DIAGNOSTIC RESET
481 001412' 012767 000023 176466 MOV #23,ERRTP
482 ;*****
483 001420' 104405 000000' 000000 HRDERS,BEGIN,NULL ;"TO11 DONE" FAILED TO CAUSE INTERRUPT
484 ;*****
485
486 ;TEST THAT "TO10 ER" CAN CAUSE A VECTORED INTERRUPT
487 ;-----
488
489 001426' 004767 001306 DT12: JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
490 001432' 005067 001602 CLR INTFLG ;INIT SOFTWARE FLAG
491 001436' 012777 020040 001562 MOV #ERR10S!INTRON,#STATUS ;ENABLE "TO10ER" TO CAUSE INTR.
492 001444' 000240 NOP ;A LITTLE STALL
493 001446' 000240 NOP
494 001450' 000240 NOP
495 001452' 005767 001562 TST INTFLG ;DID INTR. OCCUR ??
496 001456' 001015 BNE DT13 ;BR IF IT DID
497 001460' 010567 176414 MOV R5,CSRA ;SAVE THE ERROR INFO
498 001464' 012767 020040 176412 MOV #ERR10S!INTRON,ASTAT
499 001472' 004767 001242 JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
500 001476' 012767 000023 176402 MOV #23,ERRTP
501 ;*****
502 001504' 104405 000000' 000000 HRDERS,BEGIN,NULL ;"TO10ER" FAILED TO CAUSE AN INTR.
503 ;*****
504
505 ;TEST THAT "TO11ER" CAN CAUSE A VECTORED INTERRUPT
506 ;-----
507
508 001512' 005067 001522 DT13: CLR INTFLG
509 001516' 012777 000042 001502 MOV #ERR11S!INTRON,#STATUS ;ENAB "TO11ER" TO CAUSE INTR.
510 001524' 000240 NOP ;A LITTLE STALL
511 001526' 000240 NOP
512 001530' 000240 NOP
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513 001532' 005767 001502 TST INTFLG ;DID THE INTR OCCUR ??
514 001536' 001015 BNE 18 ;BR IF IT DID
515 001540' 010567 176334 MOV R5,CSRA ;SAVE THE ERROR INFO
516 001544' 012767 000042 176332 MOV #ERR15,INTRON,ASTAT
517 001552' 004767 001162 JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
518 001556' 012767 000023 176322 MOV #23,ERRTYP
519 *****
520 001564' 104405 000000' 000000 HRDRS,BEGIN,NULL ;"TUI1ER" FAILED TO CAUSE AN INTERRUPT
521 *****
522 001572' 010010 18: MOV R0,(R0) ;RESTORE THAP CATCHER IN DIE
523 001574' 062720 000002 ADD #2,(R0)+ ;BEFORE LEAVING
524 001600' 005010 CLR (R0)
525 001602' 005740 TST -(R0)
526
527 ;TEST TO VERIFY "MSTR CLR" CAN CLEAR "TO11 BC"
528 ;-----
529
530 001604' 004767 001130 DT14: JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
531 001610' 016767 001432 176270 MOV NPRLOC,ASB ;GET NPR ADDRESS
532 001616' 012777 007777 001364 MOV #7777,@TO11BC ;LOAD TO11 BYTE COUNT
533 001624' 004767 001110 JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
534 001630' 016777 001412 001356 MOV NPRLOC,@TO11AD ;LOAD TO 11 ADDRESS
535 001636' 000240 NOP ;STALL A LITTLE
536 001640' 000240 NOP
537 001642' 000240 NOP
538 001644' 017767 001344 176236 MOV @TO11AD,AWAS ;GET THE TO 11 ADDRESS
539 001652' 026767 176230 176230 CMP ASB,AWAS ;DID IT GET MODIFIED ??
540 001660' 001412 BEQ DT15 ;BR IF NOT
541 001662' 016767 001326 176214 MOV TO11AD,WASADR ;SAVE THE ERROR INFO
542 001670' 012767 000106' 176204 MOV #ASB,SBADR
543 001676' 010567 176176 MOV R5,CSRA
544 *****
545 001702' 104404 000000' DATERS,BEGIN ;DATA ERROR!!!
546 *****
547
548 ;TEST TO VERIFY ABC REG INCREMENTS DURING TO11 TRANSFER
549 ;-----
550
551 001706' 016767 001334 176172 DT15: MOV NPRLOC,ASB ;GET THE NPR ADDRESS
552 001714' 005267 176166 INC ASB ;INCREMENT IT
553 001720' 004767 001014 JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
554 001724' 016703 001316 MOV NPRLOC,R3 ;GET NPR ADDRESS AGAIN
555 001730' 062703 000002 ADD #2,R3 ;ADD +2 TO IT
556 001734' 004767 001034 JSR PC,T11LOC ;GO LOCK IN TO11 MAJOR STATE
557 001740' 005067 176142 CLR ERRTYP
558 *****
559 001744' 104405 000000' 000000 HRDRS,BEGIN,NULL ;FAILED TO LOCK IN "TO11" MAJOR STATE
560 *****
561 001752' 000450 BR DT16 ;GO TO NEXT TEST
562 001754' 012777 027777 001226 MOV #TO11BM!7777,@TO11BC ;LOAD TO 11 BYTE COUNT REG.
563 001762' 016777 001260 001224 MOV NPRLOC,@TO11AD ;LOAD TO 11 ADDR. REG.
564 001770' 012777 040010 001226 MOV #10:EDONES,@DIAG2 ;GO SET UP DIAG REG.
565 001776' 004767 000722 JSR PC,DIAG9P ;GO PULSE THE CLOCK
566 002002' 012777 000014 001214 MOV #14,@DIAG2 ;CHANGE DIAG REG.
567 002010' 004767 000710 JSR PC,DIAG9P ;GO PULSE THE CLOCK
568 002014' 012777 000020 001202 MOV #20,@DIAG2 ;CHANGE THE DIAG REG.
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569 002022' 004767 000676 JSR PC,DIAG9P ;GO PULSE THE CLOCK
570 002026' 017767 001162 176054 MOV @TO11AD,AWAS ;GET THE TO 11 ADDRESS
571 002034' 026767 176046 176046 CMP ASB,AWAS ;DID IT INCREMENT PROPERLY ??
572 002042' 001414 BEQ DT16 ;BR IF IT DID
573 002044' 016767 001144 176032 MOV TO11AD,WASADR ;SAVE THE ERROR INFO
574 002052' 012767 000106' 176022 MOV #ASB,SBADR
575 002060' 010567 176014 MOV R5,CSRA
576 002064' 004767 000650 JSR PC,DIAGRT ;GO DO DIAGNOSTIC RESET
577 *****
578 002070' 104404 000000' DATERS,BEGIN ;DATA ERROR!!!
579 *****
580
581 ;TEST TO CHECK THE "TO11 DONE" FLAG GETS SET
582 ;-----
583
584
585 002074' 012767 000200 176004 DT16: MOV #TO11DN,ASB ;TO11DN BIT SHOULD GET SET
586 002102' 004767 000632 JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
587 002106' 005077 001076 CLR #TO11BC ;CLEAR TO 11 BYTE COUNT REG.
588 002112' 004767 000622 JSR PC,DIAGRT ;GO DO A DIAGRT
589 002116' 016777 001124 001070 MOV NPRLOC,@TO11AD ;LOAD THE TO 11 ADDR REG.
590 002124' 004767 000644 JSR PC,T11LOC ;GO LOCK IN TO 11 MAJOR STATE
591 002130' 005067 175752 CLR ERRTYP
592 *****
593 002134' 104405 000000' 000000 HRDRS,BEGIN,NULL ;FAILED TO LOCK IN TO 11 STATE
594 *****
595 002142' 000504 BR DT17 ;GO TO NEXT TEST
596 002144' 005077 001040 CLR #TO11BC ;LOAD TO 11 BYTE COUNT REG.
597 002150' 005077 001050 CLR #DIAG2 ;START AT MINOR STATE "TO11 DLY RD"
598 002154' 005077 001042 CLR #DIAG1 ;TURN CLOCK BACK ON
599 002160' 005004 CLR R4 ;INIT BREAK TIMER
600 002162' 18:
601 002162' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
602 002166' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
603 002172' 032777 000100 001022 BIT #TO11,@DIAG1 ;DID WE LEAVE THE TO11 STATE ??
604 002200' 001415 BEQ Z8 ;BR IF YES
605 002202' 005304 DEC R4 ;COUNT THE TIMER
606 002204' 001366 BNE 18 ;BR IF NO TIME OUT
607 002206' 010567 175666 MOV R5,CSRA ;SAVE THE ERROR INFO
608 002212' 017767 001004 175664 MOV #DIAG1,ASTAT
609 002220' 005067 175662 CLR ERRTYP
610 *****
611 002224' 104405 000000' 000000 HRDRS,BEGIN,NULL ;FAILED TO LEAVE THE TO11 STATE
612 *****
613 002232' 000450 BR DT17 ;GO TO NEXT TEST
614 002234' 005004 26: R4 ;INIT THE TIMER AGAIN
615 002236' 38:
616 002236' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
617 002242' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
618 002246' 032777 000100 000746 BIT #TO11,@DIAG1 ;BACK IN TO11 STATE YET
619 002254' 001015 BNE 48 ;BR IF YES
620 002256' 005304 DEC R4 ;COUNT THE BREAK TIMER
621 002260' 001366 BNE 38 ;BR IF NO TIMEOUT
622 002262' 017767 000734 175614 MOV #DIAG1,ASTAT ;SAVE THE ERROR INFO
623 002270' 010567 175604 MOV R5,CSRA
624 002274' 005067 175606 CLR ERRTYP
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625      002300' 104405 000000' 000000      ;*****  
626      ;HDRERS,BEGIN,NULL      ;FAILED TO GET BACK IN TO11 STATE  
627      ;*****  
628      002306' 000422      BR      DT17      ;GO TO NEXT TEST  
629      002310' 000240      4$:      NOP  
630      002312' 017767      MOV      @STATUS,AWAS      ;GET THE STATUS REG.  
631      002320' 032767      BIT      @T011DN,AWAS      ;T011 DONE BIT SET ?  
632      002326' 001012      BNE      DT17      ;BR IF YES  
633      002330' 010567      MOV      R5,CSRA      ;SAVE THE ERROR INFO  
634      002334' 012767      MOV      @WAS,WASADR  
635      002342' 012767      MOV      @ASB,SBAADR  
636      ;*****  
637      002350' 104404 000000'      DATERS,BEGIN      ;DATA ERROR!!!  
638      ;*****  
639      ;CHECK THAT STST NULL FLOP CAN BE SET  
640      ;-----  
641      002354' 012767 000040 175524      DT17:  MOV      @NULSTP,ASB      ;NULSTP SHOULD GET SET  
642      002362' 004767 000402      JSR      PC,T11L0K      ;LOCK IN TO 11 STATE  
643      002366' 005067 175514      CLR      ERRTYP  
644      ;*****  
645      002372' 104405 000000' 000000      ;HDRERS,BEGIN,NULL      ;FAILED TO LOCK IN TO 11 STATE  
646      ;*****  
647      002400' 000446      BR      DT20      ;GO TO NEXT TEST  
648      002402' 012777 077777 000600      MOV      @ZSTOP137777,@T011BC      ;SET THE NULL STOP BIT  
649      002410' 005077 000604      CLR      @T011UT      ;CLEAR TO 11 DATA REG.  
650      002414' 016777 000626 000572      MOV      @NPRLOC,@T011AD      ;LOAD THE T011 ADDR REG.  
651      002422' 012777 040010 000574      MOV      @10:EDONES,@DIAG2      ;LOAD DIAG2 REG  
652      002430' 004767 000270      JSR      PC,DIAG9P      ;GO PULSE THE CLOCK  
653      002434' 012777 000014 000562      MOV      @14,@DIAG2      ;SET UP DIAG2  
654      002442' 004767 000256      JSR      PC,DIAG9P      ;GO PULSE THE CLOCK  
655      002446' 017767 000554 175434      MOV      @STATUS,AWAS      ;GET THE STATUS REG.  
656      002454' 042767 177737 175426      BIC      @C<NULSTP>,AWAS      ;CLEAR OUT JUNK BITS  
657      002462' 026767 175420 175420      CMP      ASB,AWAS      ;DID NULSTP SET ??  
658      002470' 001412      BEQ      DT20      ;BR IF YES  
659      002472' 010567 175402      MOV      R5,CSRA      ;SAVE THE ERROR INFO  
660      002476' 012767 000110' 175400      MOV      @WAS,WASADR  
661      002504' 012767 000106' 175370      MOV      @ASB,SBAADR  
662      ;*****  
663      002512' 104404 000000'      DATERS,BEGIN      ;DATA ERROR!!!  
664      ;*****  
665      ;CHECK ABC INCREMENTATION DURING TO 10 E-BUFF FILL  
666      ;-----  
667      002516' 004767 000216      DT20:  JSR      PC,DIAGRT      ;GO DO A DIAGNOSTIC RESET  
668      002522' 016767 000520 175356      MOV      @NPRLOC,ASB      ;SAVE NPR ADDRESS  
669      002530' 062767 000001 175350      ADD      #1,ASB      ;INCREMENT IT  
670      002536' 016777 000504 000446      MOV      @NPRLOC,@T010AD      ;LOAD THE TO 10 ADDR REG.  
671      002544' 004767 000322      JSR      PC,T10L0C      ;GO LOCK IN TO 10 STATE  
672      002550' 005067 175332      CLR      ERRTYP  
673      ;*****  
674      002554' 104405 000000' 000000      ;HDRERS,BEGIN,NULL      ;FAILED TO LOCK IN TO 10 STATE  
675      ;*****  
676      002562' 000441      BR      DTEXTIT      ;EXIT TESTS
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681      002564' 012777 000010 000432      MOV      @10,@DIAG2      ;SET UP TO SHIFT STATES  
682      002572' 004767 000126      JSR      PC,DIAG9P      ;GO PULSE THE CLOCK  
683      002576' 012777 040000 000420      MOV      @EDONES,@DIAG2      ;SET EDONES BIT  
684      002604' 000240      NOP  
685      002606' 012777 000014 000410      MOV      @14,@DIAG2      ;SHIFT STATES  
686      002614' 004767 000104      JSR      PC,DIAG9P      ;PULSE THE CLOCK TWICE  
687      002620' 004767 000100      JSR      PC,DIAG9P  
688      002624' 017767 000362 175256      MOV      @T010AD,AWAS      ;GET THE TO 10 ADDRESS  
689      002632' 026767 175250 175250      CMP      ASB,AWAS      ;DID IT GET INCREMENTED ??  
690      002640' 001412      BEQ      DTEXTIT      ;BR IF YES  
691      002642' 010567 175232      MOV      R5,CSRA      ;SAVE THE ERROR INFO  
692      002646' 016767 000340 175230      MOV      @T010AD,WASADR  
693      002654' 012767 000106' 175220      MOV      @ASB,SBAADR  
694      ;*****  
695      002662' 104404 000000'      DATERS,BEGIN      ;DATA ERROR!!!  
696      ;*****  
697      002666' 005767 175142      DTEXTIT: TST      PASCNT      ;FIRST TIME THROUGH ?  
698      002672' 001405      BEQ      1$      ;BR IF YES-QUICK PASS  
699      002674' 005767 000336      TST      ICOUN      ;DO IT AGAIN ??  
700      002700' 001402      BEQ      1$      ;BR IF NO  
701      002702' 000167 175440      JMP      @UAGIN      ;GO DO IT  
702      002706' 000207      1$:      RTS      PC      ;GO TO NEXT DTE20  
703      ;COMMON SUBROUTINES  
704      ;-----  
705      002710' 012777 000060 000304      DIAGPU: MOV      @PULSE!D1011,@DIAG1      ;PULSE THE CLOCK  
706      002716' 000240      NOP  
707      002720' 000240      NOP  
708      002722' 000207      RTS      PC  
709      002724' 012777 002060 000270      DIAG9P: MOV      @DS05!PULSE!D1011,@DIAG1  
710      002732' 000240      NOP  
711      002734' 000240      NOP  
712      002736' 000207      RTS      PC  
713      002740' 012777 000100 000256      DIAGRT: MOV      @DRESET,@DIAG2      ;DIAGNOSTIC RESET  
714      002746' 000240      NOP  
715      002750' 000240      NOP  
716      002752' 000207      RTS      PC  
717      002754' 012777 000040 000240      DIAGME: MOV      @D1011,@DIAG1      ;SET DIAGNOSTIC MODE  
718      002762' 000240      NOP  
719      002764' 000240      NOP  
720      002766' 000207      RTS      PC  
721      002770' 004767 177744      T11L0K: JSR      PC,DIAGRT  
722      002774' 004767 177754      T11L0C: JSR      PC,DIAGME  
723      003000' 005004      T11LKA: CLR      R4      ;INIT BREAK TIMER  
724      003002' 032777 000100 000212      1$:  BIT      @T011,@DIAG1      ;IN T011 STATE YET ??  
725      003010' 001017      BNE      T11KB      ;BR IF YES  
726      003012' 004767 177672      JSR      PC,DIAGPU      ;GO PULSE THE CLOCK  
727      003016' 104407 000000'      BREAKS,BEGIN      ;TEMPORARY RETURN TO MONITOR...
```

```
737 003022' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
738 003026' 005304 DEC R4 ;COUNT THE TIMER
739 003030' 001364 BNE IS ;BR IF NO TIMEOUT
740 003032' 016767 000164 175040 MOV DIAG1,CSMA ;SAVE THE ERROR INFO
741 003040' 017767 000156 175034 MOV @DIAG1,ACSR
742 003046' 000207 RTS PC ;ERROR RETURN
743 003050' 012777 002040 000144 T11KB: MOV #D1011:DS05,@DIAG1 ;SET STATE HOLD
744 003056' 062716 000012 ADD #12,(SP) ;MOVE PC AROUND ERROR CALL
745 003062' 000240 NUP
746 003064' 000207 RTS PC ;NOW RETURN
747
748 003066' 004767 177646 T10L0K: JSR PC,DIAGRT ;GO DO A DIAGNOSTIC RESET
749 003072' 004767 177656 T10L0C: JSR PC,DIAGME ;GO SET DIAG MODE
750 003076' 005004 CLM R4 ;INIT TIME OUT COUNTER
751 003100'
752 003100' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
753 003104' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
754 003110' 032777 000200 000104 BIT #T010,@DIAG1 ;SHIFT TO TO 10 STATE YET ?
755 003116' 001013 BNE T10LKB ;BR IF YES
756 003120' 004767 177564 JSR PC,DIAGPU ;PULSE THE CLOCK
757 003124' 005304 DEC R4 ;COUNT THE TIMER
758 003126' 001364 BNE IS ;BR IF NO TIMEOUT
759 003130' 016767 000066 174742 MOV DIAG1,CSRA ;SAVE THE ERROR INFO
760 003136' 017767 000060 174736 MOV @DIAG1,ACSR
761 003144' 000207 RTS PC ;ERROR RETURN
762 003146' 012777 002040 000046 T10LKB: MOV #D1011:DS05,@DIAG1 ;LOCK IN TO 10 STATE
763 003154' 062716 000012 ADD #12,(SP) ;MOVE PC AROUND ERROR CALL
764 003160' 000240 NUP
765 003162' 000207 RTS PC ;NORMAL RETURN
766
767
768 003164' 005167 000050 DTINT: COM INTFLG ;SET SOFTWARE FLAG
769 003170' 000002 RTI
770
771 ;CONSTANTS,ADDRESS TABLE, AND VARIABLES
772 ;-----
773
774 ;THIS TABLE GETS LOADED WITH THE ADDRESSES OF THE DTE REGISTERS
775
776 003172' 000000 DLYCNT: 0
777 003174' 000000 DEXWD3: 0
778 003176' 000000 DEXWD2: 0
779 003200' 000000 DEXWD1: 0
780 003202' 000000 TENAD1: 0
781 003204' 000000 TENAD2: 0
782 003206' 000000 T010BC: 0
783 003210' 000000 T011BC: 0
784 003212' 000000 T010AD: 0
785 003214' 000000 T011AD: 0
786 003216' 000000 T010DT: 0
787 003220' 000000 T011DT: 0
788 003222' 000000 DIAG1: 0
789 003224' 000000 DIAG2: 0
790 003226' 000000 STATUS: 0
791 003230' 000000 DIAG3: 0
792
```

```
793 ;VARIABLES AND FLAGS
794
795 003232' 000000 TDVD1: 0 ;DEVICE SELECT BITS BUFFERS
796 003234' 000000 TDVD2: 0
797 003236' 000000 ICOUN: 0 ;PASS ITERATION COUNTER
798 003240' 000000 INTFLG: 0 ;SOFTWARE INTERRUPT FLAG
799 003242' 000100 ITCNT: 100 ;NO. OF TEST ITERATIONS FOR EA. DTE20
800 003244' 000000 NPRBUF: 0 ;NPR XFER BUFFER
801
802 ;CONSTANTS
803
804 003246' 003244' NPRLOC: NPRBUF ;ADDRESS POINTER TO NPR BUFFER
805
806 000001 .END
```

ACSR	000102H	221#	415*	741*	760*										
ADDR	000006H	187#	270												
ADDR22=	001000	239#													
ASB	000106H	225#	295*	303	311*	323	342*	347	350	365*	369	372	392*	397	
		409*	531*	539	542	551*	552*	571	574	585*	635	643*	659	663	
		672*	673*	689	693										
ASTAT	000104R	223#	331*	417*	441*	460*	479*	458*	516*	608*	622*				
AWAS	000110R	226#	299*	320*	346*	347	368*	369	393*	412*	415	538*	539	570*	
		571	630*	631	634	657*	658*	659	662	688*	689				
BEGIN	000000R	184#	267	278	305	326	334	353	375	400	420	445	464	483	
		502	520	545	559	578	593	601	602	611	616	617	626	637	
		647	665	678	695	736	737	752	753						
BIT0 =	000001	239#	410												
BIT1 =	000002	239#	251												
BIT10 =	002000	239#	244												
BIT11 =	004000	239#	260												
BIT12 =	010000	239#	253												
BIT13 =	020000	239#	243	256											
BIT14 =	040000	239#	248	262											
BIT15 =	100000	239#	252	257											
BIT2 =	000004	239#													
BIT3 =	000010	239#													
BIT4 =	000020	239#	245												
BIT5 =	000040	239#	245	246	250	255									
BIT6 =	000100	239#	249	259											
BIT7 =	000200	239#	247	258	261										
BIT8 =	000400	239#	254												
BIT9 =	001000	239#													
BREAKS=	104407	239#	601	602	616	617	736	737	752	753					
BR1	000012R	189#	432												
BR2	000013R	190#													
RTODS =	104421	239#													
CDATAS=	104412	239#													
CONFIG	000056R	209#													
CSRA	000100R	219#	301*	324*	330*	351*	373*	398*	416*	440*	459*	478*	497*	515*	
		543*	575*	607*	623*	633*	661*	691*	740*	759*					
DATCKS=	104411	239#													
DATERS=	104404	239#	305	326	353	375	400	545	578	637	665	695			
DEXWD1	003200R	779#													
DEXWD2	003176R	778#													
DEXWD3	003174R	777#													
DIAGME	002754R	297	314	319	344	361	367	386	725#	731	749				
DIAGPU	002710H	710#	735	756											
DIAGRT	002740R	296	313	318	343	360	366	385	408	429	442	451	461	470	
		480	489	499	517	530	533	553	576	586	588	671	720#	730	
		748													
DIAG1	003222R	316	363	389	598*	603	608	618	622	710*	715*	725*	733	740	
		741	743*	754	759	760	762*	788#							
DIAG2	003224R	564*	566*	597*	653*	655*	681*	683*	685*	720*	789#				
DIAG3	003230R	791#													
DIAG9P	002724R	565	567	569	654	656	682	686	687	715#					
DLYCNT	003172R	282	298*	299	302	312	336	345*	346	349	359	377	383	411*	
		412	776#												
DOAGIN	000346R	290#	703												
DON10S=	100000	257#	434	441											
DON11S=	000200	258#	472	479											

DRESET=	000100	249#	720												
DS05 =	002000	244#	715	743	762										
DTMAX=	000004	242#	277												
DTESIZ=	000040	241#	276	287											
DTEXIT	002666H	680	690	699#											
DTINT	003164R	431	768#												
DT01	000352R	295#													
DT02	000430R	300	311#												
DT03	000562R	342#													
DT04	000650R	348	359#												
DT05	000760R	383#													
DT06	001062R	408#													
DT07	001160R	429#													
DT10	001256R	439	451#												
DT11	001342R	458	470#												
DT12	001426R	477	489#												
DT13	001512R	496	508#												
DT14	001604R	530#													
DT15	001706R	540	551#												
DT16	002074R	561	572	585#											
DT17	002354H	595	613	628	632	643#									
DT20	002516R	649	660	671#											
DVID1	000014R	191#	264												
D1011 =	000040	246#	710	715	725	743	762								
EDUNES=	040000	248#	564	653	683										
ENDITS=	104413	239#	278												
ENDS =	104410	239#	267												
ERRTYP	000106R	224#	332*	418*	443*	462*	481*	500*	518*	557*	591*	609*	624*	645*	
		676*													
ERR10S=	020000	256#	491	498											
ERR11S=	000002	251#	509	516											
EXITS =	104400	239#													
GETPAS=	104415	239#													
GOEXDT	000316R	275	282#												
GWBUFFS=	104414	239#													
HRDCNT	000044R	204#													
HRDERS=	104405	239#	334	420	445	464	483	502	520	559	593	611	626	647	
		678													
HRDPAS	000050R	206#													
ICUNT	000036R	201#													
ICOUN	003236R	289#	290*	701	797#										
ICOUNT	000040R	202#													
IDNUM	000122R	231#													
INIT	000030R	198#													
INTFLG	003240R	430*	438	452*	457	471*	476	490*	495	508*	513	768*	798#		
INTR	000120R	230#													
INTRON=	000040	250#	434	441	453	460	472	479	491	498	509	516			
ITCNT	003242R	289	799#												
MAP22S=	104416	239#													
MODNAM	000000R	185#													
MODSP	000224R	199	237#												
MSGNS =	104403	239#													
MSGSS =	104402	239#													
MSG\$ =	104401	239#													
NPRBUF	003244R	800#	804												
NPRLOC	003246R	531	534	551	554	563	589	652	672	674	804#				

[illegible][illegible]

[illegible]

. ABS.	000000	000
	003250	001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

```

XDTADO.OBJ,XDTADO.LST/CRF/SOL/NL:TOC=DDXCOM.P11,XDTADO.P11
RUN-TIME: 4 7 1 SECONDS
RUN-TIME RATIO: 40/13=2.9
CORE USED: 7K (13 PAGES)

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DOCUMENT PAGES: 21

